

OpenCL (Open Computing Language) is a multi-vendor open standard for general-purpose parallel programming of heterogeneous systems that include CPUs, GPUs, and other processors. OpenCL provides a uniform programming environment for software developers to write efficient, portable code for high-performance compute servers, desktop computer systems, and handheld devices.

[n.n.n] refers to the section in the OpenCL Specification.

[n.n.n] refers to the section in the OpenCL Extension Specification

Text shown in purple is as per the OpenCL Extension Specification.

Specifications are available at www.khronos.org/opencl.

The OpenCL Platform Layer

The OpenCL platform layer implements platform-specific features that allow applications to query OpenCL devices, device configuration information, and to create OpenCL contexts using one or more devices.

Querying Platform Info and Devices [4.1, 4.2]

```
cl_int clGetPlatformIDs (cl_uint num_entries,
    cl_platform_id *platforms, cl_uint *num_platforms)

cl_int clGetPlatformInfo (cl_platform_id platform,
    cl_platform_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
param_name: CL_PLATFORM_{PROFILE, VERSION},
    CL_PLATFORM_{NAME, VENDOR, EXTENSIONS}

cl_int clGetDeviceIDs (cl_platform_id platform,
    cl_device_type device_type, cl_uint num_entries,
    cl_device_id *devices, cl_uint *num_devices)
device_type: CL_DEVICE_TYPE_{ACCELERATOR, ALL, CPU},
    CL_DEVICE_TYPE_{CUSTOM, DEFAULT, GPU}

cl_int clGetDeviceInfo (cl_device_id device,
    cl_device_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
param_name:
    CL_DEVICE_{NAME, VENDOR, PROFILE, TYPE},
    CL_DEVICE_NATIVE_VECTOR_WIDTH_{CHAR, INT},
    CL_DEVICE_NATIVE_VECTOR_WIDTH_{LONG, SHORT},
    CL_DEVICE_NATIVE_VECTOR_WIDTH_{DOUBLE, HALF},
    CL_DEVICE_NATIVE_VECTOR_WIDTH_FLOAT,
    CL_DEVICE_PREFERRED_VECTOR_WIDTH_{CHAR, INT},
    CL_DEVICE_PREFERRED_VECTOR_WIDTH_{LONG, SHORT},
    CL_DEVICE_PREFERRED_VECTOR_WIDTH_{DOUBLE, HALF},
    CL_DEVICE_PREFERRED_VECTOR_WIDTH_FLOAT,
    CL_DEVICE_PREFERRED_INTEROP_USER_SYNC,
    CL_DEVICE_ADDRESS_BITS, CL_DEVICE_AVAILABLE,
    CL_DEVICE_BUILT_IN_KERNELS,
    CL_DEVICE_COMPILER_AVAILABLE,
    CL_DEVICE_{DOUBLE, HALF, SINGLE}_FP_CONFIG,
    CL_DEVICE_ENDIAN_LITTLE, CL_DEVICE_EXTENSIONS,
    CL_DEVICE_ERROR_CORRECTION_SUPPORT,
    CL_DEVICE_EXECUTION_CAPABILITIES,
    CL_DEVICE_GLOBAL_MEM_CACHE_{SIZE, TYPE},
    CL_DEVICE_GLOBAL_MEM_{CACHELINE_SIZE, SIZE},
    CL_DEVICE_HOST_UNIFIED_MEMORY,
    CL_DEVICE_IMAGE_MAX_{ARRAY, BUFFER}_SIZE,
    CL_DEVICE_IMAGE_SUPPORT,
    CL_DEVICE_IMAGE2D_MAX_{WIDTH, HEIGHT},
    CL_DEVICE_IMAGE3D_MAX_{WIDTH, HEIGHT, DEPTH},
    CL_DEVICE_LOCAL_MEM_{TYPE, SIZE},
    CL_DEVICE_MAX_{READ, WRITE}_IMAGE_ARGS,
    CL_DEVICE_MAX_CLOCK_FREQUENCY,
    CL_DEVICE_MAX_COMPUTE_UNITS,
    CL_DEVICE_MAX_CONSTANT_ARGS_BUFFER_SIZE,
    CL_DEVICE_MAX_{MEM_ALLOC, PARAMETER}_SIZE,
    CL_DEVICE_MAX_SAMPLERS,
    CL_DEVICE_MAX_WORK_GROUP_SIZE,
    CL_DEVICE_MAX_WORK_ITEM_{DIMENSIONS, SIZES},
    CL_DEVICE_MEM_BASE_ADDR_ALIGN,
    CL_DEVICE_OPENCL_C_VERSION, CL_DEVICE_PARENT_DEVICE,
    CL_DEVICE_PARTITION_AFFINITY_DOMAIN,
    CL_DEVICE_PARTITION_MAX_SUB_DEVICES,
    CL_DEVICE_PARTITION_{PROPERTIES, TYPE},
    CL_DEVICE_PLATFORM, CL_DEVICE_PRINTF_BUFFER_SIZE,
    CL_DEVICE_PROFILING_TIMER_RESOLUTION,
    CL_DEVICE_QUEUE_PROPERTIES,
    CL_DEVICE_REFERENCE_COUNT,
    CL_DEVICE_VENDOR_ID, CL_{DEVICE, DRIVER}_VERSION
```

Partitioning a Device [4.3]

```
cl_int clCreateSubDevices (cl_device_id in_device,
    const cl_device_partition_property *properties,
    cl_uint num_devices, cl_device_id *out_devices,
    cl_uint *num_devices_ret)
properties: CL_DEVICE_PARTITION_EQUIALLY,
    CL_DEVICE_PARTITION_BY_{COUNTS, AFFINITY_DOMAIN}
(Affinity domain may be:
    CL_DEVICE_AFFINITY_DOMAIN_NUMA,
    CL_DEVICE_AFFINITY_DOMAIN_{L4, L3, L2, L1}_CACHE,
    CL_DEVICE_AFFINITY_DOMAIN_NEXT_PARTITIONABLE)

cl_int clRetainDevice (cl_device_id device)
```

The OpenCL Runtime

Command Queues [5.1]

```
cl_command_queue clCreateCommandQueue (cl_context context, cl_device_id device,
    cl_command_queue_properties properties,
    cl_int *errcode_ret)
properties: CL_QUEUE_PROFILING_ENABLE,
    CL_QUEUE_OUT_OF_ORDER_EXEC_MODE_ENABLE

cl_int clRetainCommandQueue (cl_command_queue command_queue)
```

```
cl_int clReleaseCommandQueue (cl_command_queue command_queue)
```

```
cl_int clGetCommandQueueInfo (cl_command_queue command_queue,
    cl_command_queue_info param_name,
    size_t param_value_size,
    void *param_value,
    size_t *param_value_size_ret)
param_name: CL_QUEUE_CONTEXT,
    CL_QUEUE_DEVICE,
    CL_QUEUE_REFERENCE_COUNT,
    CL_QUEUE_PROPERTIES
```

Buffer Objects

Elements of a buffer object are stored sequentially and accessed using a pointer by a kernel executing on a device. Data is stored in the same format as it is accessed by the kernel.

Create Buffer Objects [5.2.1]

```
cl_mem clCreateBuffer (cl_context context,
    cl_mem_flags flags, size_t size, void *host_ptr,
    cl_int *errcode_ret)
flags: CL_MEM_READ_WRITE,
    CL_MEM_{WRITE, READ}_ONLY,
    CL_MEM_HOST_NO_ACCESS,
    CL_MEM_HOST_{READ, WRITE}_ONLY,
    CL_MEM_{USE, ALLOC, COPY}_HOST_PTR

cl_mem clCreateSubBuffer (cl_mem buffer,
    cl_mem_flags flags,
    cl_buffer_create_type buffer_create_type,
    const void *buffer_create_info, cl_int *errcode_ret)
flags: same as for clCreateBuffer
buffer_create_type: CL_BUFFER_CREATE_TYPE_REGION
```

Read, Write, Copy Buffer Objects [5.2.2]

```
cl_int clEnqueueReadBuffer (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_read, size_t offset, size_t size,
    void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueReadBufferRect (
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_read, const size_t *buffer_origin,
    const size_t *host_origin, const size_t *region,
    size_t buffer_row_pitch, size_t buffer_slice_pitch,
    size_t host_row_pitch, size_t host_slice_pitch,
    void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clReleaseDevice (cl_device_id device)
```

Contexts [4.4]

```
cl_context clCreateContext (const cl_context_properties *properties,
    cl_uint num_devices, const cl_device_id *devices,
    void (CL_CALLBACK *pfn_notify) (const char *errinfo, const void *private_info,
        size_t cb, void *user_data),
    void *user_data, cl_int *errcode_ret)
properties: NULL or CL_CONTEXT_PLATFORM,
    CL_CONTEXT_INTEROP_USER_SYNC,
    CL_CONTEXT_{D3D10, D3D11}_DEVICE_KHR,
    CL_CONTEXT_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR,
    CL_GL_CONTEXT_KHR, CL_CGL_SHAREGROUP_KHR,
    CL_{EGL, GLX}_DISPLAY_KHR, CL_WGL_HDC_KHR
```

```
cl_context clCreateContextFromType (const cl_context_properties *properties,
    cl_device_type device_type,
    void (CL_CALLBACK *pfn_notify) (const char *errinfo, const void *private_info,
        size_t cb, void *user_data),
    void *user_data, cl_int *errcode_ret)
properties: See clCreateContext
```

```
cl_int clRetainContext (cl_context context)
```

```
cl_int clReleaseContext (cl_context context)
```

```
cl_int clGetContextInfo (cl_context context,
    cl_context_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
param_name: CL_CONTEXT_REFERENCE_COUNT,
    CL_CONTEXT_{DEVICES, NUM_DEVICES, PROPERTIES},
    CL_CONTEXT_D3D10_PREFER_SHARED_RESOURCES_KHR,
    CL_CONTEXT_D3D11_PREFER_SHARED_RESOURCES_KHR
```

Get CL Extension Function Pointers [9.2]

```
void* clGetExtensionFunctionAddressForPlatform (cl_platform_id platform, const char *funcname)
```

cl_int clEnqueueWriteBuffer (

```
    cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_write, size_t offset, size_t size,
    const void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

cl_int clEnqueueWriteBufferRect (

```
    cl_command_queue command_queue,
    cl_mem buffer, cl_bool blocking_write,
    const size_t *buffer_origin, const size_t *host_origin,
    const size_t *region, size_t buffer_row_pitch,
    size_t buffer_slice_pitch, size_t host_row_pitch,
    size_t host_slice_pitch, const void *ptr,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

cl_int clEnqueueFillBuffer (

```
    cl_command_queue command_queue,
    cl_mem buffer, const void *pattern,
    size_t pattern_size, size_t offset, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

cl_int clEnqueueCopyBuffer (

```
    cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_buffer,
    size_t src_offset, size_t dst_offset, size_t size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

cl_int clEnqueueCopyBufferRect (

```
    cl_command_queue command_queue,
    cl_mem src_buffer, cl_mem dst_buffer,
    const size_t *src_origin, const size_t *dst_origin,
    const size_t *region, size_t src_row_pitch,
    size_t src_slice_pitch, size_t dst_row_pitch,
    size_t dst_slice_pitch, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Map Buffer Objects [5.2.3]

```
void *clEnqueueMapBuffer (cl_command_queue command_queue, cl_mem buffer,
    cl_bool blocking_map, cl_map_flags map_flags,
    size_t offset, size_t size, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event,
    cl_int *errcode_ret)
map_flags: CL_MAP_{READ, WRITE},
    CL_MAP_WRITE_INVALIDATE_REGION
```

Memory Objects [5.4.1, 5.4.2]

```
cl_int clRetainMemObject (cl_mem memobj)
```

```
cl_int clReleaseMemObject (cl_mem memobj)
```

```
cl_int clSetMemObjectDestructorCallback (cl_mem memobj, void (CL_CALLBACK *pfn_notify) (cl_mem memobj, void *user_data),
    void *user_data)
```

```
cl_int clEnqueueUnmapMemObject (cl_command_queue command_queue,
    cl_mem memobj, void *mapped_ptr,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Migrate Memory Objects [5.4.4]

```
cl_int clEnqueueMigrateMemObjects (cl_command_queue command_queue,
    cl_uint num_mem_objects,
    const cl_mem *mem_objects,
    cl_mem_migration_flags flags,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
flags: CL_MIGRATE_MEM_OBJECT_HOST,
    CL_MIGRATE_MEM_OBJECT_CONTENT_UNDEFINED
```

Query Memory Object [5.4.5]

```
cl_int clGetMemObjectInfo (cl_mem memobj,
    cl_mem_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
```

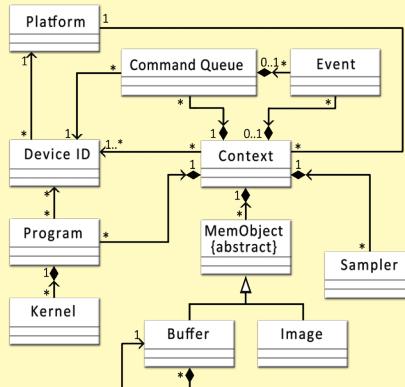
```
param_name: CL_MEM_{TYPE, FLAGS, SIZE, HOST_PTR},
    CL_MEM_{MAP, REFERENCE}_COUNT, CL_MEM_OFFSET,
    CL_MEM_CONTEXT, CL_MEM_ASSOCIATED_MEMOBJECT,
    CL_MEM_{D3D10, D3D11}_RESOURCE_KHR,
    CL_MEM_DX9_MEDIA_ADAPTER_TYPE_KHR,
    CL_MEM_DX9_MEDIA_SURFACE_INFO_KHR
```

OpenCL Class Diagram [2.1]

The figure below describes the OpenCL specification as a class diagram using the Unified Modeling Language¹ (UML) notation. The diagram shows both nodes and edges which are classes and their relationships. As a simplification it shows only classes, and no attributes or operations.

Annotations

Relationships	
abstract classes	{abstract}
aggregations	◆
inheritance	Δ
relationship navigability	^
Cardinality	
many	*
one and only one	1
optionally one	0..1
one or more	1..*



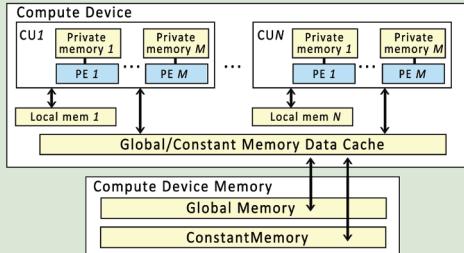
¹Unified Modeling Language (<http://www.uml.org/>) is a trademark of Object Management Group (OMG).

OpenCL Device Architecture Diagram [3.3]

The table below shows memory regions with allocation and memory access capabilities.

	Global	Constant	Local	Private
Host	Dynamic allocation Read/Write access	Dynamic allocation Read/Write access	Dynamic allocation No access	No allocation No access
Kernel	No allocation Read/Write access	Static allocation Read-only access	Static allocation Read/Write access	Static allocation Read/Write access

This conceptual OpenCL device architecture diagram shows processing elements (PE), compute units (CU), and devices. The host is not shown.



Program Objects

Create Program Objects [5.6.1]

```

cl_program clCreateProgramWithSource (
    cl_context context, cl_uint count, const char **strings,
    const size_t *lengths, cl_int *errcode_ret)

cl_program clCreateProgramWithBinary (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list, const size_t *lengths,
    const unsigned char **binaries, cl_int *binary_status,
    cl_int *errcode_ret)

cl_program clCreateProgramWithBuiltInKernels (
    cl_context context, cl_uint num_devices,
    const cl_device_id *device_list,
    const char *kernel_names, cl_int *errcode_ret)

cl_int clRetainProgram (cl_program program)
cl_int clReleaseProgram (cl_program program)
  
```

Building Program Executables [5.6.2]

```

cl_int clBuildProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, void (CL_CALLBACK*pfn_notify)
        (cl_program program, void *user_data),
    void *user_data)
  
```

Separate Compilation and Linking [5.6.3]

```

cl_int clCompileProgram (cl_program program,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, cl_uint num_input_headers,
    const cl_program *input_headers,
    const char **header_include_names,
    void (CL_CALLBACK*pfn_notify)
        (cl_program program, void *user_data),
    void *user_data)
  
```

Kernel and Event Objects

Create Kernel Objects [5.7.1]

```

cl_kernel clCreateKernel (cl_program program,
    const char *kernel_name, cl_int *errcode_ret)

cl_int clCreateKernelsInProgram (cl_program program,
    cl_uint num_kernels, cl_kernel *kernels,
    cl_int *num_kernels_ret)
  
```

```

cl_int clRetainKernel (cl_kernel kernel)
cl_int clReleaseKernel (cl_kernel kernel)
  
```

Kernel Arguments and Queries [5.7.2, 5.7.3]

```

cl_int clSetKernelArg (cl_kernel kernel, cl_uint arg_index,
    size_t arg_size, const void *arg_value)

cl_int clGetKernelInfo (cl_kernel kernel,
    cl_kernel_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
  
```

```

param_name: CL_KERNEL_FUNCTION_NAME,
CL_KERNEL_NUM_ARGS, CL_KERNEL_REFERENCE_COUNT,
CL_KERNEL_ATTRIBUTES, CONTEXT, PROGRAM)
  
```

```

cl_int clGetKernelWorkGroupInfo (
    cl_kernel kernel, cl_device_id device,
    cl_kernel_work_group_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
  
```

```

param_name: CL_KERNEL_GLOBAL_WORK_SIZE,
CL_KERNEL_COMPILE_JWORK_GROUP_SIZE,
CL_KERNEL_LOCAL_PRIVATE_MEM_SIZE,
CL_KERNEL_PREFERRED_WORK_GROUP_SIZE_MULTIPLE
  
```

```

cl_program clLinkProgram (cl_context context,
    cl_uint num_devices, const cl_device_id *device_list,
    const char *options, cl_uint num_input_programs,
    const cl_program *input_programs,
    void (CL_CALLBACK*pfn_notify)
        (cl_program program, void *user_data),
    void *user_data, cl_int *errcode_ret)
  
```

Unload the OpenCL Compiler [5.6.6]

```

cl_int clUnloadPlatformCompiler (
    cl_platform_id platform)
  
```

Query Program Objects [5.6.7]

```

cl_int clGetProgramInfo (cl_program program,
    cl_program_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
  
```

param_name: CL_PROGRAM_REFERENCE_COUNT,
CL_PROGRAM_CONTEXT, NUM_DEVICES, DEVICES,
CL_PROGRAM_SOURCE, BINARY_SIZES, BINARIES,
CL_PROGRAM_NUM_KERNELS, KERNEL_NAMES

```

cl_int clGetProgramBuildInfo (
    cl_program program, cl_device_id device,
    cl_program_build_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
  
```

param_name: CL_PROGRAM_BINARY_TYPE,
CL_PROGRAM_BUILD_STATUS, OPTIONS, LOG)

Compiler Options [5.6.4]

Preprocessor: (-D processed in order listed in
clBuildProgram or clCompileProgram)

```

-D name           -D name=definition      -I dir
  
```

Math intrinsics:

```

-cl-single-precision-constant      -cl-denorms-are-zero
-cl-fp32-correctly-rounded-divide-sqrt
  
```

```

cl_int clGetKernelArgInfo (cl_kernel kernel,
    cl_uint arg_idx, cl_kernel_arg_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
  
```

param_name:

CL_KERNEL_ARG_ACCESS, ADDRESS, TYPE) _QUALIFIER,
CL_KERNEL_ARG_NAME, CL_KERNEL_ARG_TYPE_NAME

Execute Kernels [5.8]

```

cl_int clEnqueueNDRangeKernel (
    cl_command_queue command_queue,
    cl_kernel kernel, cl_uint work_dim,
    const size_t *global_work_offset,
    const size_t *global_work_size,
    const size_t *local_work_size,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
  
```

```

cl_int clEnqueueTask (
    cl_command_queue command_queue,
    cl_kernel kernel, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
  
```

```

cl_int clEnqueueNativeKernel (cl_command_queue
    command_queue, void (*user_func)(void *),
    void *args, size_t cb_args, cl_uint num_mem_objects,
    const cl_mem *mem_list, const void **args_mem_loc,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
  
```

Event Objects [5.9]

```

cl_event clCreateUserEvent (cl_context context,
    cl_int *errcode_ret)
  
```

Optimization options:

-cl-opt-disable	-cl-mad-enable
-cl-no-signed-zeros	-cl-finite-math-only
-cl-unsafe-math-optimizations	-cl-fast-relaxed-math

Warning request/suppress:

-w	-Werror
----	---------

Control OpenCL C language version:

-cl-std=CL1.1	// OpenCL 1.1 specification.
-cl-std=CL1.2	// OpenCL 1.2 specification.

Query kernel argument information:

-cl-kernel-arg-info

Linker Options [5.6.5]

Library linking options:

-create-library
-enable-link-options

-cl-denorms-are-zero
-cl-no-signed-zeros
-cl-unsafe-math-optimizations
-cl-finite-math-only
-cl-fast-relaxed-math

```

cl_int clSetUserEventStatus (cl_event event,
    cl_int execution_status)
  
```

```

cl_int clWaitForEvents (cl_uint num_events,
    const cl_event *event_list)
  
```

```

cl_int clGetEventInfo (cl_event event,
    cl_event_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
  
```

param_name: CL_EVENT_COMMAND_(QUEUE, TYPE),
CL_EVENT_COMMAND_REFERENCE_COUNT,
CL_EVENT_COMMAND_EXECUTION_STATUS

```

cl_int clSetEventCallback (cl_event event,
    cl_int command_exec_callback_type,
    void (CL_CALLBACK*pfn_event_notify)
        (cl_event event, cl_int event_command_exec_status,
        void *user_data),
    void *user_data)
  
```

```

cl_int clRetainEvent (cl_event event)
  
```

```

cl_int clReleaseEvent (cl_event event)
  
```

Markers, Barriers, and Waiting for Events [5.10]

```

cl_int clEnqueueMarkerWithWaitList (
    cl_command_queue command_queue,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
  
```

```

cl_int clEnqueueBarrierWithWaitList (
    cl_command_queue command_queue,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
  
```

Profiling Operations [5.12]

```

cl_int clGetEventProfilingInfo (cl_event event,
    cl_profiling_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
  
```

param_name: CL_PROFILING_COMMAND_QUEUED,
CL_PROFILING_COMMAND_SUBMIT, END)

Flush and Finish [5.13]

```

cl_int clFlush (cl_command_queue command_queue)
  
```

```

cl_int clFinish (cl_command_queue command_queue)
  
```

Supported Data Types

The optional double scalar and vector types are supported if CL_DEVICE_DOUBLE_FP_CONFIG is not zero.

Built-in Scalar Data Types [6.1.1]

OpenCL Type	API Type	Description
bool	--	true (1) or false (0)
char	cl_char	8-bit signed
unsigned char, uchar	cl_uchar	8-bit unsigned
short	cl_short	16-bit signed
unsigned short, ushort	cl_ushort	16-bit unsigned
int	cl_int	32-bit signed
unsigned int, uint	cl_uint	32-bit unsigned
long	cl_long	64-bit signed
unsigned long, ulong	cl_ulong	64-bit unsigned
float	cl_float	32-bit float
double	cl_double	64-bit IEEE 754
half	cl_half	16-bit float (storage only)
size_t	--	32- or 64-bit unsigned integer
ptrdiff_t	--	32- or 64-bit signed integer
intptr_t	--	32- or 64-bit signed integer
uintptr_t	--	32- or 64-bit unsigned integer
void	void	void

Built-in Vector Data Types [6.1.2]

OpenCL Type	API Type	Description
charn	cl_charn	8-bit signed
ucharn	cl_ucharn	8-bit unsigned
shortn	cl_shortn	16-bit signed
ushortn	cl_ushortn	16-bit unsigned
intn	cl_intn	32-bit signed
uintn	cl_uintn	32-bit unsigned
longn	cl_longn	64-bit signed
ulongn	cl_ulongn	64-bit unsigned
floatn	cl_floatn	32-bit float
doublen	cl_doublen	64-bit float

Other Built-in Data Types [6.1.3]

The optional types listed here other than event_t are only defined if CL_DEVICE_IMAGE_SUPPORT is CL_TRUE.

OpenCL Type	Description
image2d_t	OPTIONAL 2D image handle
image3d_t	OPTIONAL 3D image handle
image2d_array_t	OPTIONAL 2D image array
image1d_t	OPTIONAL 1D image handle
image1d_buffer_t	OPTIONAL 1D image buffer
image1d_array_t	OPTIONAL 1D image array
sampler_t	OPTIONAL sampler handle
event_t	event handle

Reserved Data Types [6.1.4]

OpenCL Type	Description
booln	boolean vector
halfn	16-bit, vector
quad, quadn	128-bit float, vector
complex half, complex halfn	16-bit complex, vector
imaginary half, imaginary halfn	16-bit complex, vector
complex float, complex floatn	32-bit complex, vector
imaginary float, imaginary floatn	32-bit complex, vector
complex double, complex doublen	64-bit complex, vector
imaginary double, imaginary doublen	64-bit complex, vector
complex quad, complex quadn	128-bit complex, vector
imaginary quad, imaginary quadn	128-bit complex, vector
floatnxm	$n \times m$ matrix of 32-bit floats
doublenxm	$n \times m$ matrix of 64-bit floats

Preprocessor Directives & Macros [6.10]

```
#pragma OPENCL FP_CONTRACT on-off-switch
on-off-switch: ON, OFF, DEFAULT
```

```
#pragma OPENCL EXTENSION extensionname : behavior
```

```
#pragma OPENCL EXTENSION all : behavior
```

FILE	Current source file
--------	---------------------

func	Current function name
--------	-----------------------

Vector Component Addressing [6.1.7]

Vector Components

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
float2 v;	v.x, v.s0	v.y, v.s1														
float3 v;	v.x, v.s0	v.y, v.s1	v.z, v.s2													
float4 v;	v.x, v.s0	v.y, v.s1	v.z, v.s2	v.w, v.s3												
float8 v;	v.s0	v.s1	v.s2	v.s3	v.s4	v.s5	v.s6	v.s7								
float16 v;	v.s0	v.s1	v.s2	v.s3	v.s4	v.s5	v.s6	v.s7	v.s8	v.s9	v.sa, v.sA	v.sb, v.sB	v.sc, v.sC	v.sd, v.sD	v.se, v.sE	v.sf, v.sF

Vector Addressing Equivalences

Numeric indices are preceded by the letter s or S, e.g.: s1. Swizzling, duplication, and nesting are allowed, e.g.: v.yx, vxx, v.lo.x

	v.lo	v.hi	v.odd	v.even		v.lo	v.hi	v.odd	v.even
float2	v.x, v.s0	v.y, v.s1			float8	v.s0123	v.s4567	v.s1357	v.s0246
float3*	v.s01, v.xy	v.s23, v.zw	v.s13, v.yw	v.s02, v.xz	float16	v.s01234567	v.s89abcdef	v.s13579bdf	v.s02468ace
float4	v.s01, v.xy	v.s23, v.zw	v.s13, v.yw	v.s02, v.xz					

*When using .lo or .hi with a 3-component vector, the .w component is undefined.

Operators and Qualifiers

Operators [6.3]

These operators behave similarly as in C99 except that operands may include vector types when possible:

+ - * % / -- ++ == != &	&
~ ^ > < >= <= ! &&	
? : >> << = , op= sizeof	

Address Space Qualifiers [6.5]

global, global	local, local
constant, constant	private, private

Function Qualifiers [6.7]

kernel, kernel	
attribute__((vec_type_hint(type))) //type defaults to int	
attribute__((work_group_size_hint(X, Y, Z)))	
attribute__((reqd_work_group_size(X, Y, Z)))	

Specify Type Attributes [6.11.1]

Use to specify special attributes of enum, struct and union types.

attribute((aligned(n)))	attribute((endianness(host)))
attribute((aligned))	attribute((endianness(device)))
attribute((packed))	attribute((endianness))

Math Constants [6.12.2] [9.5.2]

The values of the following symbolic constants are type float, accurate within the precision of a single precision floating-point number.

INFINITY	Constant float expression, positive or unsigned infinity.	M LN2_F	Value of $\log_e 2$
NAN	Constant float expression, quiet NaN.	M LN10_F	Value of $\log_e 10$
		M PI_F	Value of π
		M PI_2_F	Value of $\pi / 2$
		M PI_4_F	Value of $\pi / 4$
		M_1_PI_F	Value of $1 / \pi$
		M_2_PI_F	Value of $2 / \pi$
		M_2_SQRTPI_F	Value of $2 / \sqrt{\pi}$
		M_SQRT2_F	Value of $\sqrt{2}$
		M_SQRT1_2_F	Value of $1 / \sqrt{2}$

Integer Built-in Functions [6.12.3]

T is type char, charn, uchar, ucharn, short, shortn, ushort, ushortn, int, intn, uint, uintn, long, longn, ulong, or ulongn, where n is 2, 3, 4, 8, or 16. Ts is the unsigned version of T. Tsc is the scalar version of T.

Tu abs (Tx)	x
Tu abs_diff (Tx, Ty)	x - y without modulo overflow
T add_sat (Tx, Ty)	x + y and saturates the result
T hadd (Tx, Ty)	(x + y) >> 1 without mod. overflow
T rhadd (Tx, Ty)	(x + y + 1) >> 1
T clamp (Tx, T min, T max)	min(max(x, minval), maxval)
T clamp (Tx, Tsc min, Tsc max)	min(max(x, minval), maxval)
T clz (Tx)	number of leading 0-bits in x
T mad_hi (Ta, Tb, Tc)	mul_hi(a, b) + c
T mad_sat (Ta, Tb, Tc)	a * b + c and saturates the result
T max (Tx, Ty)	y if x < y, otherwise it returns x
T max (Tx, Tsc y)	y if y < x, otherwise it returns x
T min (Tx, Ty)	y if y < x, otherwise it returns x
T min (Tx, Tsc y)	y if y < x, otherwise it returns x
T mul_hi (Tx, Ty)	high half of the product of x and y
T rotate (Tv, Ti)	result[indx] = v[indx] << i

x - y and saturates the result

Number of non-zero bits in x

For upsample, return type is scalar when the parameters are scalar.

short[n] upsample (char[n] hi, uchar[n] lo)	result[i] = ((short)hi[i]<<8) lo[i]
ushort[n] upsample (uchar[n] hi, uchar[n] lo)	result[i] = ((ushort)hi[i]<<8) lo[i]
int[n] upsample (short[n] hi, ushort[n] lo)	result[i] = ((int)hi[i]<<16) lo[i]
uint[n] upsample (ushort[n] hi, ushort[n] lo)	result[i] = ((uint)hi[i]<<16) lo[i]
long[n] upsample (int[n] hi, uint[n] lo)	result[i] = ((long)hi[i]<<32) lo[i]
ulong[n] upsample (uint[n] hi, uint[n] lo)	result[i] = ((ulong)hi[i]<<32) lo[i]

The following fast integer functions optimize the performance of kernels. In these functions, T is type int, uint, intn or intn, where n is 2, 3, 4, 8, or 16.

T mad24 (Tx, Ty, Tz)	Multiply 24-bit integer values x, y, add 32-bit int. result to 32-bit int. z
T mul24 (Tx, Ty)	Multiply 24-bit integer values x and y

__LINE__	Integer line number	__FAST_RELAXED_MATH__	1 if -cl-fast-relaxed-math optimization option is specified
__OPENCL_VERSION__	Integer version number, e.g: 120	FP_FAST_FMA	Defined if double fma is fast
CL_VERSION_1_0	Substitutes integer 100 for 1.0	FP_FAST_FMAF	Defined if float fma is fast
CL_VERSION_1_1	Substitutes integer 110 for 1.1	FP_FAST_FMA_HALF	Defined if half fma is fast
CL_VERSION_1_2	Substitutes integer 120 for 1.2	kernel_exec(X, typen)	Same as:
__OPENCL_C_VERSION__	Sub. integer for OpenCL C version.	kernel_attribute__(work_group_size_hint(X, 1, 1))	
__ENDIAN_LITTLE__	1 if device is little endian	kernel_attribute__(vec_type_hint(typen))	
__IMAGE_SUPPORT__	1 if images are supported		

Math Built-in Functions [6.12.2] [9.5.2]

Ts is type float, optionally double, or half if the `half` extension is enabled. *Tn* is the vector form of *Ts*, where *n* is 2, 3, 4, 8, or 16. *T* is *Ts* and *Tn*. *Q* is qualifier `_global`, `_local`, or `_private`. *HN* indicates that half and native variants are available using only the float or float₂ types by prepending "half_" or "native_" to the function name. Prototypes shown in brown text are available in half_ and native_ forms only using the float or float₂ types.

<i>Tacos</i> (<i>T</i>)	Arc cosine
<i>Tacosh</i> (<i>T</i>)	Inverse hyperbolic cosine
<i>Tacospi</i> (<i>Tx</i>)	$\text{acos}(x) / \pi$
<i>Tasin</i> (<i>T</i>)	Arc sine
<i>Tasinh</i> (<i>T</i>)	Inverse hyperbolic sine
<i>Tasinpi</i> (<i>Tx</i>)	$\text{asin}(x) / \pi$
<i>Tatan</i> (<i>Ty_over_x</i>)	Arc tangent
<i>Tatan2</i> (<i>Ty</i> , <i>Tx</i>)	Arc tangent of <i>y</i> / <i>x</i>
<i>Tanh</i> (<i>T</i>)	Hyperbolic arc tangent
<i>Tatnpi</i> (<i>Tx</i>)	$\text{atan}(x) / \pi$
<i>Tatan2pi</i> (<i>Tx</i> , <i>Ty</i>)	$\text{atan}2(y, x) / \pi$
<i>Tcbt</i> (<i>T</i>)	Cube root
<i>Tceil</i> (<i>T</i>)	Round to integer toward + infinity
<i>Tcopysign</i> (<i>Tx</i> , <i>Ty</i>)	<i>x</i> with sign changed to sign of <i>y</i>
<i>Tcos</i> (<i>T</i>) HN	Cosine
<i>Tcosh</i> (<i>T</i>)	Hyperbolic cosine
<i>Tcospi</i> (<i>Tx</i>)	$\cos(\pi x)$
<i>Thalf_divide</i> (<i>Tx</i> , <i>Ty</i>)	<i>x</i> / <i>y</i>
<i>Tnative_divide</i> (<i>Tx</i> , <i>Ty</i>)	(<i>T</i> may only be float or float ₂)
<i>Terfc</i> (<i>T</i>)	Complementary error function
<i>Terf</i> (<i>T</i>)	Calculates error function of <i>T</i>
<i>Texp</i> (<i>Tx</i>) HN	Exponential base e
<i>Texp2</i> (<i>T</i>) HN	Exponential base 2

<i>Texp10</i> (<i>T</i>)	HN	Exponential base 10
<i>Texpm1</i> (<i>Tx</i>)		$e^x - 1.0$
<i>Tfabs</i> (<i>T</i>)		Absolute value
<i>Tfdim</i> (<i>Tx</i> , <i>Ty</i>)		Positive difference between <i>x</i> and <i>y</i>
<i>Tfloor</i> (<i>T</i>)		Round to integer toward -infinity
<i>Tfma</i> (<i>Ta</i> , <i>Tb</i> , <i>Tc</i>)		Multiply and add, then round
<i>Tfmax</i> (<i>Tx</i> , <i>Ty</i>)		Return <i>y</i> if <i>x</i> < <i>y</i> , otherwise it returns <i>x</i>
<i>Tfmin</i> (<i>Tx</i> , <i>Ty</i>)		Return <i>y</i> if <i>y</i> < <i>x</i> , otherwise it returns <i>x</i>
<i>Tfmod</i> (<i>Tx</i> , <i>Ty</i>)		Modulus. Returns <i>x</i> - <i>y</i> * trunc(<i>x/y</i>)
<i>Tfract</i> (<i>Tx</i> , <i>QT</i> * <i>iptr</i>)		Fractional value in <i>x</i>
<i>Tfrexp</i> (<i>Tx</i> , <i>Qint</i> * <i>exp</i>)		Extract mantissa and exponent
<i>Tfrexp</i> (<i>Tx</i> , <i>Qintn</i> * <i>exp</i>)		
<i>Thypot</i> (<i>Tx</i> , <i>Ty</i>)		Square root of $x^2 + y^2$
<i>int[n]</i> <i>ilogb</i> (<i>Tx</i>)		Return exponent as an integer value
<i>Tldepx</i> (<i>Tx</i> , <i>int n</i>)		
<i>Tldepx</i> (<i>Tx</i> , <i>intn n</i>)		$x * 2^n$
<i>Tlgamma</i> (<i>Tx</i>)		
<i>Tlgamma_r</i> (<i>Tx</i> , <i>Qint</i> * <i>signp</i>)		Log gamma function
<i>Tlgamma_r</i> (<i>Tx</i> , <i>Qintn</i> * <i>signp</i>)		
<i>Tlog</i> (<i>T</i>) HN		Natural logarithm
<i>Tlog2</i> (<i>T</i>) HN		Base 2 logarithm
<i>Tlog10</i> (<i>T</i>) HN		Base 10 logarithm
<i>Tlog1p</i> (<i>Tx</i>)		$\ln(1.0 + x)$
<i>Tlogb</i> (<i>Tx</i>)		Exponent of <i>x</i>
<i>Tmad</i> (<i>Ta</i> , <i>Tb</i> , <i>Tc</i>)		Approximates <i>a</i> * <i>b</i> + <i>c</i>
<i>Tmaxmag</i> (<i>Tx</i> , <i>Ty</i>)		Maximum magnitude of <i>x</i> and <i>y</i>
<i>Tminmag</i> (<i>Tx</i> , <i>Ty</i>)		Minimum magnitude of <i>x</i> and <i>y</i>
<i>Tmodf</i> (<i>Tx</i> , <i>QT</i> * <i>iptr</i>)		Decompose floating-point number
<i>float[n]</i> <i>nan</i> (<i>uint[n]</i> <i>nancode</i>)		Quiet NaN
<i>half[n]</i> <i>nan</i> (<i>ushort[n]</i> <i>nancode</i>)		(Return is scalar when <i>nancode</i> is scalar)
<i>double[n]</i> <i>nan</i> (<i>ulong[n]</i> <i>nancode</i>)		
<i>Tnextafter</i> (<i>Tx</i> , <i>Ty</i>)		Next representable floating-point value after <i>x</i> in the direction of <i>y</i>
<i>Tpow</i> (<i>Tx</i> , <i>Ty</i>)		Compute <i>x</i> to the power of <i>y</i>
<i>Tpown</i> (<i>Tx</i> , <i>int y</i>)		<i>Compute</i> x^y , where <i>y</i> is an integer
<i>Tpown</i> (<i>Tx</i> , <i>intn y</i>)		
<i>Tpowr</i> (<i>Tx</i> , <i>Ty</i>) HN		Compute x^y , where <i>x</i> is ≥ 0
<i>Thalf_recip</i> (<i>Tx</i>)		$1/x$
<i>Tnative_recip</i> (<i>Tx</i>)		(<i>T</i> may only be float or float ₂)
<i>Tremainder</i> (<i>Tx</i> , <i>Ty</i>)		Floating point remainder
<i>Tremquo</i> (<i>Tx</i> , <i>Ty</i> , <i>Qint quo</i>)		Remainder and quotient
<i>Tremquo</i> (<i>Tx</i> , <i>Ty</i> , <i>Qintn quo</i>)		
<i>Trint</i> (<i>T</i>)		Round to nearest even integer
<i>Trootn</i> (<i>Tx</i> , <i>int y</i>)		<i>Compute</i> <i>x</i> to the power of $1/y$
<i>Trootn</i> (<i>Tx</i> , <i>intn y</i>)		
<i>Tround</i> (<i>Tx</i>)		Integral value nearest to <i>x</i> rounding
<i>Trsqrt</i> (<i>T</i>) HN		Inverse square root
<i>Tsin</i> (<i>T</i>) HN		Sine
<i>Tsincos</i> (<i>Tx</i> , <i>QT</i> * <i>cosval</i>)		Sine and cosine of <i>x</i>
<i>Tsinh</i> (<i>T</i>)		Hyperbolic sine
<i>Tsinpi</i> (<i>Tx</i>)		$\sin(\pi x)$
<i>Tsqrt</i> (<i>T</i>) HN		Square root
<i>Ttan</i> (<i>T</i>) HN		Tangent
<i>Ttanh</i> (<i>T</i>)		Hyperbolic tangent
<i>Ttanpi</i> (<i>Tx</i>)		$\tan(\pi x)$
<i>Ttgamma</i> (<i>T</i>)		Gamma function
<i>Ttrunc</i> (<i>T</i>)		Round to integer toward zero

Geometric Built-in Functions [6.12.5] [9.5.4]

Ts is scalar type float, optionally double, or half if the `half` extension is enabled. *T* is *Ts* and the 2-, 3-, or 4-component vector forms of *Ts*.

<i>float[3,4]</i> <i>cross</i> (<i>float[3,4]</i> <i>p0</i> , <i>float[3,4]</i> <i>p1</i>)	Cross product
<i>double[3,4]</i> <i>cross</i> (<i>double[3,4]</i> <i>p0</i> , <i>double[3,4]</i> <i>p1</i>)	
<i>half[3,4]</i> <i>cross</i> (<i>half[3,4]</i> <i>p0</i> , <i>half[3,4]</i> <i>p1</i>)	
<i>Ts</i> <i>distance</i> (<i>Tp0</i> , <i>Tp1</i>)	Vector distance
<i>Ts</i> <i>dot</i> (<i>Tp0</i> , <i>Tp1</i>)	Dot product
<i>Ts</i> <i>length</i> (<i>Tp</i>)	Vector length
<i>T</i> <i>normalize</i> (<i>Tp</i>)	Normal vector length 1
<i>float</i> <i>fast_distance</i> (<i>float p0</i> , <i>float p1</i>)	Vector distance
<i>float</i> <i>fast_distance</i> (<i>floatn p0</i> , <i>floatn p1</i>)	
<i>float</i> <i>fast_length</i> (<i>float p</i>)	Vector length
<i>float</i> <i>fast_length</i> (<i>floatn p</i>)	
<i>float</i> <i>fast_normalize</i> (<i>float p</i>)	Normal vector length 1
<i>floatn</i> <i>fast_normalize</i> (<i>floatn p</i>)	

Vector Data Load/Store [6.12.7] [9.5.6]

T is type char, uchar, short, ushort, int, uint, long, ulong, or float, optionally double, or half if the `half` extension is enabled. *Tn* refers to the vector form of type *T*, where *n* is 2, 3, 4, 8, or 16. *Q* is an Address Space Qualifier listed in 6.5 unless otherwise noted. When red, *Q* cannot be `_constant`. *R* defaults to the current rounding mode, or is one of the Rounding Modes listed in 6.2.3.2.

<i>Tn</i> <i>vload</i> (<i>size_t offset</i> , <i>const QT</i> * <i>p</i>)	Read vector data from address (<i>p</i> + (<i>offset</i> * <i>n</i>))
<i>void</i> <i>vstore</i> (<i>Tn data</i> , <i>size_t offset</i> , <i>QT</i> * <i>p</i>)	Write vector data to address (<i>p</i> + (<i>offset</i> * <i>n</i>))
<i>float</i> <i>vload_half</i> (<i>size_t offset</i> , <i>const Qhalf</i> * <i>p</i>)	Read a half from address (<i>p</i> + <i>offset</i>)
<i>floatn</i> <i>vload_halfn</i> (<i>size_t offset</i> , <i>const Qhalf</i> * <i>p</i>)	Read a halfn from address (<i>p</i> + (<i>offset</i> * <i>n</i>))
<i>void</i> <i>vstore_half</i> (<i>float data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_half_R</i> (<i>float data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_half</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_half_R</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vload_halfn</i> (<i>size_t offset</i> , <i>const Qhalf</i> * <i>p</i>)	Read half vector data from (<i>p</i> + (<i>offset</i> * <i>n</i>)). For half3, read from (<i>p</i> + (<i>offset</i> * 4)).
<i>void</i> <i>vstore_halfn</i> (<i>float data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_halfn_R</i> (<i>float data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_halfn</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_halfn_R</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	

<i>void</i> <i>vstore_half_R</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	Write a half to address (<i>p</i> + <i>offset</i>)
<i>void</i> <i>vstore_halfn_R</i> (<i>float data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_halfn_R</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	
<i>void</i> <i>vstore_halfn_R</i> (<i>double data</i> , <i>size_t offset</i> , <i>Qhalf</i> * <i>p</i>)	

Async Copies and Prefetch Functions [6.12.10] [9.5.7]

T is type char, uchar, uchar, uchar, short, ushort, ushort, int, intn, uint, uintn, long, longn, ulong, ulongn, float, floatn, optionally double or doublen, or half or halfn if the `half` extension is enabled.

<i>event_t</i> <i>async_work_group_copy</i> (<i>_local T* dst</i> , <i>const _global T* src</i> , <i>size_t num_gentypes</i> , <i>event_t event</i>)	Copies <i>num_gentypes</i> <i>T</i> elements from <i>src</i> to <i>dst</i>
<i>event_t</i> <i>async_work_group_copy</i> (<i>_global T* dst</i> , <i>const _local T* src</i> , <i>size_t num_gentypes</i> , <i>event_t event</i>)	
<i>event_t</i> <i>async_work_group_strided_copy</i> (<i>_local T* dst</i> , <i>const _global T* src</i> , <i>size_t num_gentypes</i> , <i>size_t src_stride</i> , <i>event_t event</i>)	Copies <i>num_gentypes</i> <i>T</i> elements from <i>src</i> to <i>dst</i>
<i>event_t</i> <i>async_work_group_strided_copy</i> (<i>_global T* dst</i> , <i>const _local T* src</i> , <i>size_t num_gentypes</i> , <i>size_t dst_stride</i> , <i>event_t event</i>)	
<i>void</i> <i>wait_group_events</i> (<i>int num_events</i> , <i>event_t* event_list</i>)	Wait for events that identify the <i>async_work_group_copy</i> operations to complete
<i>void</i> <i>prefetch</i> (<i>const _global T* p</i> , <i>size_t num_gentypes</i>)	Prefetch <i>num_gentypes</i> * <i>sizeof(T)</i> bytes into the global cache

Work-Item Built-in Functions [6.12.1]

These functions query the number of dimensions, the global and local work size specified to *cEnqueueNDRangeKernel*, and the global and local identifier of each work-item when this kernel is executed on a device. *D* is the dimension index.

<i>uint</i> <i>get_work_dim</i> ()	Number of dimensions in use
<i>size_t</i> <i>get_global_size</i> (<i>uint D</i>)	Number of global work-items
<i>size_t</i> <i>get_global_id</i> (<i>uint D</i>)	Global work-item ID value
<i>size_t</i> <i>get_local_size</i> (<i>uint D</i>)	Number of local work-items
<i>size_t</i> <i>get_local_id</i> (<i>uint D</i>)	Local work-item ID
<i>size_t</i> <i>get_num_groups</i> (<i>uint D</i>)	Number of work-groups
<i>size_t</i> <i>get_group_id</i> (<i>uint D</i>)	Returns the work-group ID
<i>size_t</i> <i>get_global_offset</i> (<i>uint D</i>)	Returns global offset

Common Built-in Functions [6.12.4] [9.5.3]

These functions operate component-wise and use round to nearest even rounding mode. Ts is type float, optionally double, or half if the **half extension** is enabled. Tn is the vector form of Ts , where n is 2, 3, 4, 8, or 16. T is Ts and Tn .

$T \text{clamp}(Tx, T \min, T \max)$	Clamp x to range given by \min, \max
$T \text{degrees}(T \text{radians})$	radians to degrees
$T \text{max}(Tx, Ty)$	Max of x and y

$T \min(Tx, Ty)$	Min of x and y
$T \text{min}(Tx, Ty)$	
$T \text{mix}(Tx, Ty, T \alpha)$	Linear blend of x and y
$T \text{mix}(Tx, Ty, Ts \alpha)$	
$T \text{radians}(T \text{degrees})$	degrees to radians
$T \text{step}(T \text{edge}, Tx)$	0.0 if $x < \text{edge}$, else 1.0
$T \text{step}(Ts \text{edge}, Tn x)$	
$T \text{smoothstep}(T \text{edge}0, T \text{edge}1, Tx)$	Step and interpolate
$T \text{smoothstep}(Ts \text{edge}0, Ts \text{edge}1, Tn x)$	
$T \text{sign}(Tx)$	Sign of x

Relational Built-in Functions [6.12.6]

These functions can be used with built-in scalar or vector types as arguments and return a scalar or vector integer result. T is type float, floatn, char, uchar, ucharn, short, shortn, ushort, ushortn, int, intrn, uintn, uintn, long, longn, ulong, ulongn, or optionally double or doublen. Ti is type char, uchar, short, shortn, int, intrn, long, or longn. Tu is type uchar, ucharn, ushort, ushortn, uint, uintn, ulong, or ulongn. n is 2, 3, 4, 8, or 16. **Optional extension** enables half and halfn types.

$\text{int } \text{isequal}(\text{float } x, \text{float } y)$	Compare of $x == y$
$\text{intn } \text{isequal}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isequal}(\text{double } x, \text{double } y)$	Compare of $x != y$
$\text{longn } \text{isequal}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isequal}(\text{half } x, \text{half } y)$	Compare of $x > y$
$\text{shortn } \text{isequal}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{isnotequal}(\text{float } x, \text{float } y)$	Compare of $x != y$
$\text{intn } \text{isnotequal}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isnotequal}(\text{double } x, \text{double } y)$	Compare of $x > y$
$\text{longn } \text{isnotequal}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isnotequal}(\text{half } x, \text{half } y)$	Compare of $x >= y$
$\text{shortn } \text{isnotequal}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{isgreater}(\text{float } x, \text{float } y)$	Compare of $x < y$
$\text{intn } \text{isgreater}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isgreater}(\text{double } x, \text{double } y)$	Compare of $x < y$
$\text{longn } \text{isgreater}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isgreater}(\text{half } x, \text{half } y)$	Compare of $x < y$
$\text{shortn } \text{isgreater}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{isgreaterequal}(\text{float } x, \text{float } y)$	Compare of $x < y$
$\text{intn } \text{isgreaterequal}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isgreaterequal}(\text{double } x, \text{double } y)$	Compare of $x < y$
$\text{longn } \text{isgreaterequal}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isgreaterequal}(\text{half } x, \text{half } y)$	Compare of $x < y$
$\text{shortn } \text{isgreaterequal}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{isless}(\text{float } x, \text{float } y)$	Compare of $x < y$
$\text{intn } \text{isless}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isless}(\text{double } x, \text{double } y)$	Compare of $x < y$
$\text{longn } \text{isless}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isless}(\text{half } x, \text{half } y)$	Compare of $x < y$
$\text{shortn } \text{isless}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{islessequal}(\text{float } x, \text{float } y)$	Compare of $x < y$
$\text{intn } \text{islessequal}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{islessequal}(\text{double } x, \text{double } y)$	Compare of $x < y$
$\text{longn } \text{islessequal}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{islessequal}(\text{half } x, \text{half } y)$	Compare of $x < y$
$\text{shortn } \text{islessequal}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{islessgreater}(\text{float } x, \text{float } y)$	Compare of $(x < y) (x > y)$
$\text{intn } \text{islessgreater}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{islessgreater}(\text{double } x, \text{double } y)$	Compare of $(x < y) (x > y)$
$\text{longn } \text{islessgreater}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{islessgreater}(\text{half } x, \text{half } y)$	Compare of $(x < y) (x > y)$
$\text{shortn } \text{islessgreater}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{isfinite}(\text{float})$	Test for finite value
$\text{intn } \text{isfinite}(\text{floatn})$	
$\text{int } \text{isfinite}(\text{double})$	
$\text{longn } \text{isfinite}(\text{doublen})$	
$\text{int } \text{isfinite}(\text{half})$	
$\text{shortn } \text{isfinite}(\text{halfn})$	

$\text{int } \text{isinf}(\text{float})$	Test for + or -infinity
$\text{intn } \text{isinf}(\text{floatn})$	
$\text{int } \text{isinf}(\text{double})$	Test for a NaN
$\text{longn } \text{isinf}(\text{doublen})$	
$\text{int } \text{isinf}(\text{half})$	Test for a normal value
$\text{shortn } \text{isinf}(\text{halfn})$	
$\text{int } \text{isnan}(\text{float})$	Test for a normal value
$\text{intn } \text{isnan}(\text{floatn})$	
$\text{int } \text{isnan}(\text{double})$	Test if arguments are ordered
$\text{longn } \text{isnan}(\text{doublen})$	
$\text{int } \text{isnan}(\text{half})$	Test if arguments are unordered
$\text{shortn } \text{isnan}(\text{halfn})$	
$\text{int } \text{isnormal}(\text{float})$	Test for sign bit
$\text{intn } \text{isnormal}(\text{floatn})$	
$\text{int } \text{isnormal}(\text{double})$	
$\text{longn } \text{isnormal}(\text{doublen})$	
$\text{int } \text{isnormal}(\text{half})$	
$\text{shortn } \text{isnormal}(\text{halfn})$	
$\text{int } \text{isordered}(\text{float } x, \text{float } y)$	Test if arguments are ordered
$\text{intn } \text{isordered}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isordered}(\text{double } x, \text{double } y)$	
$\text{longn } \text{isordered}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isordered}(\text{half } x, \text{half } y)$	
$\text{shortn } \text{isordered}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{isunordered}(\text{float } x, \text{float } y)$	Test for sign bit
$\text{intn } \text{isunordered}(\text{floatn } x, \text{floatn } y)$	
$\text{int } \text{isunordered}(\text{double } x, \text{double } y)$	
$\text{longn } \text{isunordered}(\text{doublen } x, \text{doublen } y)$	
$\text{int } \text{isunordered}(\text{half } x, \text{half } y)$	
$\text{shortn } \text{isunordered}(\text{halfn } x, \text{halfn } y)$	
$\text{int } \text{signbit}(\text{float})$	1 if MSB in component of x is set; else 0
$\text{intn } \text{signbit}(\text{floatn})$	
$\text{int } \text{signbit}(\text{double})$	
$\text{longn } \text{signbit}(\text{doublen})$	
$\text{int } \text{signbit}(\text{half})$	
$\text{shortn } \text{signbit}(\text{halfn})$	
$\text{int } \text{any}(Ti x)$	1 if MSB in component of x is set; else 0
$\text{int } \text{all}(Ti x)$	1 if MSB in all components of x are set; else 0
$T \text{bitselect}(Ta, Tb, Tc)$	Each bit of result is corresponding bit of a if corresponding bit of c is 0
$\text{half } \text{bitselect}(\text{half } a, \text{half } b, \text{half } c)$	
$\text{halfn } \text{bitselect}(\text{halfn } a, \text{halfn } b, \text{halfn } c)$	
$T \text{select}(Ta, Tb, Tc)$	For each component of a vector type, $\text{result}[i] = \text{if MSB of } c[i] \text{ is set ? } b[i] : a[i]$
$T \text{select}(Ta, Tb, Tu c)$	For scalar type, $\text{result} = c : b : a$
$\text{halfn } \text{select}(\text{halfn } a, \text{halfn } b, \text{shortn } c)$	
$\text{half } \text{select}(\text{half } a, \text{half } b, \text{short } c)$	
$\text{halfn } \text{select}(\text{halfn } a, \text{halfn } b, \text{ushortn } c)$	
$\text{half } \text{select}(\text{half } a, \text{half } b, \text{ushort } c)$	

Miscellaneous Vector Functions [6.12.12]

Tm and Tn are type charn, ucharn, shortn, ushortn, intrn, uintn, longn, ulongn, floatn, optionally doublen, or halfn if the **half extension** is enabled, where n is 2, 4, 8, or 16 except in `vec_step` it may also be 3. TUn is ucharn, ushortn, uintn, or ulongn.

$\text{int } \text{vec_step}(Tm a)$	Takes a built-in scalar or vector data type argument, returns an integer showing number of elements in the scalar or vector. Returns 1 for scalar, 4 for 3-component vector, else number of elements in the specified type.
$Tn \text{shuffle}(Tm x, TUn mask)$	Construct permutation of elements from one or two input vectors, return a vector with same element type as input and length that is the same as the shuffle mask.

printf Function [6.12.13]

Writes output to an implementation-defined stream.

```
int printf(constant char * restrict format, ...)
```

printf output synchronization

When the event associated with a particular kernel invocation completes, the output of applicable printf() calls is flushed to the implementation-defined output stream.

printf format string

The format string follows C99 conventions and supports an optional vector specifier:

```
%[flags][width].[precision][vector][length]conversion
```

Atomic Functions [6.12.11] [9.3]

These functions provide atomic operations on 32-bit signed and unsigned integers and single precision floating-point to locations in `_global` or `_local` memory. T is type int or unsigned int. T may also be type float for `atomic_xchg`, and type long or ulong for extended 64-bit atomic functions. Q is volatile `_global` or volatile `_local`.

$T \text{atomic_add}(Q T *p, T val)$	Read, add, and store
$T \text{atomic_sub}(Q T *p, T val)$	Read, subtract, and store
$T \text{atomic_xchg}(Q T *p, T val)$	Read, swap, and store
$T \text{atomic_inc}(Q T *p)$	Read, increment, and store
$T \text{atomic_dec}(Q T *p)$	Read, decrement, and store
$T \text{atomic_cmpxchg}(Q T *p, T cmp, T val)$	Read, store (* $p == \text{cmp}$) ? $\text{val} : p$
$T \text{atomic_min}(Q T *p, T val)$	Read, store min(* p, val)
$T \text{atomic_max}(Q T *p, T val)$	Read, store max(* p, val)
$T \text{atomic_and}(Q T *p, T val)$	Read, store (* $p & val$)
$T \text{atomic_or}(Q T *p, T val)$	Read, store (* $p val$)
$T \text{atomic_xor}(Q T *p, T val)$	Read, store (* $p ^ val$)

Optional extensions enable forms of these functions using the `atom_` prefix that implement atomic operations on 64-bit signed and unsigned integers. To use any of these forms, include the following in the OpenCL program source:

```
#pragma OPENCL EXTENSION extension-name : enable
```

Use `cl_khr_int64_base_atomics` for `extension-name` to enable 64-bit versions of the following functions:

<code>atom_add</code>	<code>atom_sub</code>	<code>atom_inc</code>
<code>atom_dec</code>	<code>atom_xchg</code>	<code>atom_cmpxchg</code>

Use `cl_khr_int64_extended_atomics` for `extension-name` to enable 64-bit versions of the following functions:

<code>atom_min</code>	<code>atom_max</code>	<code>atom_and</code>
<code>atom_or</code>	<code>atom_xor</code>	

<code>void barrier(cl_mem_fence_flags flags)</code>	Work-items in a work-group must execute this before any can continue
<code>void mem_fence(cl_mem_fence_flags flags)</code>	Orders loads and stores of a work-item executing a kernel
<code>void read_mem_fence(cl_mem_fence_flags flags)</code>	Orders memory loads
<code>void write_mem_fence(cl_mem_fence_flags flags)</code>	Orders memory stores

Examples:

The following examples show the use of the vector specifier in the `printf` format string.

```
float4 f = (float4)(1.0f, 2.0f, 3.0f, 4.0f);
printf("f4 = %2.2v4f\n", f);
```

Output: f4 = 1.00,2.00,3.00,4.00

```
uchar4 uc = (uchar4)(0xFA, 0xFB, 0xFC, 0xFD);
printf("uc = %#v4x\n", uc);
```

Output: uc = 0xfa,0xfb,0xfc,0xfd

```
uint2 ui = (uint2)(0x12345678, 0x87654321);
printf("unsigned short value = (%#v2hx)\n", ui);
```

Output: unsigned short value = (0x5678,0x4321)

OpenCL Image Processing:

Following is a subset of the OpenCL specification that pertains to image processing and graphics.

Image Objects

Create Image Objects [5.3.1]

```
cl_mem clCreateImage (cl_context context,
    cl_mem_flags flags,
    const cl_image_format *image_format,
    const cl_image_desc *image_desc,
    void **host_ptr, cl_int *errcode_ret)
flags:
    CL_MEM_READ_WRITE,
    CL_MEM_(WRITE, READ)_ONLY,
    CL_MEM_HOST_(WRITE, READ)_ONLY,
    CL_MEM_HOST_NO_ACCESS,
    CL_MEM_(USE, ALLOC, COPY)_HOST_PTR
```

Query List of Supported Image Formats [5.3.2]

```
cl_int clGetSupportedImageFormats (
    cl_context context, cl_mem_flags flags,
    cl_mem_object_type image_type,
    cl_uint num_entries, cl_image_format *image_formats,
    cl_uint *num_image_formats)
```

flags: See `clCreateImage`

`image_type`: CL_MEM_OBJECT_IMAGE{1D, 2D, 3D},
 CL_MEM_OBJECT_BUFFER,
 CL_MEM_OBJECT_IMAGE{1D, 2D}_ARRAY

Read, Write, Copy Image Objects [5.3.3]

```
cl_int clEnqueueReadImage (
    cl_command_queue command_queue,
    cl_mem image, cl_bool blocking_read,
    const size_t *origin, const size_t *region,
    size_t row_pitch, size_t slice_pitch, void *ptr,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)

cl_int clEnqueueWriteImage (
    cl_command_queue command_queue,
    cl_mem image, cl_bool blocking_write,
    const size_t *origin, const size_t *region,
    size_t input_row_pitch, size_t input_slice_pitch,
    const void *ptr, cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Image Read and Write Built-in Functions

[6.12.14] [9.4, 9.5.8]

The built-in functions defined in this section can only be used with image memory objects created with `clCreateImage`. `sampler` specifies the addressing and filtering mode to use. To enable the `read_imageh` and `write_imageh` forms, enable the extension `cl_khr_fp16`. To enable the type `image3d_t` in functions `write_image{f, i, ui, h}`, enable the extension `cl_khr_3d_image_writes`.

Read and write functions for 1D images

Read an element from a 1D image, or write a color value to a location in a 1D image.

```
float4 read_imagef (image1d_t image, sampler_t sampler,
    {int, float} coord)
float4 read_imagef (image1d_t image, int coord)
float4 read_imagef (image1d_array_t image,
    sampler_t sampler, {int2, float4} coord)
float4 read_imagef (image1d_array_t image, int2 coord)
float4 read_imagef (image1d_buffer_t image, int coord)

int4 read_imagei (image1d_t image, sampler_t sampler,
    {int, float} coord)
int4 read_imagei (image1d_t image, int coord)
int4 read_imagei (image1d_array_t image, sampler_t sampler,
    {int2, float2} coord)
int4 read_imagei (image1d_array_t image, int2 coord)
int4 read_imagei (image1d_buffer_t image, int coord)

uint4 read_imageui (image1d_t image, sampler_t sampler,
    {int, float} coord)
uint4 read_imageui (image1d_t image, int coord)
uint4 read_imageui (image1d_array_t image,
    sampler_t sampler, {int2, float2} coord)
uint4 read_imageui (image1d_array_t image, int2 coord)
uint4 read_imageui (image1d_buffer_t image, int coord)

half4 read_imageh (image1d_t image, sampler_t sampler,
    {int, float} coord)
half4 read_imageh (image1d_t image, int coord)
half4 read_imageh (image1d_array_t image,
    sampler_t sampler, {int2, float4} coord)
half4 read_imageh (image1d_array_t image, int2 coord)
half4 read_imageh (image1d_buffer_t image, int coord)

void write_imagef (image1d_t image, int coord, float4 color)
void write_imagef (image1d_array_t image, int2 coord,
    float4 color)
void write_imagef (image1d_buffer_t image, int coord,
    float4 color)
```

Read and write functions for 1D images (continued)

```
void write_imagei (image1d_t image, int coord, int4 color)
void write_imagei (image1d_array_t image, int2 coord,
    int4 color)
void write_imagei (image1d_buffer_t image, int coord,
    int4 color)

void write_imageh (image1d_t image, int coord, half4 color)
void write_imageh (image1d_array_t image, int2 coord,
    half4 color)
void write_imageh (image1d_buffer_t image, int coord,
    half4 color)

void write_imageui (image1d_t image, int coord, uint4 color)
void write_imageui (image1d_array_t image, int2 coord,
    uint4 color)
void write_imageui (image1d_buffer_t image, int coord,
    uint4 color)
```

Read and write functions for 2D images

Read an element from a 2D image, or write a color value to a location in a 2D image.

```
float4 read_imagef (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
float4 read_imagef (image2d_t image, int2 coord)
float4 read_imagef (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
float4 read_imagef (image2d_array_t image, int4 coord)

int4 read_imagei (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
int4 read_imagei (image2d_t image, int2 coord)
int4 read_imagei (image2d_array_t image, sampler_t sampler,
    {int4, float4} coord)
int4 read_imagei (image2d_array_t image, int4 coord)

uint4 read_imageui (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
uint4 read_imageui (image2d_t image, int2 coord)
uint4 read_imageui (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
uint4 read_imageui (image2d_array_t image, int4 coord)

half4 read_imageh (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
half4 read_imageh (image2d_t image, int2 coord)
half4 read_imageh (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
half4 read_imageh (image2d_array_t image, int4 coord)

half4 read_imageh (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
half4 read_imageh (image2d_t image, int2 coord)
half4 read_imageh (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
half4 read_imageh (image2d_array_t image, int4 coord)

half4 read_imageh (image2d_t image, sampler_t sampler,
    {int2, float2} coord)
half4 read_imageh (image2d_t image, int2 coord)
half4 read_imageh (image2d_array_t image,
    sampler_t sampler, {int4, float4} coord)
half4 read_imageh (image2d_array_t image, int4 coord)
```

Query Image Objects [5.3.6]

```
cl_int clGetImageInfo (cl_mem image,
    cl_image_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
param_name: CL_IMAGE_{ARRAY, ELEMENT}_SIZE,
    CL_IMAGE_{ROW, SLICE}_PITCH,
    CL_IMAGE_{FORMAT, BUFFER, HEIGHT, WIDTH, DEPTH},
    CL_IMAGE_NUM_{SAMPLES, MIP_LEVELS},
    CL_IMAGE_DX9_MEDIA_PLANE_KHR,
    CL_IMAGE_{D3D10, D3D11}_SUBRESOURCE_KHR
```

Image Formats [5.3.1.1, 9.5]

Supported image formats: `image_channel_order` with `image_channel_data_type`.

Built-in support: [Table 5.8]

CL_RGBA: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT{8,16}, CL_SIGNED_INT{8,16,32}, CL_UNSIGNED_INT{8,16,32}
--

CL_BGRA: CL_UNORM_INT8

Optional support: [Table 5.6]

CL_R, CL_A: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT{8,16}, CL_SIGNED_INT{8,16,32}, CL_UNSIGNED_INT{8,16,32}, CL_SNORM_INT{8,16}

CL_INTENSITY: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT{8,16}, CL_SNORM_INT{8,16}

CL_LUMINANCE: CL_UNORM_INT{8,16}, CL_HALF_FLOAT, CL_FLOAT, CL_SNORM_INT{8,16}

CL_RG, CL_RA: CL_HALF_FLOAT, CL_FLOAT, CL_UNORM_INT{8,16}, CL_SIGNED_INT{8,16,32}, CL_UNSIGNED_INT{8,16,32}, CL_SNORM_INT{8,16}

CL_RGB: CL_UNORM_SHORT_{555,565}, CL_UNORM_INT_101010

CL_ARGB: CL_UNORM_INT8, CL_SIGNED_INT8, CL_UNSIGNED_INT8, CL_SNORM_INT8

CL_BGRA: CL_{SIGNED, UNSIGNED}_INT8, CL_SNORM_INT8
--

Map and Unmap Image Objects [5.3.5]

```
void * clEnqueueMapImage (
    cl_command_queue command_queue, cl_mem image,
    cl_bool blocking_map, cl_map_flags map_flags,
    const size_t *origin, const size_t *region,
    size_t *image_row_pitch, size_t *image_slice_pitch,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Also see `clGetMemObjectInfo` [5.4.5]

Read and write functions for 2D images (continued)

```
void write_imagef (image2d_t image, int2 coord, float4 color)
void write_imagef (image2d_array_t image, int2 coord,
    int4 color)
```

```
void write_imagei (image2d_t image, int2 coord, int4 color)
void write_imagei (image2d_array_t image, int4 coord,
    int4 color)
```

```
void write_imageui (image2d_t image, int2 coord, uint4 color)
void write_imageui (image2d_array_t image, int4 coord,
    uint4 color)
```

```
void write_imageh (image2d_t image, int2 coord, half4 color)
void write_imageh (image2d_array_t image, int4 coord,
    half4 color)
```

Read and write functions for 3D images

Read an element from a 3D image, or write a color value to a location in a 3D image.

```
float4 read_imagef (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
float4 read_imagef (image3d_t image, int4 coord)
```

```
int4 read_imagei (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
int4 read_imagei (image3d_t image, int4 coord)
```

```
uint4 read_imageui (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
uint4 read_imageui (image3d_t image, int4 coord)
```

```
half4 read_imageh (image3d_t image, sampler_t sampler,
    {int4, float4} coord)
half4 read_imageh (image3d_t image, int4 coord)
```

Use this pragma to enable writes to type `image3d_t`:

```
#pragma OPENCL EXTENSION cl_khr_3d_image_writes : enable
```

```
void write_imagef (image3d_t image, int4 coord,
    float4 color)
```

```
void write_imagei (image3d_t image, int4 coord, int4 color)
```

```
void write_imageui (image3d_t image, int4 coord, uint4 color)
```

```
void write_imageh (image3d_t image, int4 coord, half4 color)
```

Access Qualifiers [6.6]

Apply to 2D and 3D image types to declare if the image memory object is being read or written by a kernel.

_read_only, _read_only

_write_only, _write_only

OpenCL Image Processing (continued): Following is a subset of the OpenCL specification that pertains to image processing and graphics.

Sampler Objects [5.5]

```
cl_sampler clCreateSampler (
    cl_context context, cl_bool normalized_coords,
    cl_addressing_mode addressing_mode,
    cl_filter_mode filter_mode, cl_int *errcode_ret)
addressing_mode: CL_ADDRESS_[MIRRORED_]REPEAT,
    CL_ADDRESS_CLAMP[TO_EDGE], CL_ADDRESS_NONE
filter_mode: CL_FILTER_[NEAREST, LINEAR]
cl_int clRetainSampler (cl_sampler sampler)
cl_int clReleaseSampler (cl_sampler sampler)
cl_int clGetSamplerInfo (cl_sampler sampler,
    cl_sampler_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
param_name: CL_SAMPLER_REFERENCE_COUNT,
    CL_SAMPLER_[CONTEXT, FILTER_MODE],
    CL_SAMPLER_ADDRESSING_MODE,
    CL_SAMPLER_NORMALIZED_COORDS
```

Sampler Declaration Fields [6.12.14.1]

The sampler can be passed as an argument to the kernel using `clSetKernelArg`, or can be declared in the outermost scope of kernel functions, or it can be a constant variable of type `sampler_t` declared in the program source.

```
const sampler_t <sampler-name> =
    <normalized-mode> | <address-mode> | <filter-mode>
normalized-mode:
    CLK_NORMALIZED_COORDS_{TRUE, FALSE}
address-mode:
    CLK_ADDRESS_{REPEAT, CLAMP, NONE},
    CLK_ADDRESS_[CLAMP_TO_EDGE, MIRRORED_REPEAT]
filter-mode: CLK_FILTER_NEAREST, CLK_FILTER_LINEAR
```

Direct3D 10 Sharing [9.9]

Provide interoperability between OpenCL and Direct3D 10. If supported, `cl_khr_d3d10_sharing` will be present in `CL_PLATFORM_EXTENSIONS` or `CL_DEVICE_EXTENSIONS`.

```
cl_int clGetDeviceIDsFromD3D10KHR (
    cl_platform_id platform,
    cl_d3d10_device_source_khr d3d_device_source,
    void *d3d_object,
    cl_d3d10_device_set_khr d3d_device_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_uint *num_devices)
d3d_device_source:
    CL_D3D10_{DEVICE, DXGI_ADAPTER}_KHR
d3d_device_set:
    CL_ALL_PREFERRED_DEVICES_FOR_D3D10_KHR
cl_mem clCreateFromD3D10BufferKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Buffer *resource, cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
```

```
cl_mem clCreateFromD3D10Texture2DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Texture2D *resource, UINT subresource,
    cl_int *errcode_ret)
```

flags: See `clCreateFromD3D10BufferKHR`

```
cl_mem clCreateFromD3D10Texture3DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D10Texture3D *resource, UINT subresource,
    cl_int *errcode_ret)
```

flags: See `clCreateFromD3D10BufferKHR`

```
cl_int clEnqueueAcquireD3D10ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueReleaseD3D10ObjectsKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

Direct3D 11 Sharing [9.11]

Provide interoperability between OpenCL and Direct3D 11. If supported, `cl_khr_d3d11_sharing` will be present in `CL_PLATFORM_EXTENSIONS` or `CL_DEVICE_EXTENSIONS`.

```
cl_mem clCreateFromD3D11Texture2DKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Texture2D *resource,
    UINT subresource, cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
```

Image Query Functions [6.12.14.5]

Query image width, height, and depth in pixels

```
int get_image_width (image{1,2,3}d_t image)
int get_image_width (image1d_buffer_t image)
int get_image_width (image{1,2}d_array_t image)

int get_image_height (image{2,3}d_t image)
int get_image_height (image2d_array_t image)

int get_image_depth (image3d_t image)
```

Query image array size

```
size_t get_image_array_size (image1d_array_t image)
size_t get_image_array_size (image2d_array_t image)
```

Query image dimensions

```
int2 get_image_dim (image2d_t image)
int2 get_image_dim (image2d_array_t image)
int4 get_image_dim (image3d_t image)
```

Query image Channel data type and order

```
int get_image_channel_data_type (image{1,2,3}d_t image)
int get_image_channel_data_type (image1d_buffer_t image)
int get_image_channel_data_type (image{1,2}d_array_t image)

int get_image_channel_order (image{1,2,3}d_t image)
int get_image_channel_order (image1d_buffer_t image)
int get_image_channel_order (image{1,2}d_array_t image)
```

OpenGL Sharing

Functions available if `cl_khr_gl_sharing` or `cl_apple_gl_sharing` is supported. Creating OpenCL memory objects from OpenGL objects using `clCreateFromGLBuffer`, `clCreateFromGLTexture`, and `clCreateFromGLRenderbuffer` ensure the OpenGL object will not be deleted while the corresponding OpenCL memory object exists.

CL Buffer Objects > GL Buffer Objects [9.7.2]

```
cl_mem clCreateFromGLBuffer (cl_context context,
    cl_mem_flags flags, GLuint bufobj, cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
```

CL Image Objects > GL Textures [9.7.3]

```
cl_mem clCreateFromGLTexture (cl_context context,
    cl_mem_flags flags, GLenum texture_target,
    GLint mipmaplevel, GLuint texture, cl_int *errcode_ret)
flags: See clCreateFromGLBuffer
```

```
texture_target: GL_TEXTURE_{1D, 2D}_{ARRAY},
    GL_TEXTURE_{3D, BUFFER, RECTANGLE},
    GL_TEXTURE_CUBE_MAP_POSITIVE_{X, Y, Z},
    GL_TEXTURE_CUBE_MAP_NEGATIVE_{X, Y, Z}
```

CL Image Objects > GL Renderbuffers [9.7.4]

```
cl_mem clCreateFromGLRenderbuffer (
    cl_context context, cl_mem_flags flags,
    GLuint renderbuffer, cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
```

Query Information [9.7.5]

```
cl_int clGetGLObjectInfo (cl_mem memobj,
    cl_gl_object_type *gl_object_type,
    GLuint *gl_object_name)
```

*`gl_object_type` returns:

```
CL_GL_OBJECT_TEXTURE_BUFFER,
    CL_GL_OBJECT_TEXTURE{1D, 2D, 3D},
    CL_GL_OBJECT_TEXTURE{1D, 2D}_{ARRAY},
    CL_GL_OBJECT_{BUFFER, RENDERBUFFER}
```

```
cl_int clGetGLTextureInfo (cl_mem memobj,
    cl_gl_texture_info param_name,
    size_t param_value_size, void *param_value,
    size_t *param_value_size_ret)
param_name:
    CL_GL_{TEXTURE_TARGET, MIPMAP_LEVEL}
```

Share Objects [9.7.6]

```
cl_int clEnqueueAcquireGLObjects (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

CL Event Objects > GL Sync Objects [9.8.2]

```
cl_event clCreateEventFromGSyncKHR (
    cl_context context, GSync sync, cl_int *errcode_ret)
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

CL Context > GL Context, Sharegroup [9.6.5]

```
cl_int clGetGLContextInfoKHR (
    const cl_context_properties *properties,
    cl_gl_context_info param_name, size_t param_value_size,
    void *param_value, size_t *param_value_size_ret)
param_name: CL_DEVICES_FOR_GL_CONTEXT_KHR,
    CL_CURRENT_DEVICE_FOR_GL_CONTEXT_KHR
```

DX9 Media Surface Sharing [9.10]

These functions allow applications to use media surfaces as OpenCL memory objects. If this extension is supported, `cl_khr_dx9_media_sharing` will be present in `CL_PLATFORM_EXTENSIONS` or `CL_DEVICE_EXTENSIONS`.

```
cl_int clGetDeviceIDsFromDX9MediaAdapterKHR (
    cl_platform_id platform, cl_uint num_media_adapters,
    cl_dx9_media_adapter_type_khr *media_adapters_type,
    void *media_adapters,
    cl_dx9_mem_adapter_set_khr media_adapter_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_int *num_devices)
media_adapter_type: CL_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR
media_adapter_set: CL_ALL_DEVICES_FOR_DXP_MEDIA_ADAPTER_KHR,
    CL_PREFERRED_DEVICES_FOR_DX9_MEDIA_ADAPTER_KHR
```

```
cl_int clGetDeviceIDsFromD3D11KHR (
    cl_platform_id platform,
    cl_d3d11_device_source_khr d3d_device_source,
    void *d3d_object,
    cl_d3d11_device_set_khr d3d_device_set,
    cl_uint num_entries, cl_device_id *devices,
    cl_uint *num_devices)
d3d_device_source: CL_D3D11_DEVICE_KHR,
    CL_D3D11 DXGI_ADAPTER_KHR
d3d_device_set: CL_PREFERRED_DEVICES_FOR_D3D11_KHR,
    CL_ALL_DEVICES_FOR_D3D11_KHR
```

```
cl_mem clCreateFromD3D11BufferKHR (
    cl_context context, cl_mem_flags flags,
    ID3D11Buffer *resource, cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
```

cl_mem clCreateFromDX9MediaSurfaceKHR (

```
cl_context context, cl_mem_flags flags,
    cl_dx9_media_adapter_type_khr adapter_type,
    void *surface_info, cl_uint plane, cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
adapter_type: CL_ADAPTER_{D3D9, D3D9EX, DXVA}_KHR
```

```
cl_int clEnqueueAcquireDX9MediaSurfacesKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

```
cl_int clEnqueueReleaseDX9MediaSurfacesKHR (
    cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

cl_mem clCreateFromD3D11Texture3DKHR (

```
cl_context context, cl_mem_flags flags,
    ID3D11Texture3D *resource, UINT subresource,
    cl_int *errcode_ret)
flags: CL_MEMORY_{READ, WRITE}_ONLY, CL_MEMORY_READ_WRITE
```

cl_int clEnqueueAcquireD3D11ObjectsKHR (

```
cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

cl_int clEnqueueReleaseD3D11ObjectsKHR (

```
cl_command_queue command_queue,
    cl_uint num_objects, const cl_mem *mem_objects,
    cl_uint num_events_in_wait_list,
    const cl_event *event_wait_list, cl_event *event)
```

OpenCL API 1.2 Reference Guide

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