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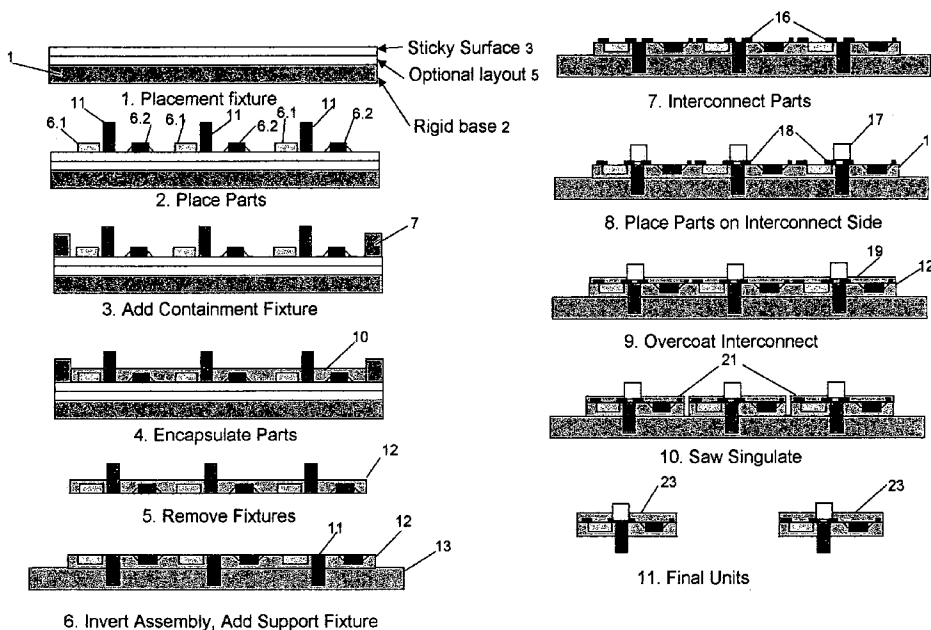
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Declaration under Rule 4.17:
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[Continued on next page]

(54) Title: PROCESS FOR PLACING, SECURING AND INTERCONNECTING ELECTRONIC COMPONENTS



1. First embodiment of the assembly process.

(57) Abstract: A method for fabricating an electronic assembly which enables the assembly and interconnection of surface mount components and/or other electrical, electronic, electro-optical, electro-mechanical and user interface devices with external I/O contacts on a planar surface without the use of solder or otherwise exposing the components to temperatures substantially above ambient.

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A. CLASSIFICATION OF SUBJECT MATTER
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According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
H05K H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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Further documents are listed in the continuation of Box C.

See patent family annex.

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C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
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INTERNATIONAL SEARCH REPORT

Information on patent family members

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