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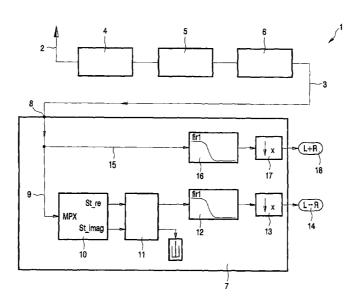
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— as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES,

[Continued on next page]

(54) Title: RECEIVER



(57) Abstract: The invention refers to a method for a receiver (1) having a signal path (3) incorporating a tuner (4), a frequency demodulator circuit (5) for supplying an analog stereo multiplex signal comprising a baseband stereo sum signal, a 19 kHz stereo pilot and a stereo difference signal, which is double sideband amplitude-modulated on a suppressed 38 kHz subcarrier, a sampler (6) for converting the analog stereo multiplex signal into a time discrete digital stereo multiplex signal and a stereo decoder (7) for decoding the time discrete digital stereo multiplex signal into a time-discrete digital stereo sum and a time discrete digital stereo difference signal. According to the invention the analog stereo multiplex signal is converted into a time discrete digital stereo multiplex signal and then the time discrete digital stereo multiplex signal is shifted over a frequency of 19 kHz to extract the pilot tone.



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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols) IPC 7 $\,$ H04S $\,$ H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

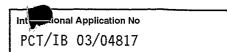
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EPO-Internal, WPI Data, PAJ

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X Further documents are listed in the continuation of box C.	χ Patent family members are listed in annex.
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