

(19)
(12)

(KR)
(A)

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(22) 2002 07 08

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(72) 1488-39 1104

(74)
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(54)

가 , SOC 100%
가 ,

2

, BMS,

1
2
3
4 3 C

system ; BMS) , BMS 가 (Battery Management System ; BMS) 가 가 (Cycle) 가 (dT/dt_ref) , (dT/dt_cal) 가 (dT/dt_cal) (dT/dt_ref) 가 (State Of Charge ; SOC) BMS 100% (dT/dt_cal) 1

$$dT/dt_{cal} = Tb0 - Tb1 + K(Tb0 - Ta0) \quad 1$$

Tb0 , K , Tb1 , Ta0 , 1

$$(dT/dt_{ref}) \quad 2$$

$$dT/dt_{ref} = lbat_{avg} / 100 * K' \quad 2$$

lbat_avg , 1 , K'

3 'B' 가 3 'A'

, BMS 4 SOC 100% 3 'C'

SOC

가 가 , 가

가 ; 가 ; SOC 100% ;

가 ;

1 (10) , SOC (Ah) , SOC (10) , (10) , (10) , BMS(20) (50) , BMS(20) (30) , 가 (30) (40) . (50) , ()

40) BMS(40) (30) 가 80% (Very High SOC) , 65% 80% (High SOC) , 40% 65% (Normal SOC) , 25% 40% (Very Low SOC) (Low SOC) , 25% (S101) BMS (S102) 1 (dT/dt_cal) (S103). ((dT/dt_ref) (dT/dt_cal) (S104). (S105), (Ibat_avg) 1 (Ibat_avg) (S106). (Ibat_avg) (Error Flag) (S107).

, (S108) (S109), 가
 S108
 SOC 100%
 (S110).
 S106 (ibat_avg) 3
 (S111),
 (S112).
 BMS 가 가 , (S113).
 가 , 가
 (S114).

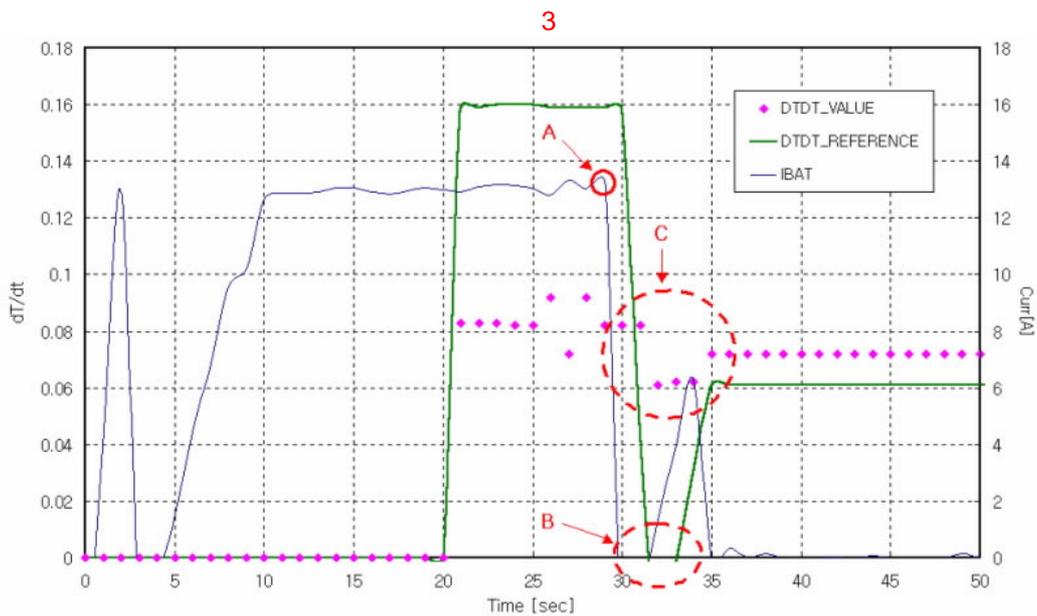
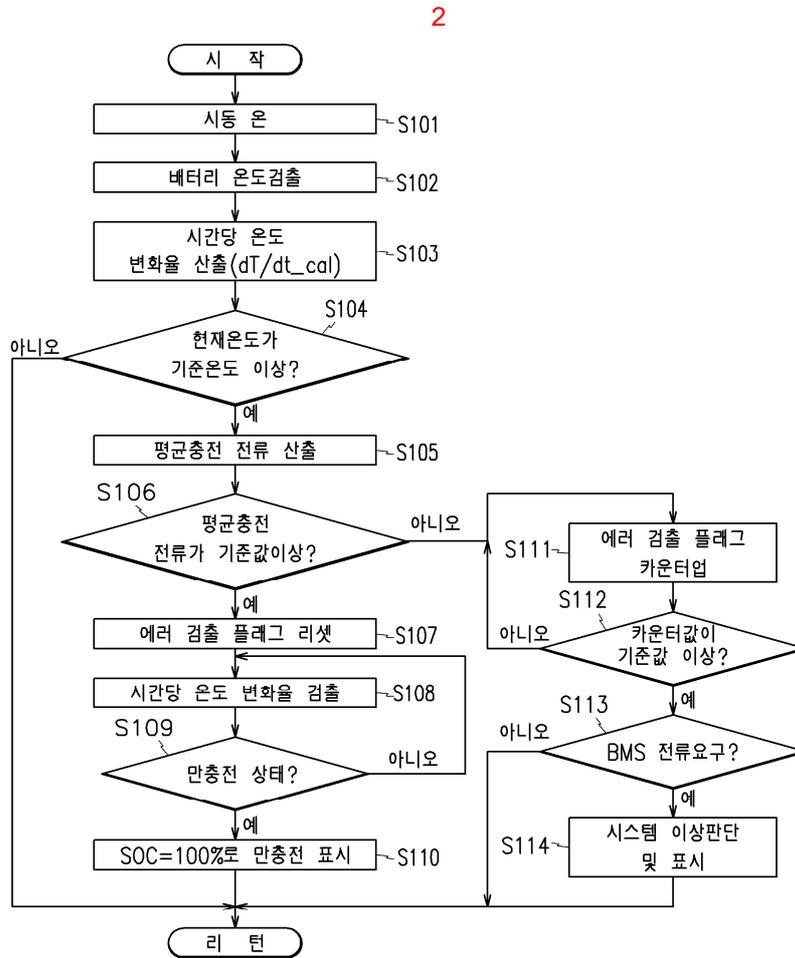
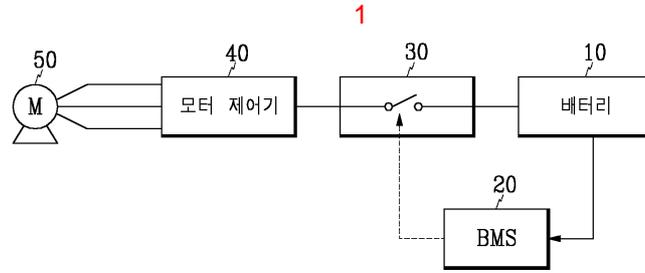
(57)

1.

;
 ;
 가 ;
 SOC 100% ;
 가 ;

2.

1 ,
 BMS 가



4

