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(54) **DIGITAL DISPLAY**

(57) A method for controlling a digital display device and a digital display system implementing the method are provided with the aim of controlling the display device in such a way as to reduce the effect of dynamic false contouring on the quality of a displayed image. According to the method, received image data defining brightness levels according to a scheme of light pulse modulation based upon binary-weighted pulse durations are converted into a data defining a sequence of pulses of binary and non-binary weighted duration in which the highest value weighting represents a pulse of a duration less than half the total duration of illumination required during an image refresh period to achieve a pixel at the highest brightness level. In a further embodiment, the weighting of highest value represents a pulse of duration less than one quarter of the duration required for highest pixel brightness.

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Fig. 3

124		Highest Pulse	e Weighting of 48		
\\8 16	32	48	48	48	48
<u> 11 A 77 77 1</u>					

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Description

[0001] This invention relates to digital displays. In particular, but not exclusively, this invention relates to digital display system and a method for configuring and operating a digital display device in such a way as to reduce the incidence of an effect known as 'dynamic false contouring' in displayed images.

[0002] Dynamic false contouring arises when there is rapid relative movement of the eye of a viewer and the surface of a digitally controlled image display panel or across a surface onto which a digitally generated image is being projected. Relative movement can have the effect that the eye sees light intended to form part of one pixel being falsely added to or subtracted from the light of an adjacent pixel, in the worst case causing a bright flash or sparkling effect to be seen. Known types of digital display device with which this effect can arise include Digital Micro-mirror Devices (DMD) and display devices based upon liquid crystal display (LCD) technologies, including Liquid Crystal on Silicon (LCOS) devices for example.

[0003] In a known method of operating a digital display device, each of the pixels in an image to be viewed is generated using a pulse modulation technique whereby the perceived brightness and colour of a pixel is determined by the total amount of light of a given wavelength that is emitted by the display device in respect of that pixel during an 'image refresh period'. The image refresh period is selected to be shorter than the minimum response period of the human eye to discrete changes to brightness or colour. An image refresh period in the range 16 to 20ms is typical. Using the pulse modulation technique, a pixel of a required brightness is generated by causing the display device to emit a predetermined combination of light pulses of equal brightness but of differing duration which, if all emitted within the image refresh period, will be integrated by the eye so that a viewer perceives the pixel as having a uniform brightness over the image refresh period. Different colours are perceived by generating different combinations of red, green and blue light pulses of appropriate duration within the image refresh period, as is well known.

[0004] In such a method, dynamic false contouring arises from the perceived false addition and false subtraction of pulses intended for adjacent pixels. In the present invention a method of driving a digital display device has been devised with the intended advantage that the effect of dynamic false contouring on perceived image quality is reduced significantly.

[0005] In a first aspect, the present invention resides in a method for controlling a digital display device to illuminate a pixel at any one of a plurality of brightness levels, wherein the display device is controllable to generate, for a given pixel and a selected one of the plurality of brightness levels, a predetermined sequence of light pulses within an image refresh period such that the pixel may be perceived as having the selected brightness level during the image refresh period, wherein the predetermined sequence of pulses comprises a predetermined combination of one or more pulses of relative duration selected according to a set of pulse weightings wherein the greatest weighting in the set of weightings represents a pulse duration of less than one half of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of the plurality of brightness levels.

10 [0006] In the present invention, by limiting the duration of the longest pulse in a pulse modulation scheme for controlling the display device to less than half the duration required for maximum brightness (255 in an 8-bit scheme), the pulse of longest duration in conventional

¹⁵ 8-bit schemes, representing a relative duration of 128 (2⁷), is necessarily replaced by more than one pulse of shorter duration. The replacement pulses may be of binary-weighted duration, meaning a relative duration defined by a power of 2, of non-binary weighted duration,

20 e.g. a duration represented by a weighting of 48 (out of a maximum 255), or a mixture of binary and non-binary weighted durations. The aim of the replacement set of pulse durations (weightings) is to provide for the same number of brightness levels as defined in received image

²⁵ data, but with the advantage that the light may be provided by pulses of shorter duration than in conventional display driving techniques so that the effect of a perceived false addition and false subtraction of the most heavilyweighted pulses is significantly reduced.

30 [0007] In one example embodiment, the set of pulse weightings comprises a plurality of binary weightings and one or more non-binary weightings. In particular, the set of pulse weightings may comprise a plurality of non-binary weightings.

³⁵ [0008] In a further variant, the highest-value weighting included in the set of pulse weightings represents a pulse of duration no more than one quarter of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of
 ⁴⁰ the plurality of brightness levels. In the 8-bit example,

this ensures the replacement of the pulse of binary weighting 64 with pulses of lesser weighting (duration), further reducing the potential effect of a perceived false addition and false subtraction of pulses between pixels.

⁴⁵ [0009] In a further example embodiment, the weighting of highest value included in the set of pulse weightings is a non-binary weighting and the set of pulse weightings includes two or more non-binary weightings of the same highest value. It is not therefore necessary for the set of ⁵⁰ allowed pulse durations all to be different and two or more

pulses of the same longest duration may be used to illuminate a pixel in a given sequence of pulses.

[0010] In another example embodiment, each of the plurality of brightness levels is defined in received image data by a binary value representing a set of pulses each of binary-weighted duration and the method further comprises converting brightness levels defined in received image data into brightness levels defined according to

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respective combinations of light pulses having relative durations selected according to the set of pulse weightings.

[0011] In a further variant, each of the plurality of brightness levels are defined in received image data by a binary value representing a set of pulses each of binary-weighted relative duration and the method further comprises converting a received binary value into a binary value representing a predetermined combination of light pulses of relative durations selected according to the set of pulse weightings, the method further comprising using the results of the conversion to control the digital display device.

[0012] In an example embodiment, the received image data comprise brightness levels represented by 8-bit binary values in the range 0 to 255, each representing a combination of pulses of binary weighted relative durations selected according to binary pulse weightings 1, 2, 4, 8, 16, 32, 64 and 128 and the method further comprises 20 converting a brightness level defined by a received 8-bit binary value into a binary value representing a predetermined combination of light pulses of relative durations selected according to the set of binary and non-binary pulse weightings 1, 2, 4, 8, 16, 32, 48, 48, 48 and 48 and 25 controlling the digital display device to illuminate pixels using the predetermined combinations of light pulses resulting from the conversion. Optionally, the set of pulse weightings further comprises a non-binary weighting of 24 to provide more flexibility in selecting the combination 30 of pulses required to achieve a particular brightness level. [0013] In a further example embodiment, the method further comprises generating and outputting a sequence of outputs for storage in an image buffer associated with

the display device, each output comprising, for a pulse of a duration defined according to a weighting in the set of pulse weightings, indications of those pixels to be illuminated with the pulse of the defined duration. In a particular variant, the sequence of outputs represents pulses of higher weighting intermixed with pulses of lower weighting. It is not therefore necessary to trigger the display device to illuminate a pixel with pulses in the order of increasing pulse duration and the display device may instead be presented with data defining a different order of pulses while still achieving the same pixel brightness during an image refresh period.

[0014] In a second aspect, the present invention resides in a digital display system, comprising:

a digital display device for displaying an image; and

a display controller arranged to control the digital display device to display pixels in an image each at a required level of brightness,

wherein the display controller comprises:

an input for receiving image data defining, for each of one or more pixels in an image to be

displayed or updated, a brightness level selected from a predetermined plurality of brightness levels;

a processor arranged to receive image data from the input and to transform a brightness level indicated for a pixel in the received image data into a representation defining a predetermined combination of pulses of relative durations selected according to a predetermined set of pulse weightings wherein the highest-value weighting in the set of pulse weightings represents a pulse of duration less than one half of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of the plurality of brightness levels and to generate a sequence of outputs for storage in an image buffer associated with the display device, each output comprising, for a pulse of a duration defined according to a weighting in the set of pulse weightings, indications of those pixels to be illuminated with the pulse of the defined duration; and

means for controlling the display device to illuminate those pixels indicated by the contents of the image buffer in a sequence and for a defined duration as determined by the sequence of stored outputs.

[0015] In one example embodiment in this second aspect of the present invention, the predetermined set of pulse weightings comprises a plurality of binary weightings and one or more of non-binary weightings. Optionally, the predetermined set of pulse weightings comprises a plurality of non-binary weightings.

[0016] In another example embodiment of the system, the weighting of highest value in the set of pulse weightings represents a pulse of duration no more than one quarter of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of the plurality of brightness levels.

[0017] In a further example embodiment of the system, 45 the weighting of highest value in the set of pulse weightings is a non-binary weighting and the set of pulse weightings includes two or more non-binary weightings having the same highest value.

[0018] In another example embodiment of the system, 50 each of the plurality of brightness levels is defined in received image data by a binary value representing a set of pulses each of binary-weighted duration and the processor is arranged to convert brightness levels defined in received image data into brightness levels defined ac-55 cording to respective combinations of light pulses having

relative durations selected according to the set of pulse weightings

[0019] In a further example embodiment of the system,

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each of the plurality of brightness levels are defined in received image data by a binary value representing a set of pulses each of binary-weighted relative duration and the processor is arranged to convert a received binary value into a binary value representing a predetermined combination of light pulses of relative durations selected according to the set of pulse weightings and to use the results of the conversion to generate the sequence of outputs.

[0020] In a particular example embodiment of the system, the received image data comprise brightness levels represented by 8-bit binary values in the range 0 to 255, each representing a combination of pulses of binary weighted relative durations selected according to binary pulse weightings 1, 2, 4, 8, 16, 32, 64 and 128 and the processor is arranged to convert a brightness level defined by a received 8-bit binary value into a binary value representing a predetermined combination of light pulses of relative durations selected according to the set of binary and non-binary pulse weightings 1, 2, 4, 8, 16, 32, 48, 48, 48 and 48 and to use the results of the conversion to generate the sequence of outputs. Optionally, the set of pulse weightings further comprises a non-binary weighting of 24 to provide more flexibility in selecting the combination of pulses required to achieve a particular brightness level.

[0021] In a further example embodiment of the system, the sequence of outputs represent pulses of higher weighting intermixed with pulses of lower weighting.

[0022] In a third aspect, the present invention resides in a digital display device incorporating or associated with a display controller arranged to implement the method according to the first aspect of the present invention or example embodiments thereof.

[0023] In a fourth aspect, the present invention resides in a digital display device arranged to illuminate pixels with light pulses of relative duration defined by weightings selected in any predetermined combination from a set of binary weightings and non-binary-weightings.

[0024] In a particular example embodiment according to this fourth aspect of the present invention, the digital display device is arranged to illuminate pixels with light pulses of relative durations defined by weightings selected in any combination from the set of weightings 1, 2, 4, 8, 16, 32, 48, 48, 48, 48.

[0025] In a further particular variant according to this fourth aspect of the present invention, the digital display device is arranged to illuminate pixels with light pulses of relative durations defined by weightings selected in any combination from the set of weightings 1, 2, 4, 8, 16, 24, 32, 48, 48, 48, 48.

[0026] Embodiments of the present invention will now be described in more detail, by way of example only and with reference to the accompanying drawings, of which:

Figure 1 shows an example sequence of light pulses in a known method for controlling a digital display device for illuminating adjacent pixels in an image to be displayed;

Figure 2 shows how the effect of 'dynamic false contouring' may arise in using the light pulse sequence shown in Figure 1;

Figure 3 shows an example of a light pulse sequence according to one embodiment of the present invention;

Figure 4 is a table providing example timings and durations for the light pulse sequence shown in Figure 3;

- Figure 5 shows an example of a light pulse sequence according to another embodiment of the present invention; and
 - Figure 6 is a simplified representation, in the form of a functional block diagram, of a digital display system according to an embodiment of the present invention.

[0027] In a typical application of a digital display device 25 for a helmet-mounted display in which various symbols may be displayed overlain on an external scene, it may suffice to display monochrome pixels at any one of 256 different brightness levels from 0 ('off') to 255 (maximum brightness), each brightness level being defined using 30 an 8 bit binary number. In conventional display devices, the 8-bit binary number for each pixel is interpreted, when controlling the display device, as a sequence of up to eight signals to trigger the display device to emit a corresponding sequence of pulses of light each of equal 35 brightness but of a duration proportional to the value of the bit, from least significant bit value 1 (20) triggering the shortest pulse, representing 1/255 of the maximum pixel brightness, to the most significant bit value 128 (27) triggering the longest pulse, representing slightly more than 40 half the maximum pixel brightness, the pixel being illuminated for a given pulse duration or not illuminated according to whether the respective bit is set to a '1' or a '0'. [0028] If different colours are required, the 256 available brightness levels need to be applied separately to 45 control different combinations of red, green and blue light sources to provide an overall colour brightness for each pixel for any of the different colours to be used to generate an image.

[0029] An image refresh period may be divided into what will be called 'sub-fields'. Each sub-field represents a period of time of a predetermined length - the sub-field 'weighting' - during which the display device may be triggered to emit or reflect a discrete pulse of light of duration proportional to the sub-field weighting for any pixel that requires it. The inherent latency of the display device will determine the shortest period of time that may be selected in respect of a sub-field. All weightings will be assumed to represent a proportion of the total duration of illumina-

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tion required to display a pixel of maximum brightness (greatest brightness level) within the image refresh period. In the 8-bit example above, the image refresh period may be divided into eight differently-weighted sub-fields wherein the sub-field weightings are binary values 1, 2, 4, 8, 16, 32, 64, 128 representing different proportions of a maximum brightness value of 255 and hence of the total duration of illumination required to achieve a pixel of brightness 255. The data defining those pixels to be illuminated in a given sub-field are referred to as a 'brightness plane' or 'bit-plane' of data.

[0030] A bit-plane for a given sub-field contains a '1' or a '0' in respect of each pixel position in an image area of the display device requiring illumination with a light pulse of the respective duration or non-illumination in that sub-field. Each of the eight different bit-planes of data is uploaded to a display device memory according to the timing of the respective sub-field.

[0031] In this example, the first bit-plane defines those pixels to be illuminated for the shortest period, a weighting of 1, represented by the lowest significant bit of the 8-bit binary brightness value. The second bit-plane defines those pixels to be illuminated for a period represented by a weighting of 2; the third being for a period represented by a weighting of 4; and so-on until the eighth bit-plane defines those pixels to be illuminated for the longest period, represented by a weighting of 4; and so-on until the eighth bit-plane defines those pixels to be illuminated for the longest period, represented by a weighting of 128. Within a short period of time of receiving a given bit-plane of data - this period being determined by the inherent latency in the display device - the display device illuminates all those pixels having a '1' in a respective position in the bit-plane of data.

[0032] One known problem with such a display driving technique is that of 'dynamic false contouring' whereby the eye is able to integrate one or more light pulses of one pixel with one or more light pulses intended for an adjacent or nearby pixel over an image refresh period due to rapid movement of the eye or at least rapid relative movement of the displayed image and the eye. Even though the changing illumination for a single pixel between sub-fields is not detectable by the eye, a sudden relative movement may cause the eye to perceive a given pixel as being brighter or less bright than intended. The effect is more pronounced when adjacent pixels are illuminated differently in respect of the most heavily-weighted sub-fields. An example of how this arises will now be described with reference to Figure 1 and Figure 2.

[0033] Referring firstly to Figure 1, in the example of an 8-bit brightness level, pixels A and B are to be illuminated at levels 127 and 128 respectively of a possible 256 different brightness levels in the range of 0 ('off') to 255 - the brightest. The brightness levels 127 and 128 should appear to the eye to be very similar over an image refresh period. Figure 1 shows the intended series of light pulses to be displayed for each of pixel A and pixel B to achieve overall perceived brightness levels of 127 and 128 respectively. Pixel A (10) has been illuminated in each of the first seven sub-fields, corresponding to pulses of weightings 1, 2, 4, 8, 16, 32 and 64, to achieve an overall perceived brightness level of 127 over the image refresh period. Pixel B (15) has received no illumination in the first seven sub-fields and is to be illuminated in the eighth sub-field with a pulse of weighting 128 to achieve

the required 128 brightness level over the image refresh period.

[0034] However, referring to Figure 2, a rapid relative movement of the eye and the displayed image causes

the eye to perceive Pixel A (20) as having a 'false addition' of the 128-weighted pulse that illuminated Pixel B (25) and to perceive Pixel B (25) as having its 128-weighted pulse falsely subtracted. As a result, the eye perceives Pixel A (20) with an overall brightness level of 255 and

¹⁵ Pixel B (25) with an overall brightness level of 0, instead of 127 and 128 respectively, creating a flash or sparkle in the vicinity of Pixel A. This is the worst case situation. However, the perception of false additions and subtractions between adjacent pixels may involve lower-weight-20 refer to the there were adjacent pixels and subtractions between adjacent pixels may involve lower-weight-21 refer to the state of the second sec

²⁰ ed sub-fields which, though causing sparkling of reduced intensity is nevertheless capable of degrading the perceived quality of the image.

[0035] According to one example embodiment of the present invention, the number of sub-fields is increased 25 for the purposes of driving the display device, which may for example be a micro-display device as supplied by Forth Dimension Displays Ltd, part number M249 SXGA. In the example of an 8-bit brightness level, this involves the introduction of a conversion step to transform an 8-30 bit representation of brightness in the image data to a 10-bit representation of brightness for the purposes of driving the display device. The present invention exploits the increased number of available sub-fields to generate a different set of available sub-field weightings (pulse du-35 rations) to that used in the conventional eight sub-field scheme described above. The chosen set of sub-field

weightings has been selected to reduce the visual impact of false addition and substitution by reducing the maximum weighting (pulse duration) while increasing the number of the most heavily weighted sub-fields.

[0036] After consideration of a number of possible combinations of sub-field weighting, the inventors have found that a display driving scheme in which conventional binary-weighted subs-fields are combined with non-bi-

45 nary-weighted sub-fields. i.e. a weighting not equal to a power of 2, having a maximum weighting of no more than illumination level '48', as measured on the 8-bit illumination range 0-255, provides a good compromise between the effects of dynamic false contouring and a need to 50 divide the image refresh period into a greater number of sub-fields than the display device is capable of supporting. Two example display driving schemes according to embodiments of the present invention, based upon subfields of maximum weight '48' and using 10 and 11 sub-55 fields, respectively, will now be described beginning with the 10 sub-field scheme and a reference to Figure 3 and to Figure 4.

[0037] Referring to Figure 3 and to Figure 4, an im-

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proved pixel illumination scheme is presented: in Figure 3 as a sequence of possible pixel illumination periods; and in Figure 4 as a table of corresponding sub-field weightings, of respective sub-field time period and of actual illumination periods of light pulses to be emitted within the time period allocated to the sub-field. As can be seen, the '64' and '128' binary-weighted sub-fields of the conventional eight sub-field scheme have been replaced by four non-binary-weighted sub-fields of weight '48', giving a ten sub-field scheme. An additional conversion step is introduced into a typical display driver arrangement to receive conventional 8-bit pixel illumination level data and to convert those data, with reference to a look-up table, into a 10-bit illumination level for use in the display driver to provide the control bits for the ten sub-fields shown in Figure 3 and Figure 4. Digital display devices may be modified to receive ten bit-planes of data within an image refresh period and to illuminate pixels for any combination of the relative durations 1, 2, 4, 8, 16, 32, 48, 48, 48 and 48, providing for overall brightness levels in the range 0 to 255 as for the conventional scheme.

[0038] While, for example, an illumination level of 127 as represented by 01111111 in the conventional eight sub-field scheme may convert to any one of six different representations in such a scheme with non-binary sub-fields of weight '48', it may be desirable to illuminate the pixels as soon as possible during an image refresh period rather than adopting a delay as may be an option when a pixel needs to be illuminated during only one or two of the four available 48-weight sub-fields. Using, for example, a 10-bit binary number to represent the ten sub-field scheme, a brightness level of '127' may in principle convert to any one of:

0011011111 35 0101011111 1001011111 40 0110011111 or 1100011111. 45

[0039] However, it has been found advantageous, but not essential, to complete the illumination of a pixel over as short a period as possible within the image refresh period, i.e. to group any required 48-weight sub-fields as closely as possible with the lower binary-weight subfields. Hence, in generating the look-up table for converting the input 8-bit image data into 10-bit display driver output, the first of those possible representations listed above for the brightness level 127 would be preferred. However, it may be advantageous when displaying certain types of image or in respect of particular regions of an image to adopt one of the alternative combinations of 48-weight sub-fields to achieve a given overall brightness where not all of the 48-weight sub-fields need to be filled **[0040]** It will also be apparent under this scheme that for brightness levels of between 48 and 63, the opportunity exists to use a 48-weight sub-field in combination with respective combinations of the lower binary-weight sub-fields. However, it is also preferred that the first use of a 48-weight sub-field be avoided as far as possible to

make maximum use of the binary-weighted sub-fields
and so reduce the opportunity for the false addition and subtraction of a 48-weight sub-field. By this principle, all brightness levels of between 1 and 63 may be achieved using the six binary-weighted sub-fields: 1, 2, 4, 8, 16 and 32. A brightness level of 64 would then be represent-

¹⁵ ed by the 10-bit value 0001010000 and brightness levels of 64 to 111 would be represented using only the same seven sub-fields, even though brightness values of between 96 and 111 would have the option of using two of the 48-weight sub-fields.

20 [0041] The inventors have realised, further, that it may be advantageous to illuminate pixels using a particular bit-plane order, intermixing the most heavily-weighted sub-fields with the lowest-weight subfields. For example, in one embodiment based upon the ten sub-field scheme

²⁵ shown in Figure 3 and Figure 4, the bit-planes are uploaded to the display device and pixels illuminated in the order:

Bit Plane 9
Bit Plane 0
Bit Plane 8
Bit Plane 1
Bit Plane 7
Bit Plane 2
Bit Plane 6
Bit Plane 3
Bit Plane 5
Bit Plane 4.

[0042] When applying this sequence or any example sequence of bit-planes according to the present invention to the control of a digital display device it may be necessary to take account of certain characteristics of the display device technology both in ordering and in the timing of upload to the display device. For example, when applying the present invention to liquid crystal display (LCD) devices, e.g. liquid crystal on silicon (LCOS) display devices, the requirements of pixel 'charge balancing' need to be taken into account, as would be apparent to a per-

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son of ordinary skill in the field of digital display device technology.

[0043] Referring to Figure 5, in the alternative eleven sub-field scheme mentioned above, according to another example embodiment of the present invention, an additional 24-weight sub-field is inserted between the 16 and 32-weight sub-fields to provide further options for using combinations of lower-weighted sub-fields to achieve a given overall level of pixel brightness. An 11-bit binary number may be used to represent this 11 sub-field scheme as would be apparent to a notional skilled person in this field, adopting the same principles as discussed above in generating a corresponding sequence of bit-planes for uploading to the display driver.

[0044] It will be apparent to a notional skilled person in the field that there are a great many alternative schemes that may be devised, based upon the same or a greater number of bit-planes and including combinations of sub-fields of binary-weight and non-binaryweight, and having a maximum weighting of less than would be used in a conventional scheme. It is generally the case that the lower the maximum weighting, the greater the required number of sub-fields and hence of bitplanes of data that need to be generated and handled by the display device in order to define a given number of luminance levels. All such combinations are intended to fall within the scope of the present invention.

[0045] In another example embodiment of the present invention a digital display system is provided to include an additional processing function arranged with access to a look-up table devised according to the principles above to convert a conventional representation of pixel brightness, e.g. using an 8-bit representation, into a display driver representation based upon 10 or 11 sub-fields and a 10 or 11-bit representation. This system will now be described with reference to Figure 6.

[0046] Referring to Figure 6, a functional block diagram is providing showing a simplified representation of a digital display system 50 in which the functionality described above may be implemented. A digital Display Device 55 is controlled by a Display Driver 60, arranged to control the illumination of pixels and in particular the timing and duration of illumination to be provided to each pixel in order to render components of an image to be displayed over a period of time corresponding to each sub-field of an image refresh period. The Display Driver 60 obtains the data to be used in controlling the Display Device 55 for a sub-field from a Bit-Plane Store 65. The bit-planes of data for each sub-field are uploaded to the Bit-Plane Store 65 by a Pixel-to-Bit-Plane Conversion module 70 which in turn receives as input image data that has been converted from input 8-bit video data 80 to a 10-bit representation defining a sub-field weighting scheme according to an embodiment of the present invention in a Conversion module 75. The Conversion module 75 performs the conversion with reference to the contents of a Look-up Table 85 which contains, for each of the 256 possible 8-bit values of received video data, a 10-bit representation resulting in the same brightness level but based upon the sequence of sub-field weightings for the 10 sub-field scheme described above. An equivalent conversion process may be implemented by the Conversion

⁵ module 75 with reference to a corresponding Look-up Table 85 to achieve the conversion to an 11-bit representation based upon the eleven sub-field embodiment described above with reference to Figure 5.

[0047] Example embodiments of the present invention described above have demonstrated the benefits of driving a digital display in such a way as to reduce the maximum weighting (light pulse duration) and at the same time increase the number of sub-fields in an image refresh period while retaining the capability to generate pix-

¹⁵ els of a required number of distinct pixel brightness levels. The present invention has also demonstrated the benefits in including non-binary-weighted light pulses in combination with binary-weighted light pulses to enable pulse combinations to be selected for driving the display device

that reduce the visual impact of false additions and subtractions of pulses between pixels. Presented with the inventive principles described above, a notional skilled person in the relevant field would be able to select alternative weightings and combinations and to test their rel-

²⁵ ative benefit in improving image quality. All such variations as would be apparent to the notional skilled person are intended to fall within the scope of the present invention.

Claims

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- 1. A method for controlling a digital display device to illuminate a pixel at any one of a plurality of brightness levels, wherein the display device is controllable to generate, for a given pixel and a selected one of said plurality of brightness levels, a predetermined sequence of light pulses within an image refresh period such that the pixel may be perceived as having said selected brightness level during the image refresh period, wherein the predetermined sequence of pulses comprises a predetermined combination of one or more pulses of relative duration defined by weightings selected in any combination from a set of pulse weightings wherein the greatest weighting in the set of weightings represents a pulse duration of less than one half of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of said plurality of brightness levels.
- The method according to claim 1, wherein the set of pulse weightings comprises a plurality of binary weightings and one or more non-binary weightings.
- **3.** The method according to claim 2, wherein the set of pulse weightings comprises a plurality of non-binary weightings.

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- 4. The method according to any one of claims 1 to 3, wherein the highest-value weighting included in the set of pulse weightings represents a pulse of duration no more than one quarter of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of said plurality of brightness levels.
- 5. The method according to claim 4, wherein the weighting of highest value included in the set of pulse weightings is a non-binary weighting and wherein the set of pulse weightings includes two or more non-binary weightings of the same highest value.
- 6. The method according to any one of claims 1 to 5, ¹⁵ wherein each of said plurality of brightness levels is defined in received image data by a binary value representing a set of pulses each of binary-weighted duration and the method further comprises converting brightness levels defined in received image data ²⁰ into brightness levels defined according to respective combinations of light pulses having relative durations defined by weightings selected in any combination from the set of pulse weightings.
- 7. The method according to claim 6, wherein each of said plurality of brightness levels are defined in received image data by a binary value representing a set of pulses each of binary-weighted relative duration and the method further comprises converting a received binary value into a binary value representing a predetermined combination of light pulses of relative durations defined by weightings selected in any combination from the set of pulse weightings, the method further comprising using the results of the conversion to control the digital display device.
- 8. The method according to claim 7, wherein the received image data comprise brightness levels rep-40 resented by 8-bit binary values in the range 0 to 255, each representing a combination of pulses of binary weighted relative durations selected according to binary pulse weightings 1, 2, 4, 8, 16, 32, 64 and 128 and the method further comprises converting a brightness level defined by a received 8-bit binary 45 value into a binary value representing a predetermined combination of light pulses of relative durations defined by weightings selected in any combination from a set of binary and non-binary pulse weightings having a maximum weighting of less than 50 128 and controlling the digital display device to illuminate pixels using said predetermined combinations of light pulses resulting from the conversion.
- 9. The method according to any one of the preceding claims, further comprising generating and outputting a sequence of outputs for storage in an image buffer associated with the display device, each output com-

prising, for a pulse of a duration defined according to a weighting in the set of pulse weightings, indications of those pixels to be illuminated with the pulse of said defined duration.

- The method according to claim 9, wherein said sequence of outputs represents pulses of higher weighting intermixed with pulses of lower weighting.
- **11.** A digital display system, comprising:

a digital display device for displaying an image; and

a display controller arranged to control the digital display device to display pixels in an image each at a required level of brightness,

wherein the display controller comprises:

an input for receiving image data defining, for each of one or more pixels in an image to be displayed or updated, a brightness level selected from a predetermined plurality of brightness levels;

- a processor arranged to receive image data from the input and to transform a brightness level indicated for a pixel in the received image data into a representation defining a predetermined combination of pulses of relative durations defined by weightings selected in any combination from a predetermined set of pulse weightings wherein the highest-value weighting in the set of pulse weightings represents a pulse of duration less than one half of the total duration of pixel illumination required within an image refresh period to achieve illumination of the pixel at the greatest of said plurality of brightness levels and to generate a sequence of outputs for storage in an image buffer associated with the display device, each output comprising, for a pulse of a duration defined according to a weighting in the set of pulse weightings, indications of those pixels to be illuminated with the pulse of said defined duration; and means for controlling the display device to illuminate those pixels indicated by the contents of the image buffer in a sequence and for a defined duration as determined by said sequence of stored outputs.
- 12. The system according to claim 11, wherein the predetermined set of pulse weightings comprises a plurality of binary weightings and one or more of nonbinary weightings.
- **13.** The system according to claims 11 or claim 12, wherein each of said plurality of brightness levels is

defined in received image data by a binary value representing a set of pulses each of binary-weighted duration and the processor is arranged to convert brightness levels defined in received image data into brightness levels defined according to respective combinations of light pulses having relative durations defined by weightings selected in any combination from the set of pulse weightings

- 14. The system according to claim 13, wherein each of said plurality of brightness levels are defined in received image data by a binary value representing a set of pulses each of binary-weighted relative duration and the processor is arranged to convert a received binary value into a binary value representing a predetermined combination of light pulses of relative durations defined by weightings selected in any combination from the set of pulse weightings and to use the results of the conversion to generate said sequence of outputs.
- 15. The system according to any one of claims 11 to 14, wherein said sequence of outputs represent pulses of higher weighting intermixed with pulses of lower weighting.

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Bit Plane Number	Nominal Weighting	Period (us)	Illumination Period (us)
Bit Plane 0	Ļ	60.667	9.333
Bit Plane 1	2	70.000	18.667
Bit Plane 2	4	88.667	37.333
Bit Plane 3	8	126.000	74.667
Bit Plane 4	16	200.667	149.333
Bit Plane 5	32	350.000	298.667
Bit Plane 6	48	499.333	448.000
Bit Plane 7	48	499.333	448.000
Bit Plane 8	48	499.333	448.000
Bit Plane 9	48	499.333	448.000

Fig. 4









EUROPEAN SEARCH REPORT

Application Number EP 15 27 5090

		DOCUMENTS CONSID				
	Category	Citation of document with ir of relevant passa	ndication, where appropriate, ages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (IPC)	
10 15	X	US 6 100 939 A (KOU AL) 8 August 2000 (* column 1, line 24 * column 11, line 3 * column 14, line 3 * column 15, line 5 * figures 1,2,13 *	GAMI AKIHIKO [JP] ET 2000-08-08) - line 30 * 2 - line 38 * 0 - line 49 * 0 - line 56 *	1-15	INV. G09G3/20 G09G3/36	
20	х	US 6 674 429 B1 (C0 6 January 2004 (200 * column 1, line 18 * column 3, line 14 * figures 1,2 *	RREA CARLOS [DE] ET AL) 4-01-06) 5 - line 23 * 6 - column 6, line 8 *	1-9, 11-14		
25	A	US 2004/070590 A1 (AL) 15 April 2004 (* paragraph [0061]	LEE HO-YOUNG [KR] ET 2004-04-15) *	1-15		
30					TECHNICAL FIELDS SEARCHED (IPC) G09G	
35						
40						
45						
1		The present search report has b	been drawn up for all claims			
50 T	Place of search		Date of completion of the search		Examiner	
14CO1		The Hague	10 June 2015	Lad	diray, Olivier	
24) 52 55 55	C, X : part Y : part docu A : tech O : non	ATEGORY OF CITED DOCUMENTS icularly relevant if taken alone icularly relevant if combined with anoth ument of the same category inological background -written disclosure	T : theory or principle E : earlier patent doc after the filing date D : document oited in L : document oited fo 	T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document oited in the application L : document cited for other reasons		
P P	P : inte	rmediate document	document	,		

EP 3 073 477 A1

ANNEX TO THE EUROPEAN SEARCH REPORT ON EUROPEAN PATENT APPLICATION NO.

EP 15 27 5090

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This annex lists the patent family members relating to the patent documents cited in the above-mentioned European search report. The members are as contained in the European Patent Office EDP file on The European Patent Office is in no way liable for these particulars which are merely given for the purpose of information.

10-06-2015	
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10	Patent o cited in se	document earch report	Publication date		Patent family member(s)		Publication date
15	US 610	0939 A	08-08-20	000 CA CN KR US US	2185592 1158048 100454786 6100939 6333766	A1 A B1 A B1	21-03-1997 27-08-1997 13-01-2005 08-08-2000 25-12-2001
20 25	US 6674	4429 B	1 06-01-20	004 AT AU CN DE DK EP ES JP JP US	343193 2109600 1338093 60031371 1149374 1026655 1149374 2274776 4497728 2002536689 6674429	T A A T2 T3 A1 A1 T3 B2 A B1	15-11-2006 25-08-2000 27-02-2002 29-03-2007 19-02-2007 09-08-2000 31-10-2001 01-06-2007 07-07-2010 29-10-2002 06-01-2004
30	US 2004	4070590 A	1 15-04-20	WO DO4 DE EP JP JP	0046782 60304649 1408683 4101147 2004133467	A1 T2 A2 B2 A	10-08-2000 05-04-2007 14-04-2004 18-06-2008 30-04-2004
35				KR US	20040032383 2004070590	A A1 	17-04-2004 15-04-2004
40							
45							
50 g							
55 55 Odd	For more details ab	out this annex : se	Official Journal of th	e European P	atent Office, No. 12/8	2	