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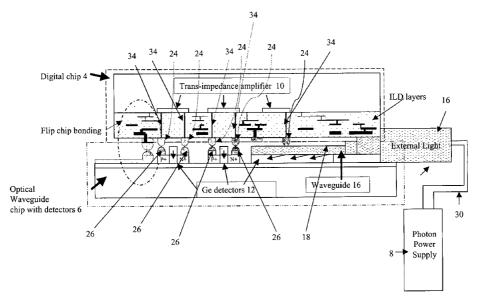
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(54) Title: HYBRID INTEGRATION OF ELECTRICAL AND OPTICAL CHIPS



(57) Abstract: A system of integrating electronic and optical functions includes an electronic chip that also includes monolithic IC electronics for performing electronic functions. Furthermore, the electronic chip includes a plurality of solder bumps for receiving input for processing. An optical chip receives an optical signal and performs optical functions on the signal, and outputs electrical signals indicative of the optical functions performed on the signal. Moreover, the optical chip includes a plurality of solder bumps for sending the electrical signals as input to the electronic chip. The solder bumps of the electronic chip and optical chip are bonded to together so that a hybrid integration is formed that separates the yield of the optical and electrical circuits forming the electronic and optical chip.



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#### INTERNATIONAL SEARCH REPORT

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# A. CLASSIFICATION OF SUBJECT MATTER IPC 7 H01L25/16

According to International Patent Classification (IPC) or to both national classification and IPC

#### B. FIELDS SEARCHED

 $\begin{array}{ccc} \mbox{Minimum documentation searched (classification system followed by classification symbols)} \\ \mbox{IPC } 7 & \mbox{H01L} \end{array}$ 

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

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