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8 1662 - 9

109 303

2 9 960 1401

(74)
:

(54)

가,

5b

1a

1b

2

3a, 3b

/PCB

4a, 4b, 4c, 4d, 4e, 4f
/PCB

5a

/PCB

5b, 5c, 5d

/PCB

6a

/PCB

6b, 6c, 6d, 6e, 6f, 6g
/PCB

7

8 7

e) (Control Hold Stat

MT - 2000 가 가 IMT - 2000 가 I
가 가

(Traffic Channel) (Dedicated Control Channel)

가 가 7 (Packet

Null State), (Initialization State), (Active State), (Control Hold State), (Suspended State), (Dormant State), (Reconnect State) (Service Option)

CDMA

가 가 가 가

PCB (Control Hold State) PCB (Normal Substate) PCB (Control Hold State) 8 2 (Slotted Substate)

/PCB 가

(Resynchronization) /PCB

/PCB 가

1a

(F - CCCH: Forward Common Control CHannel), (F - DCCH: Forward Dedicated Control CHannel) (F - DTCH: Forward Dedicated Traffic CHannel) (Sharable F - DCCH) (F - FCH: Forward Fundamental CHannel) 가 (F - SCH: Forward Supplementary CHannel)

1a , (demux) 120, 122, 124, 126
 I Q 120, 122, 124, 126
 120, 122, 124, 126 (signal mapping) 가
 110, 130, 131, 132, 133, 134, 135, 136 137
 140,
 141, 142, 143, 144, 145, 146 147

2 가 I Q 150, 152 가 가 150 15
 가 150 152 (complex multiplier) 160 가
 PN 160 I Q 170 171
 가 172 173
 174 175
 180 I Q

2가 (가,) 가 , ()
 가 가 1a

1b ,
 (PCB: Power Control Bit)가 /PCB ,
 (R - DCCH: Reverse Dedicated Control CHannel) ,
 (R - DTCH: Reverse Dedicated Traffic CHannel)
 (R - FCH: Reverse Fundamental CHannel) 가 (R - SCH: Reverse Supplemental C
 Hannel)

210 . 220, 230, 240, 250
 260
 222, 242, 252 262 . /PCB
 가 224 254 가 224, 254
 가
 160 160 I Q PN 170 171
 가 172 173
 174 175
 180 I Q

가 . /PCB
 가

가

3a, 3b	300	/PCB	/PCB	(C
losed Loop Power Control) (Outer Loop Power Control)		1		/PCB 가
4a ess Control)	400 (dmch: dedicated mac channel)	5 msec	가 5msec R - DCCH가 가 dmch	MAC (Medium Acc R - DCCH
가 5msec	4 1/2	R - DCCH 2.5 msec		R - DCCH
op Power Control) oop Power Control)		1	/PCB	(Closed Lo (Outer L
4c R - DCCH가	410	dmch	dmch	1.25msec 0.625msec
1	/PCB		(Closed Loop Power Control) (Outer Loop Power Control)	
5a	500, 510	/PCB	/PCB	R - DCCH가 (Closed Loop Power Control) (Outer Loop Power Control)
가		1		
6a	612 R - DCCH가 20 msec 600, 610	5 msec	R - DCCH가	가
가	/PCB		(Closed Loop Power Control) (Outer Loop Power Control)	
1	/PCB			
			/PCB	가

가

가 가

가,

1.25 msec , 20 msec , 5 msec 16 가

가

) 3가 (가,) 1a 가,) 2가 (가,) 3가 (가,) 2가 (가,)

3가 (가,) 2

가

" 0"

" +1"

가 (+)

가

가, (-)

3가 가

가 " -1" , " 0" " 0"

가 0 2

가

(controller)

2

/

2

1a

142, 143 144, 145

가

(Gated Transmission Controller) 190

192, 193, 194, 195

(Gated Transmission Controller) 190

/

190

192, 193, 194, 195

/

가

가

/PCB

/PCB

1b

4

6

가

1b

172

173

172 173

/PCB

가

3a

320, 340, 360

/

/PCB

320 / PCB
 $2 = (1/2) \times (E_c/N_0)_{\text{required}}$
 $1 = (E_c/N_0)_{\text{required}}$
 (20msec) 2 16 8
 $2 (< 1)$
 $2 = 1/$

340 / PCB
 $2/2 = 1/4 = (1/4) \times (E_c/N_0)_{\text{required}}$
 $1 = (E_c/N_0)_{\text{required}}$
 (20msec) 16 4
 $3 (< 2 < 3 =$

360 / PCB
 $4 = 3/2 = 2/4 = 1/8 = (1/8) \times (E_c/N_0)_{\text{required}}$
 $1 = (E_c/N_0)_{\text{required}}$
 (20msec) 16 2
 $4 (< 3 <$

3b 330, 350, 370

/ PCB

3a

3dB

가 1.5 dB

가 1dB

2

3

3b

15

가 1.5 dB

3d 330

/

1 2 (2 3 , 3 4)

/ PCB

3b 350

/

1 3 (2 4)

/ PCB

3b 370

/

1 4 / PCB

4a, 4b R - DCCH가 4a 400

R - DCCH

5 msec

20 msec

340, 360

/

4a

420, 4b 440, 460

3a 320,

CH R - DCCH

. dmch

R - DCCH

R - DC

ec 2.5 msec

5 ms

4a 420 R - DCCH가 1/2	/	2	2	3a 320	dmch ()가
p 가	/PCB	p	R - DCCH(R - DCCH	
4a 440 R - DCCH가 1/4	/	4	3 4	3a 340	dmch p 가
p	/PCB	R - DCCH가	R - DCCH		
4a 460 R - DCCH가 1/8	/	8	4 8	3a 360	dmch p 가
p	/PCB	R - DCCH가	R - DCCH		
4c, 4d, 4e, 4f 16	R - DCCH가 가	1.25 msec	4c, 4d R - DCCH가	20 msec	
$1 [dB] - x [dB]$ (x=1,2,3,4) 0.625 msec P	/PCB	가	가	1.25 msec	
4c 430 R - DCCH가 , 5	/	2	6	4c 410 1/2	dmch 가
dmch가 R - DCCH가 가	/PCB	R - DCCH가	R - DCCH	4c 410	
$1 [dB] - 2 [dB]$ P					
4d 450 R - DCCH가 , 5	/	3	6	4c 410 1/4	dmch 가
dmch가 R - DCCH가 가	/PCB	R - DCCH가	R - DCCH	4c 410	
$1 [dB] - 3 [dB]$ P					
4d 470 R - DCCH가 , 5	/	4	6	4c 410 1/8	dmch 가
dmch가 R - DCCH가 가	/PCB	R - DCCH가	R - DCCH	4c 410	
$1 [dB] - 4 [dB]$ P					

4e, 4f	R - DCCH가		$F(1)$		$B(1)$
	가		$\frac{1}{PCB}$	가	
			$1 [dB] - x [dB] (x=2,3,4)$		$F B$
	가		$\frac{1}{PCB}$	가	
$(1+F) \times 1.25 \text{ msec}$			가	dmch	R - DCCH
			$(1+F) \times 0.625 \text{ msec}$		R - DCCH
			P		
4e 431	R - DCCH가	/		2	dmch
1/2	, 5				4e 410
	R - DCCH가		dmch가	, 7	R - DCCH가
가	가		/ 1		/PCB
0			$1 [dB] - 2 [dB]$		R - DCCH
			P		4e 41
4f 451	R - DCCH가	/		3	dmch
1/4	, 5				4e 410
	R - DCCH가		dmch가	, 7	R - DCCH가
가	가		/ 1		/PCB
0			$1 [dB] - 3 [dB]$		R - DCCH
			P		4e 41
4f 471	R - DCCH가	/		4	dmch
1/8	, 5				4e 410
	R - DCCH가		dmch가	, 7	R - DCCH가
가	가		/ 1		/PCB
0			$1 [dB] - 4 [dB]$		R - DCCH
			P		4e 41
5b, 5c, 5d		/			
5b		/		$2 (< 1)$	R - DCCH가
				가	
5a	1/2				5a
1/2					1/2
	5b				
5c		/		$3 (< 2 < 1)$	R - DCCH가
				가	
5a	1/4	1/4			5a
1/4		5c			

5d / $4 (< 3 < 2 < 1)$ R - DCCH가
 가

5a 1/8
 5a 1/8 5d 5a

6b, 6c, 6d, 6e, 6f, 6g /

6b, 6c, 6d /
 /PCB $1 [dB] - x [dB] (x=2,3,4)$ 가

6b / $2 (< 1)$ R - DCCH가
 가

6a 1/2 6a 6a 1/2
 1/2 6b

622 636
 R - DCCH가 /PCB R - DCCH가 /PCB
 R - DCCH /PCB 가

634 632 R - DCCH /PCB $1 [dB] - 2 [dB]$ 636
 가 612 R - DCCH 4 P 6a 632 R - DCC
 H가 /PCB 가 /PCB 628

2 R - DCCH R - DCCH R - DCCH R -
 DCCH가 /PCB 가 R - DCCH R -
 가 가 가 가

6c / $3 (< 2 < 1)$ R - DCCH가
 가

6a 1/4
 6a 1/4
 1/4 6c 6a

656 /PCB 642 R - DCCH가
 R - DCCH가 /PCB
 /PCB R - DCCH 가
 R - DCCH 가 /PCB

656 654 /PCB $1 [dB] - 3 [dB]$
 가 652 R - DCCH 4 P 6a 652
 612 R - DCCH R - DCCH가 /PCB
 R - DCCH가 644 1 R - DCCH
 R - DCCH가 /PCB 가 R - DCCH
 가 1 $3 (< 1)$
 가 가

6d / $4 (< 3 < 2 < 1)$ R - DCCH가
 가

6a 1/8
 6a 1/8
 1/8 6d 6a

676 /PCB 662 R - DCCH가
 R - DCCH가 /PCB
 /PCB R - DCCH 가
 R - DCCH 가 /PCB

676 674 /PCB $1 [dB] - 4 [dB]$
 가 672 R - DCCH 4 P 6a 672
 612 R - DCCH R - DCCH가 /PCB
 (6d) R - DCCH
 R - DCCH가 /PCB 가 R - DCCH
 가 1 $4 (< 1)$
 가 가

6e, 6f, 6g / R - DCCH가 F(
 1) B(1) /PCB
 $1 [dB] - x [dB] (x=2,3,4)$ 가 6e, 6f,
 6g F=1, B=1

6e / $2 (< 1)$ R - DCCH가
 가

6a 1/2 6a 6a 1/2
 1/2 6e

9 637 623 /PCB R - DCCH가 /PCB
 R - DCCH가 /PCB R - DCCH /PCB
 가 R - DCCH 가

8(=9 - F=9 - 1) 7
 가 635 /PCB $1 [dB] - 2 [dB]$

623 7 623 7 $1 [dB] - 2 [$
 dB] 가 (a x b x c = a x c x b) 633 R - DCCH
 4 P 6a 612 R - DCCH 가
 /PCB 633 R - DCCH /PCB
 가 R - DCCH R - DCCH가
 1 $2 (< 1)$ 가 가

6f / 3 (< 2 < 1) R - DCCH가
 가

6a 6a 1/4 .
 6a 1/4 . 6f

9 657 643 /PCB
 R - DCCH가 R - DCCH가 R - D /PCB
 /PCB /PCB /PCB

CCH 가 R - DCCH 가 R - D /PCB

8 (= 9 - F = 9 - 1) 635 /PCB 7 1
 [dB] - 3 [dB] 가 . 9 7

643 7 623 . (a x b x c = a x c x b) 653
 1 [dB] - 3 [dB] 가 . 6a 612 R - DCCH
 R - DCCH 4 P 653 R - DCCH
 가 /PCB 645 1 /PCB

가 , R - DCCH R - DCCH가 /PCB
 1 3 (< 1) 가 , 가

6g / 4 (< 3 < 2 < 1) R - DCCH가
 가

6a 1/8
 6a 1/8 6f 6a

9 677 663 /PCB R - D /PCB
 CCH가 R - DCCH가 R - DCCH /PCB
 가 R - DCCH 가 R - DCCH /PCB

7 8(=9 - F=9 - 1) 675 /PCB 7 1[dB] - 3[dB]
 가 9 7

4 [dB] 663 7 663 1 [dB] -
 가 (a x b x c = a x c x b) 673 R - DCCH
 4 6a 612 R - DCCH 가
 /PCB P 673 R - DCCH (6g) /PCB
 가 R - DCCH R - DCCH가
 1 4 (< 1) 가 가

가 /PCB /PCB 가
 가 /PCB 가
 가 /PCB 가
 가 /PCB 가
 가 /PCB 가
 가 /PCB 가
 가 /PCB 가
 가 /PCB 가

(57)

1.

2.
1 ,

3.

4.
3 ,

5.
3 ,

6.
3 ,

가

7.
6 ,

가

8.

6 ,

,

9.

6 ,

,

, 가

10.

3 ,

,

가

11.

,

12.

13.

12 ,

,

14.

,

,

15.

14 ,

16.

14 ,

17.

14 ,

가

18.

17 ,

가

19.

17 ,

20.

17 ,

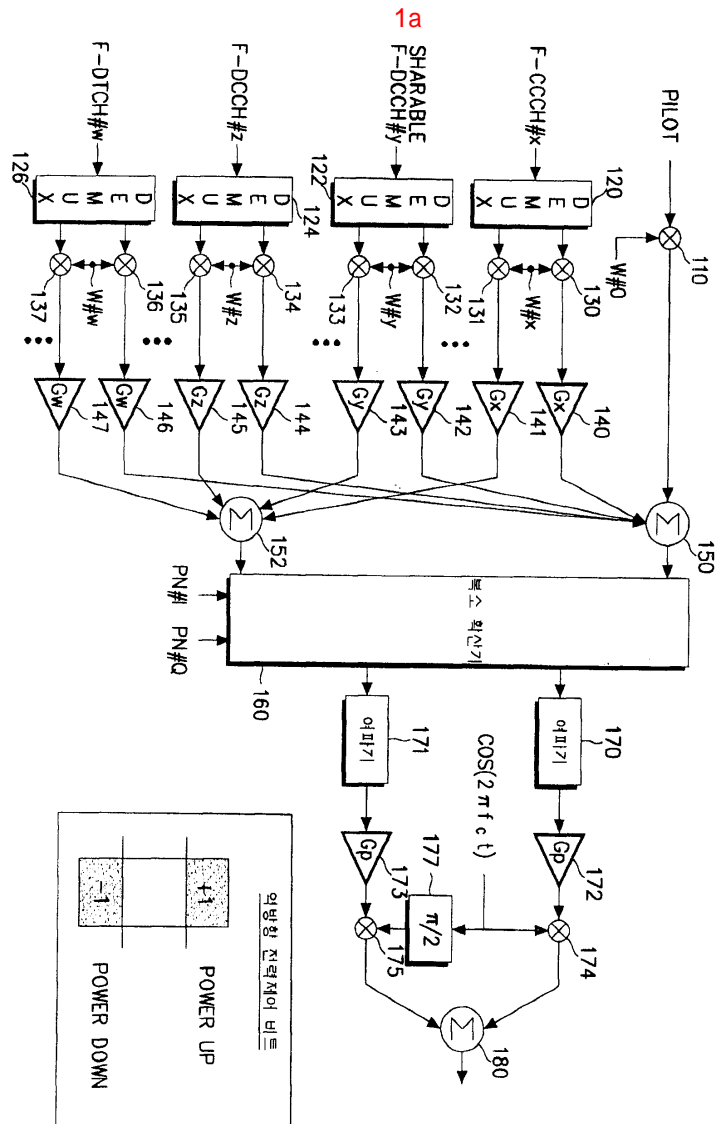
가

21.

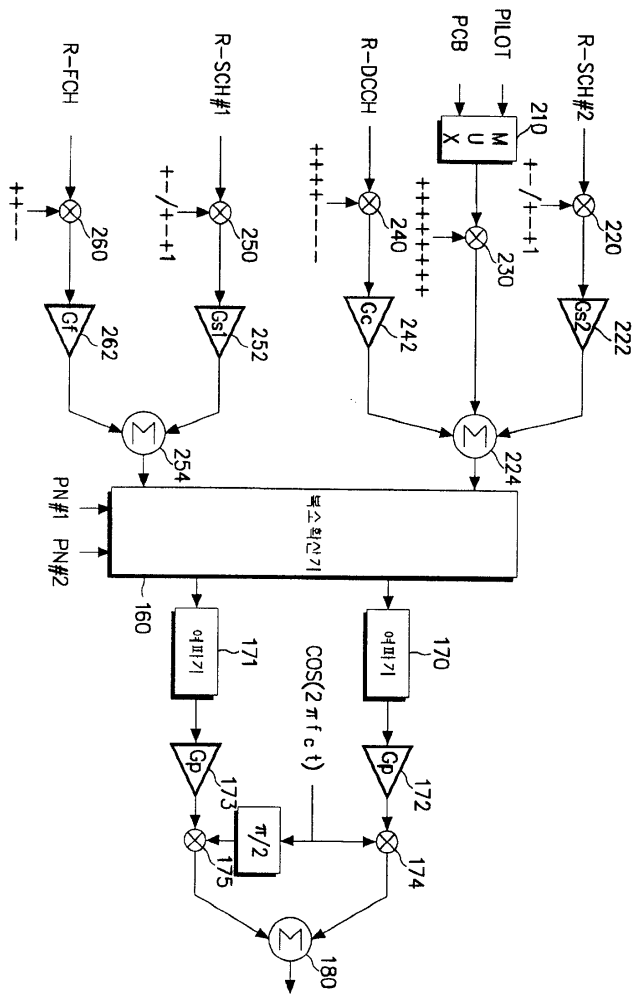
14 ,

가

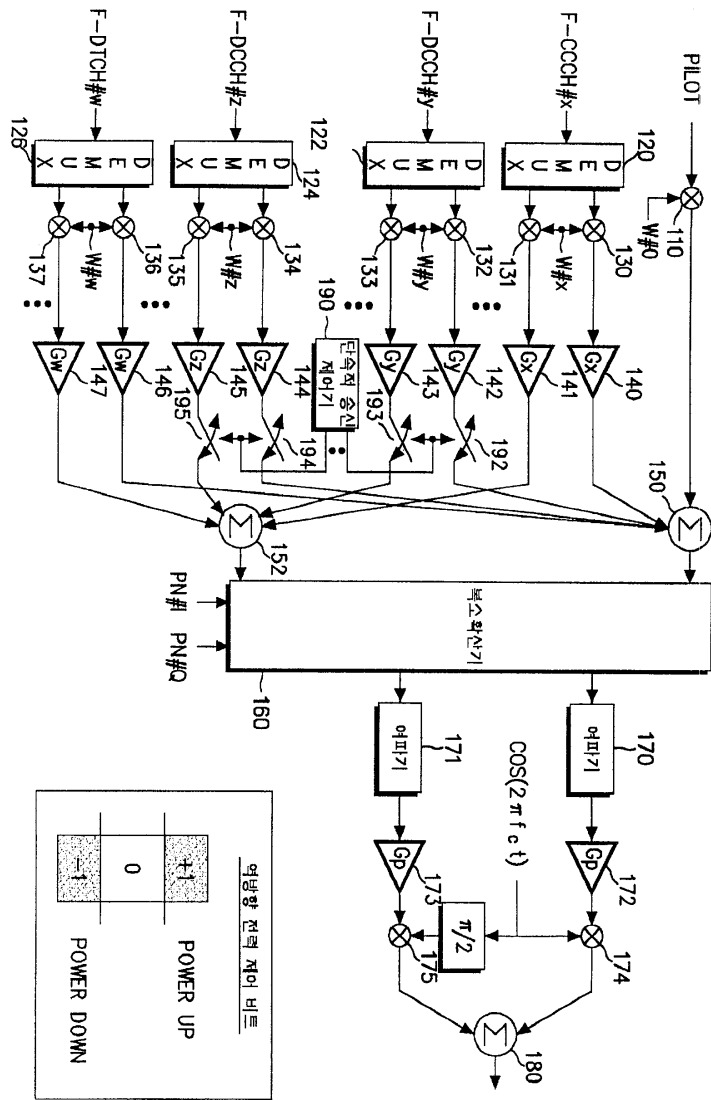
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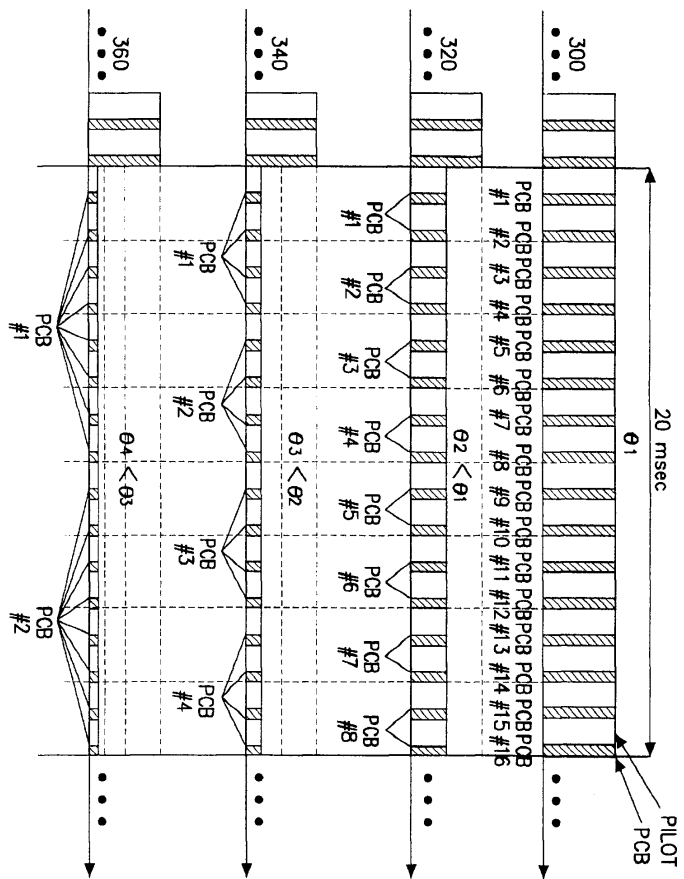
1b



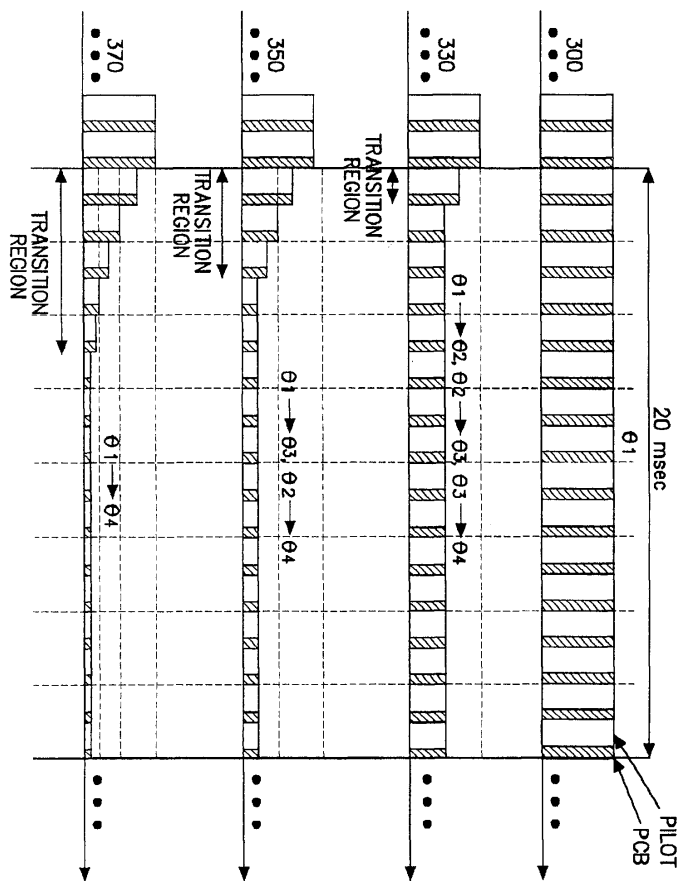
2



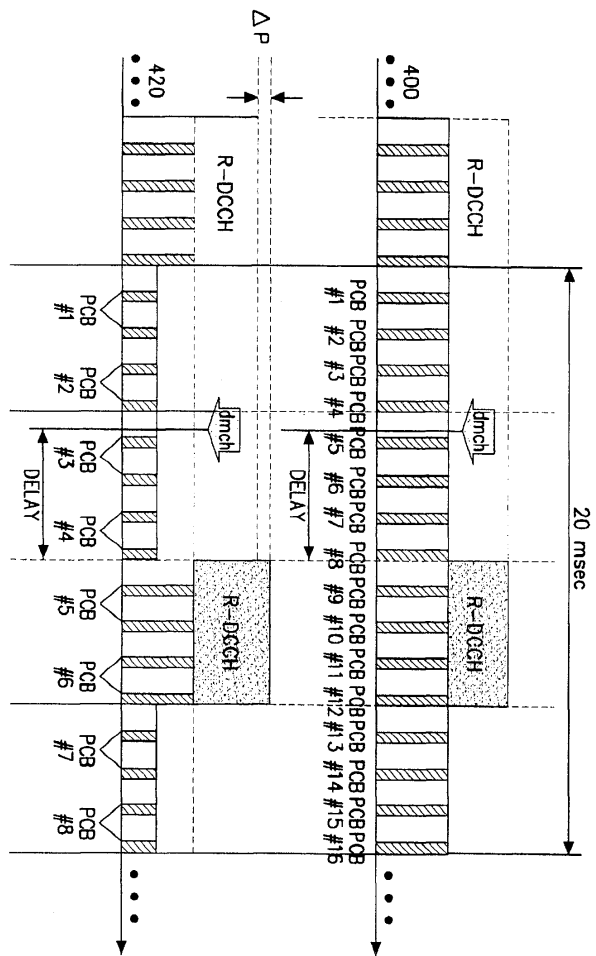
3a



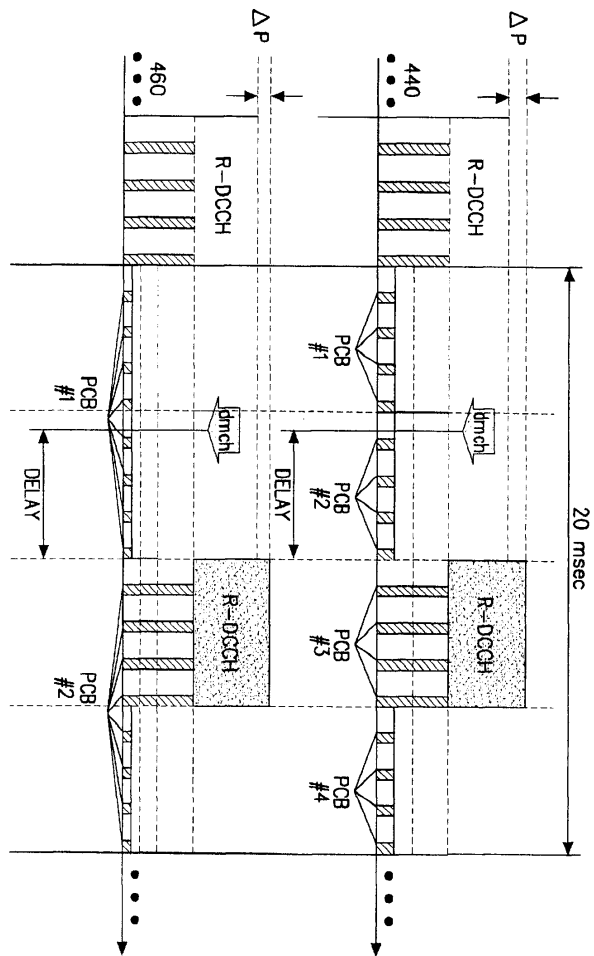
3b



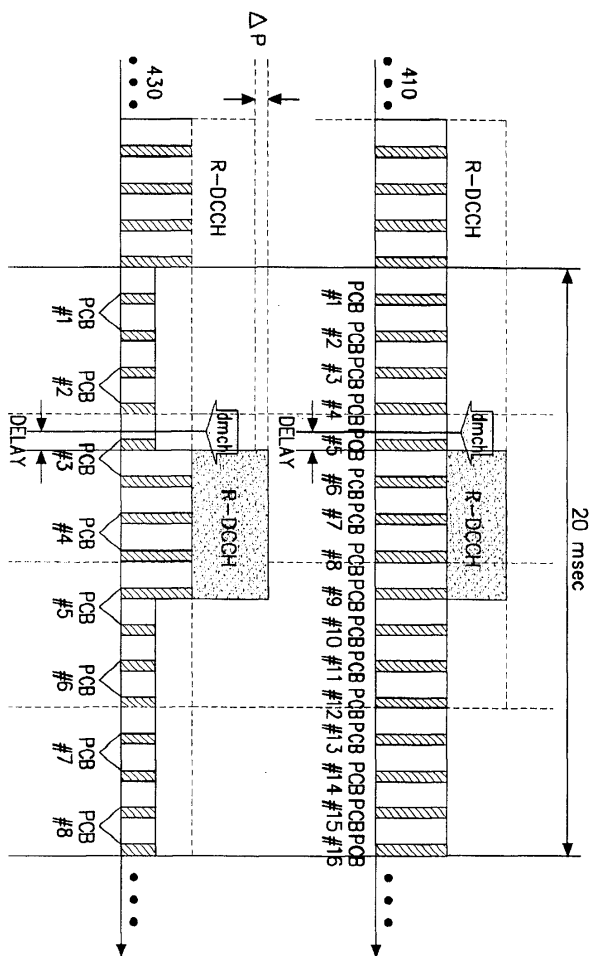
4a



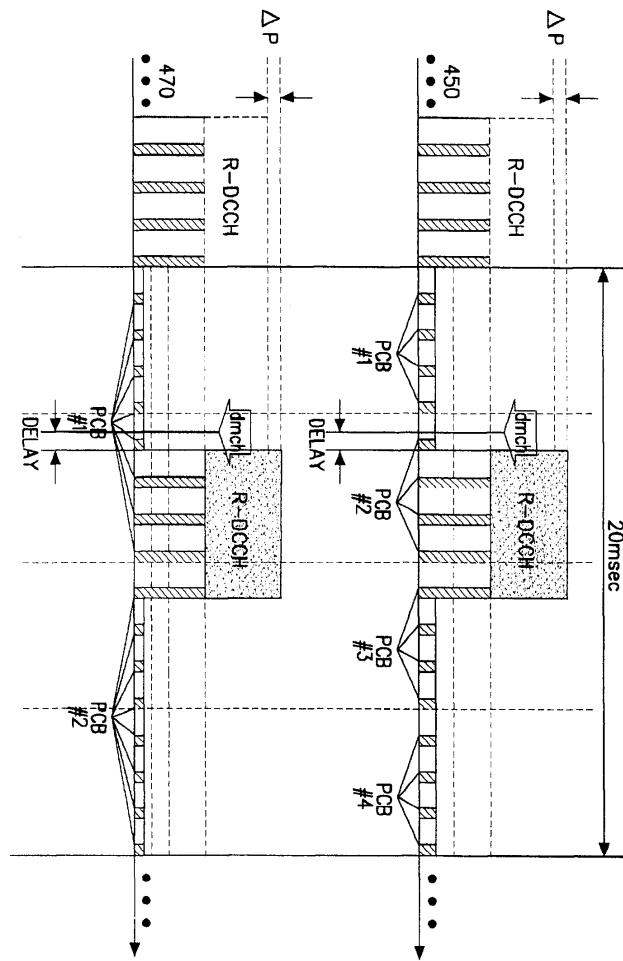
4b



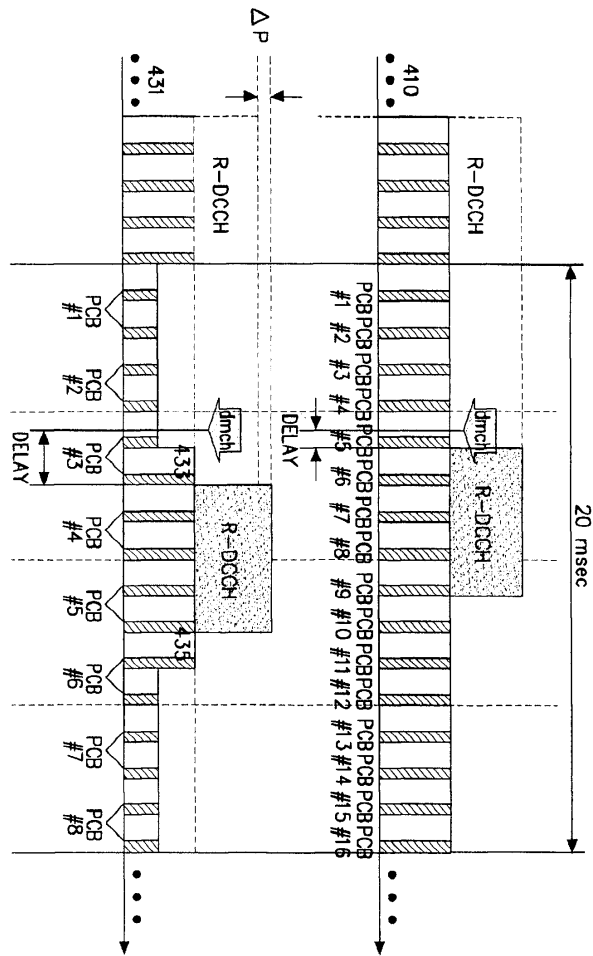
4c



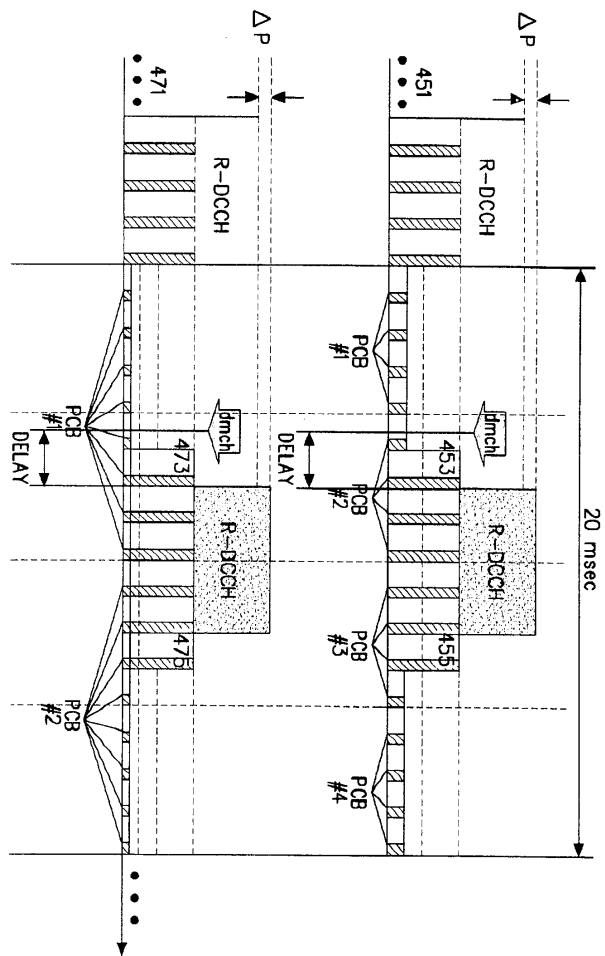
4d



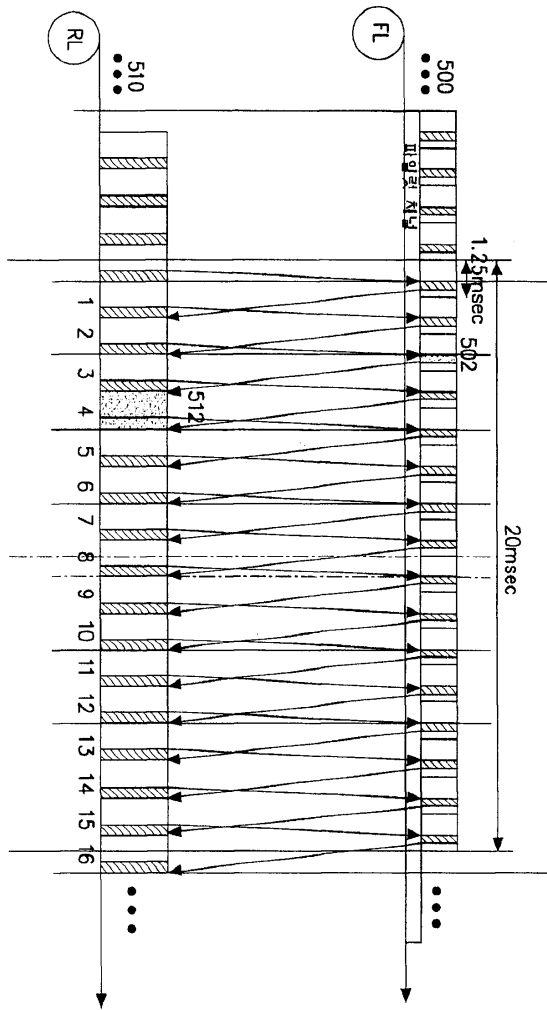
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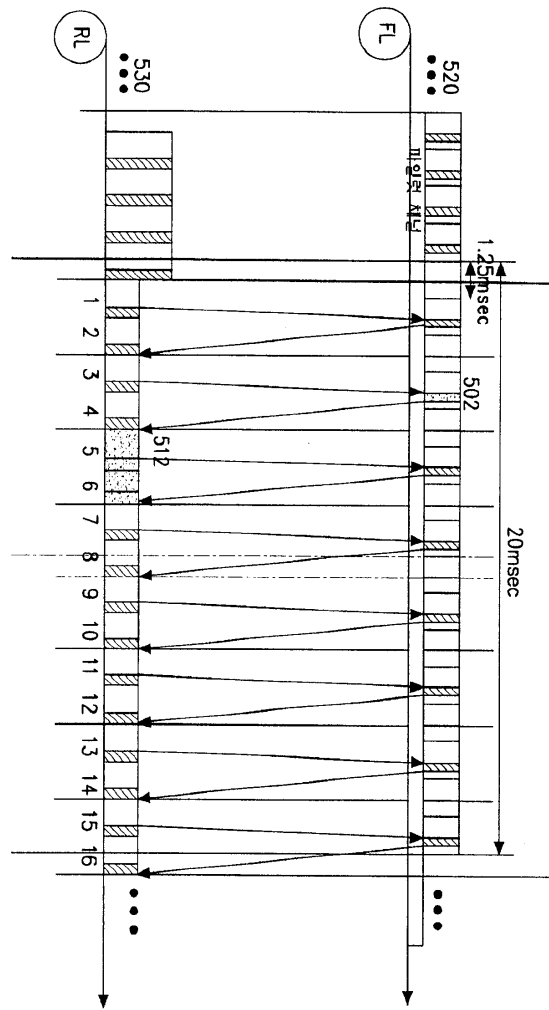
4f



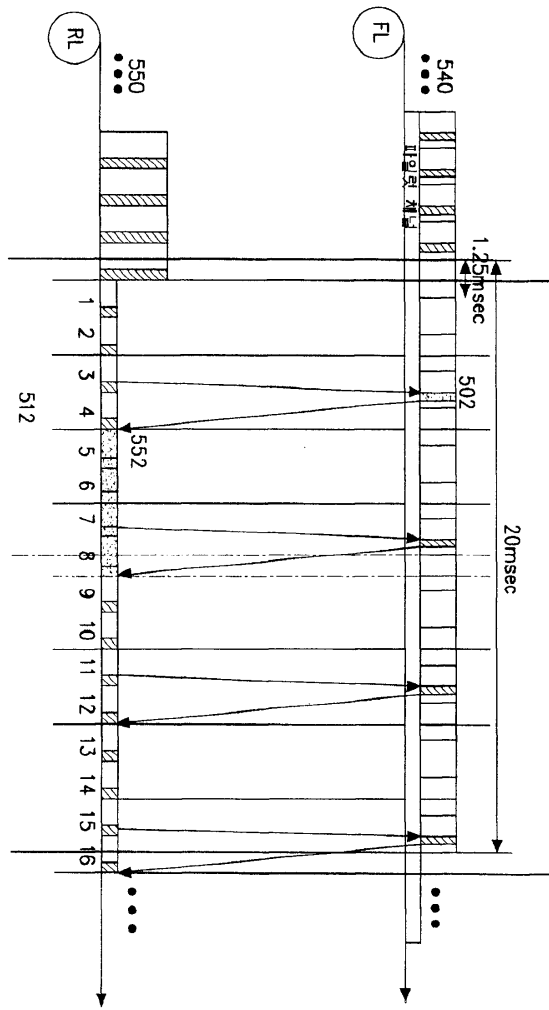
5a



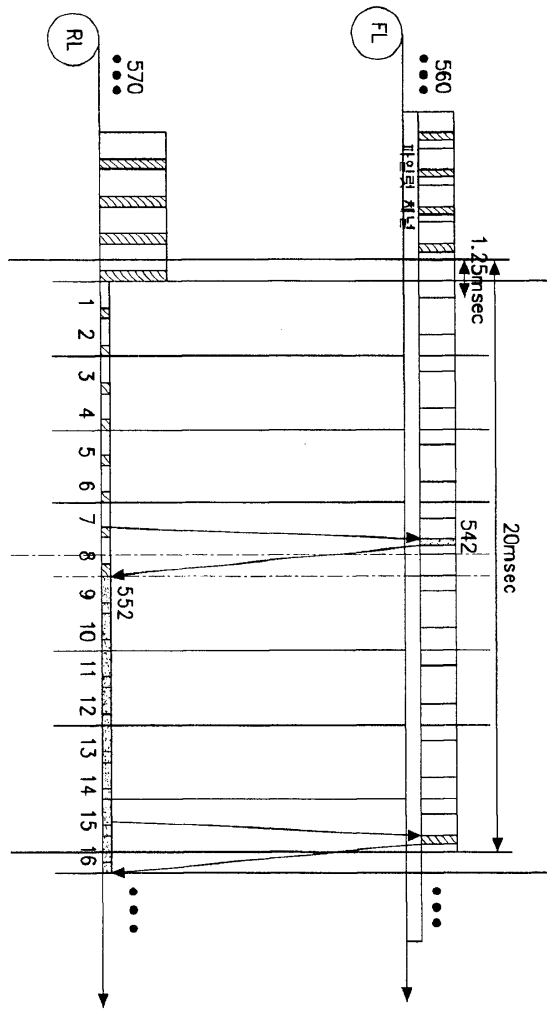
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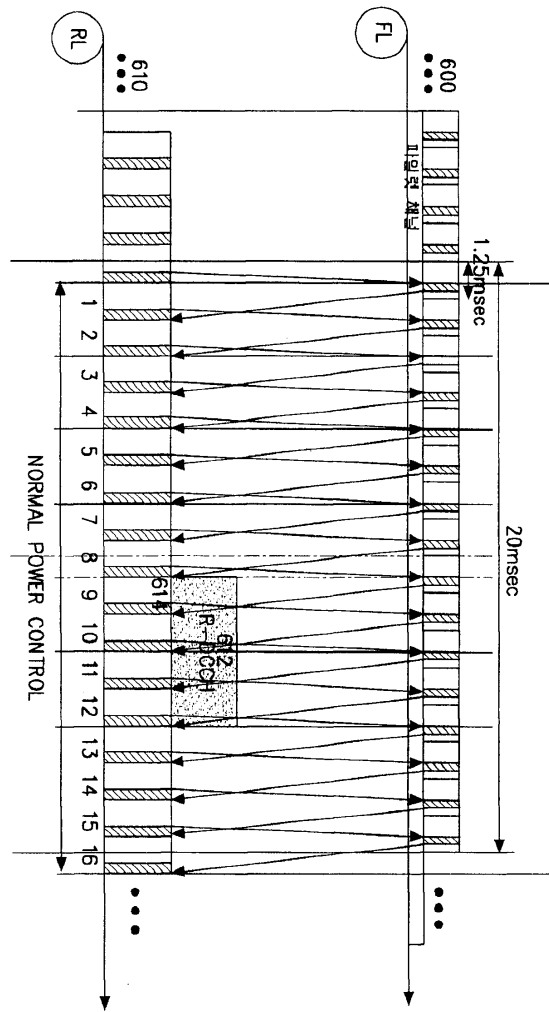
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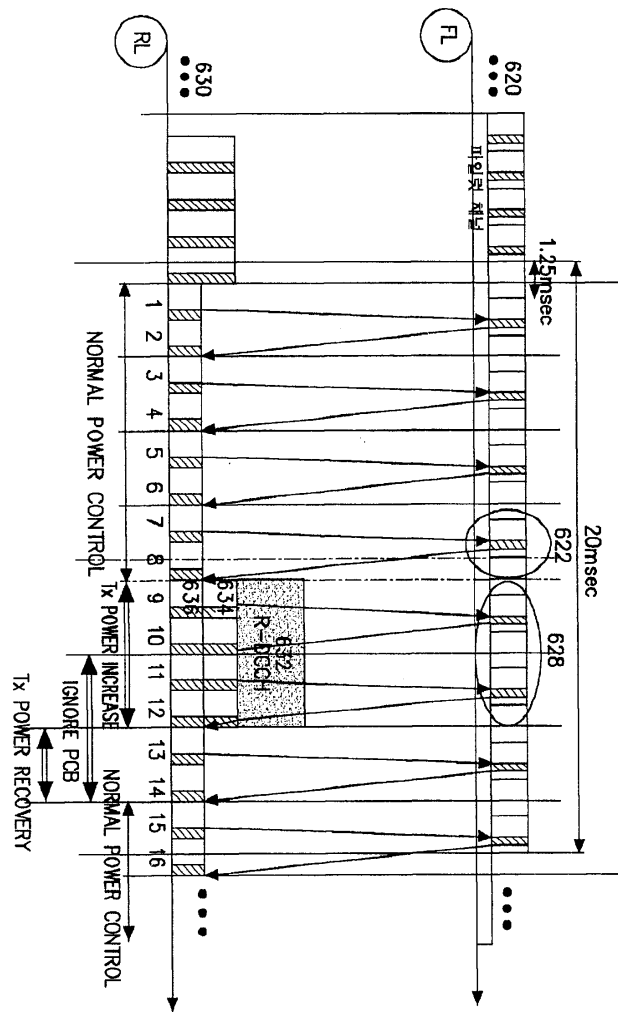
5d



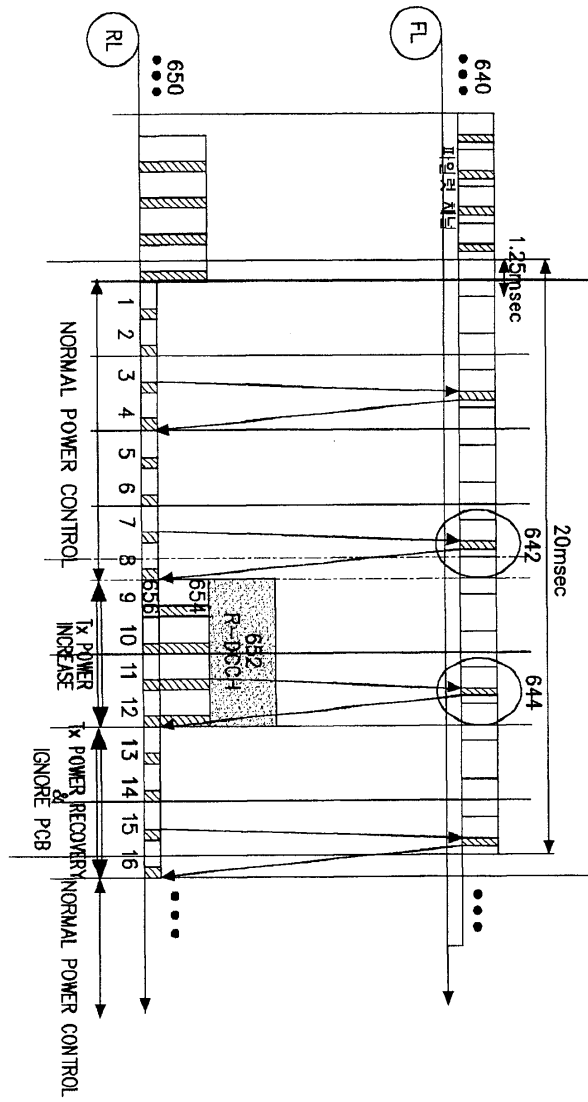
6a



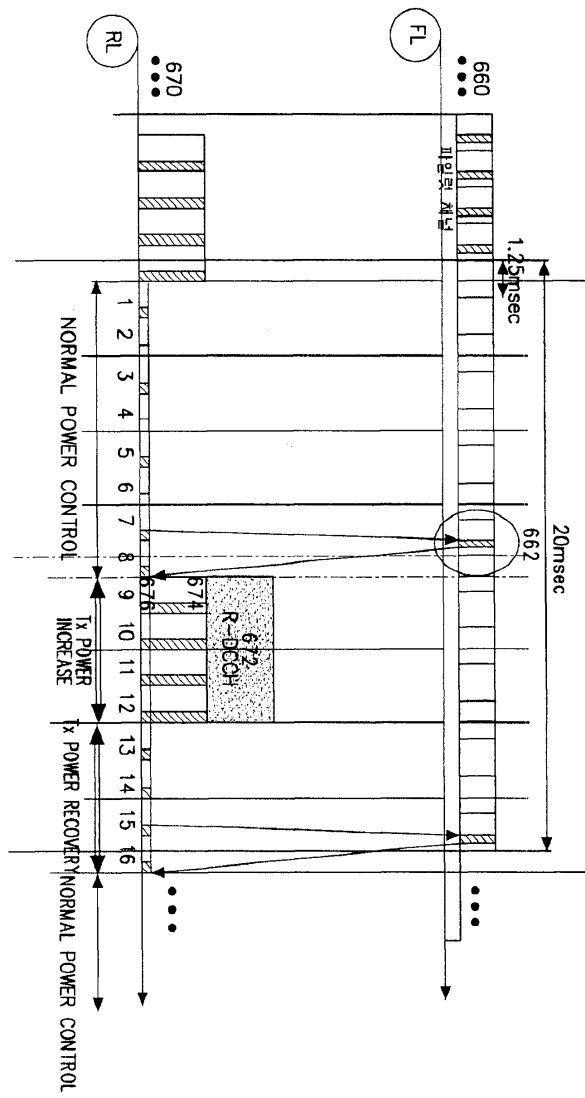
6b



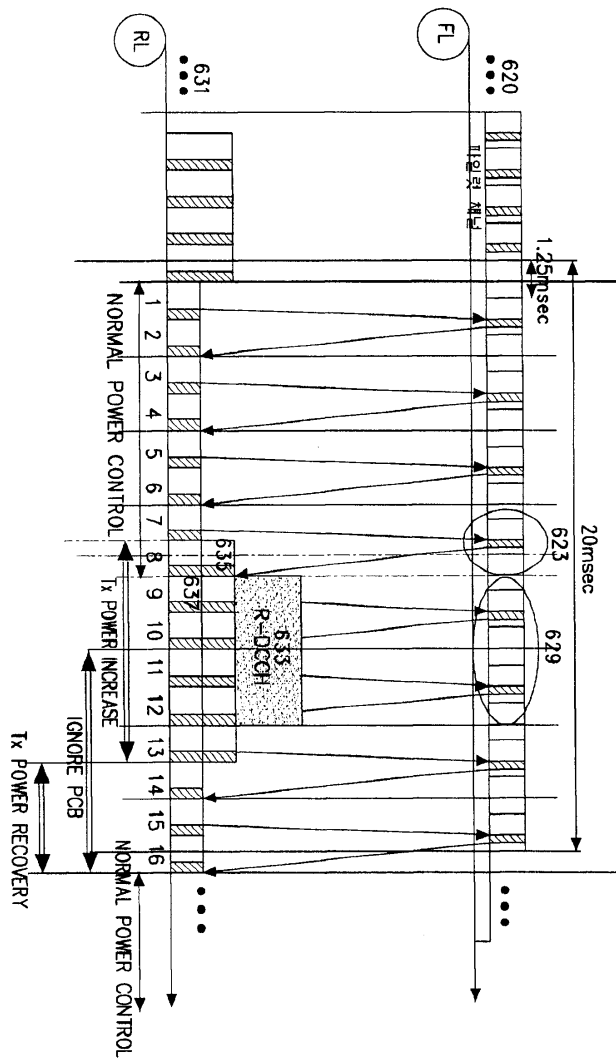
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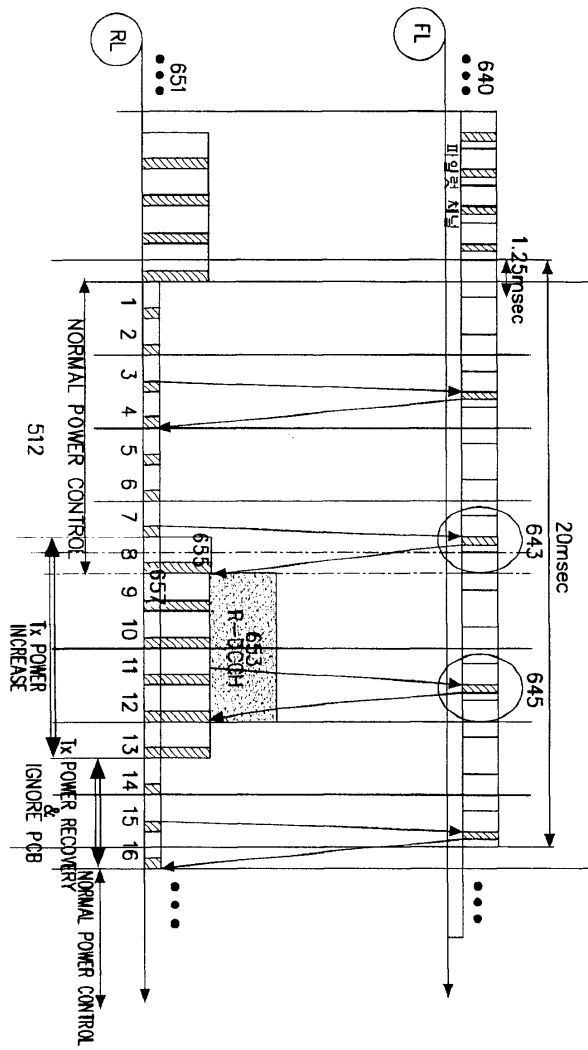
6d



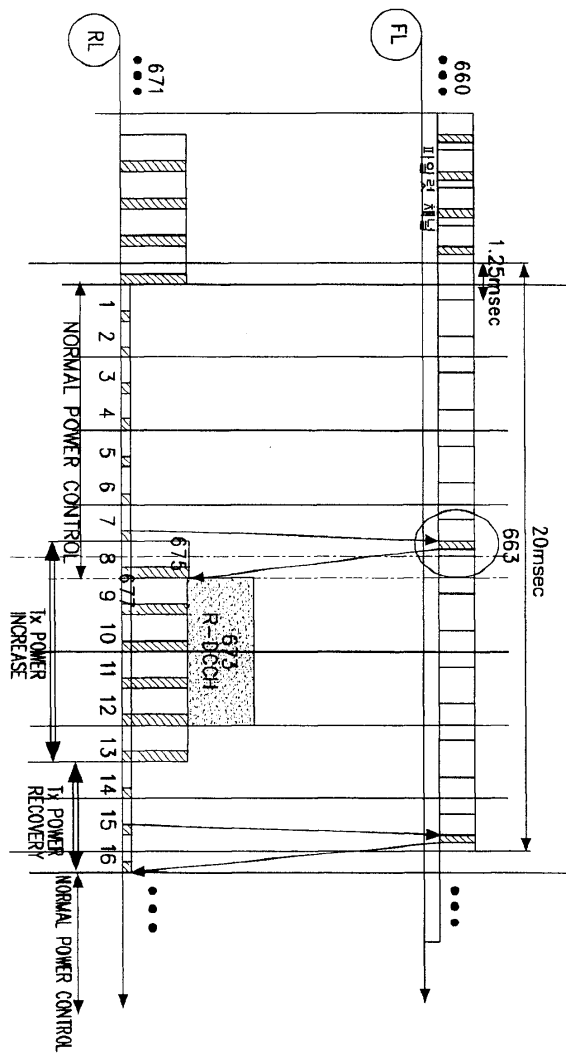
6e



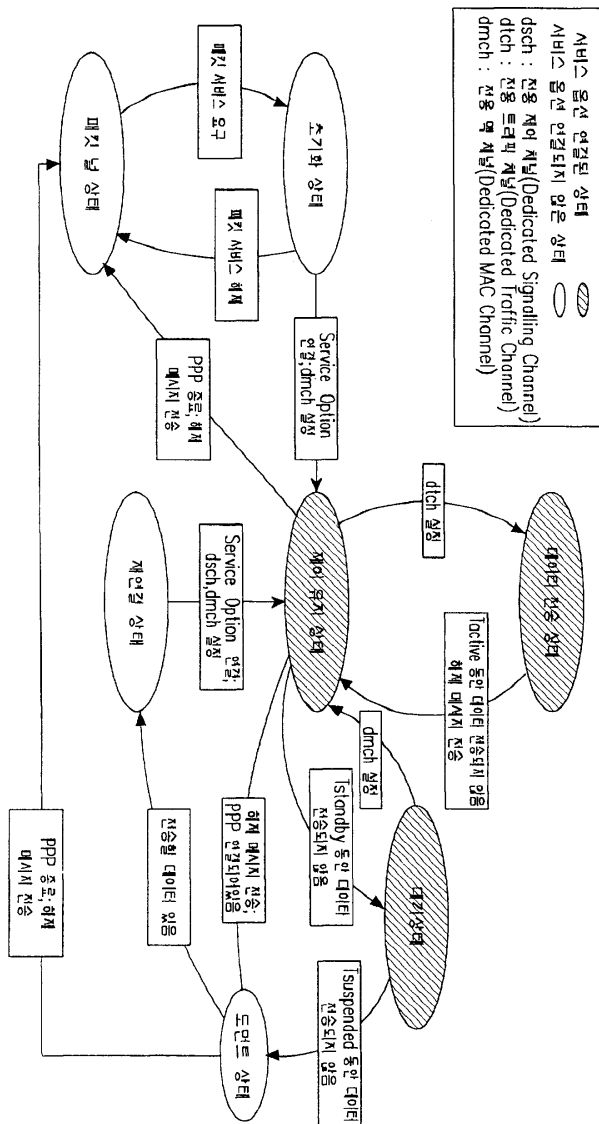
6f



6g



7



8

