⑲ Europäisches Patentamt
European Patent Office
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⑪ Publication number: **0 247 575**
A3

⑫ EUROPEAN PATENT APPLICATION

㉑ Application number: **87107642.8**

㉒ Date of filing: **26.05.87**

㊺ Int. Cl.⁴: **H05K 3/44 , H05K 3/46 ,**
H05K 3/42

㉚ Priority: **30.05.86 JP 125118/86**

㊸ Date of publication of application:
02.12.87 Bulletin 87/49

(84) Designated Contracting States:
DE FR GB

(88) Date of deferred publication of the search report:
11.10.89 Bulletin 89/41

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㊹ Multilayer printed wiring board and method for producing the same.

㊼ A multilayer printed wiring board produced by buildup process. A first layer printed wiring pattern (13) is formed on a metal core (11) through a first insulation laminate (12), and a second layer printed wiring pattern (16) is formed on the first layer printed wiring pattern through through-studs (15) and a second insulation laminate (14). The surface of the first layer printed wiring pattern (13) is roughened and the through studs (15) are formed by buildup process a conductive paste on the roughened surface.

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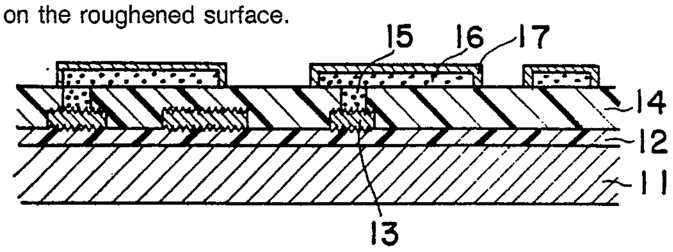

Fig. 1

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EUROPEAN SEARCH REPORT

Application Number

EP 87 10 7642

DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.4)
A	EP-A-0 048 992 (TOKYO SHIBAURA DENKI K.K.) * figures 1-4; page 5, line 12 - page 10, line 13 *	1,2,4-11,16,18,20	H 05 K 3/44 H 05 K 3/46 H 05 K 3/42
A	US-A-3 264 402 (J. M. SHAHEEN et al.) * figures 1A-F; column 2, line 58 - column 6, line 26 *	1-21	
A	US-A-4 492 730 (N. OISHI et al.) * figures 1,2; column 2, line 36 - column 5, line 3 *	1-11,20-26	
			TECHNICAL FIELDS SEARCHED (Int. Cl.4)
			H 05 K 1/00 H 05 K 3/00

The present search report has been drawn up for all claims

Place of search	Date of completion of the search	Examiner
BERLIN	13-06-1989	HAHN G

CATEGORY OF CITED DOCUMENTS

X : particularly relevant if taken alone
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