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Brekke et al.

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(54) MULTIPROBE CIRCUIT TESTER WITH ANIMATED GRAPHICAL USER INTERFACE

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(US)

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(52) **U.S. Cl.**

USPC **D14/486**

(58) Field of Classification Search

(Continued)

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(Continued)

Primary Examiner — Jack Reickel

(74) Attorney, Agent, or Firm — Seyfarth Shaw LLP

(57) CLAIM

The ornamental design for a multiprobe circuit tester with animated graphical user interface, as shown and described.

DESCRIPTION

The patent or application file contains at least one drawing executed in color. Copies of this patent or patent application publication with color drawing(s) will be provided by the Office upon request and payment of the necessary fee.

FIG. 1 is a front view of a multiprobe circuit tester with animated graphical user interface illustrating a first image in each of the first and second embodiments;

FIG. 2 is a front view thereof illustrating a second image in the first and second embodiments;

FIG. 3 is a front view thereof illustrating a third image in the first and second embodiments;

FIG. 4 is a front view thereof illustrating a fourth image in the first and second embodiments;

FIG. 5 is a front view thereof illustrating a fifth image in the first embodiment;

FIG. 6 is a front view thereof illustrating a fifth image in the second embodiment;

FIG. 7 is a front view of a multiprobe circuit tester with animated graphical user interface illustrating a first image in each of the third and fourth embodiments;

FIG. 8 is a front view thereof illustrating a second image in the third and fourth embodiments;

FIG. 9 is a front view thereof illustrating a third image in the third and fourth embodiments;

FIG. 10 is a front view thereof illustrating a fourth image in the third and fourth embodiments;

FIG. 11 is a front view thereof illustrating a fifth image in the third and fourth embodiments;

FIG. 12 is a front view thereof illustrating a sixth image in the third embodiment;

FIG. 13 is a front view thereof illustrating a sixth image in the fourth embodiment;

FIG. 14 is a front view of a multiprobe circuit tester with animated graphical user interface illustrating a first image in each of the fifth and sixth embodiments:

FIG. 15 is a front view thereof illustrating a second image in the fifth and sixth embodiments;

FIG. 16 is a front view thereof illustrating a third image in the fifth and sixth embodiments:

FIG. 17 is a front view thereof illustrating a fourth image in the fifth and sixth embodiments;

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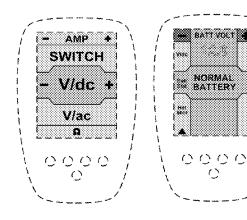


FIG. 18 is a front view thereof illustrating a fifth image in the fifth and sixth embodiments;

FIG. 19 is a front view thereof illustrating a sixth image in the fifth embodiment; and,

FIG. 20 is a front view thereof illustrating a sixth image in the sixth embodiment.

The broken line showing of a multiprobe circuit tester with animated graphical user interface is included for the purpose of illustrating portions of the article and form no part of the claimed design.

The appearance of the animated images sequentially transitions between the images shown in FIGS. 1-5. The process or period in which one transitions to another forms no part of the claimed design.

The appearance of the animated images sequentially transitions between the images shown in FIGS. **1-4** and **6**. The process or period in which one transitions to another forms no part of the claimed design.

The appearance of the animated images sequentially transitions between the images shown in FIGS. 7-12. The process or period in which one transitions to another forms no part of the claimed design.

The appearance of the animated images sequentially transitions between the images shown in FIGS. **7-11** and **13**. The process or period in which one transitions to another forms no part of the claimed design.

The appearance of the animated images sequentially transitions between the images shown in FIGS. **14-19**. The process or period in which one transitions to another forms no part of the claimed design.

The appearance of the animated images sequentially transitions between the images shown in FIGS. **14-18** and **20**. The process or period in which one transitions to another forms no part of the claimed design.

1 Claim, 7 Drawing Sheets (7 of 7 Drawing Sheet(s) Filed in Color)

(58) Field of Classification Search

CPC G06F 3/048; G06F 3/0481; G06F 3/04817; G06F 3/0482; G06F 3/0483; G06F 3/04842; G06F 3/0485; G06F 3/04855; G06F 3/0486; G06F 3/0488; G06F 3/04886; G06F 9/4443; G06F 17/211; G06F 17/212

See application file for complete search history.

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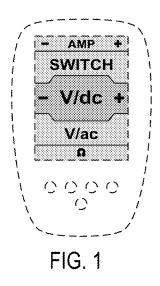
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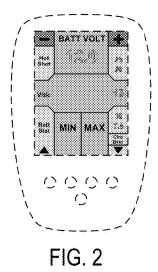
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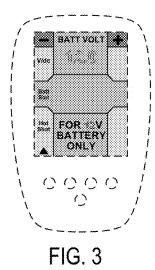
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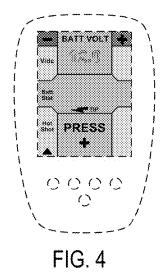
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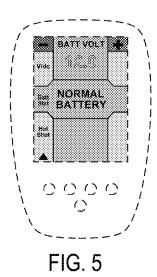
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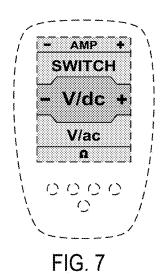


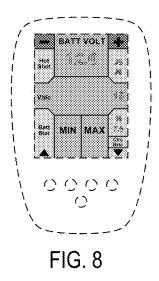


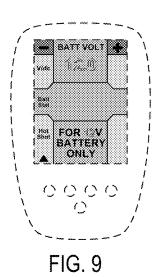


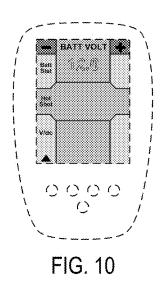
BATT VOLT SE CHECK SEE BATTERY

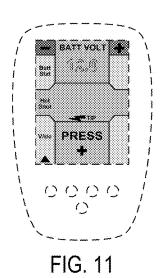
FIG. 6

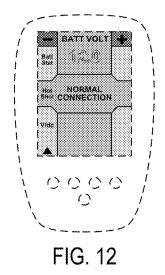












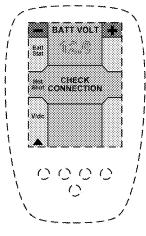
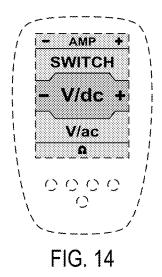


FIG. 13



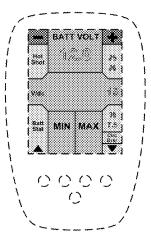


FIG. 15

