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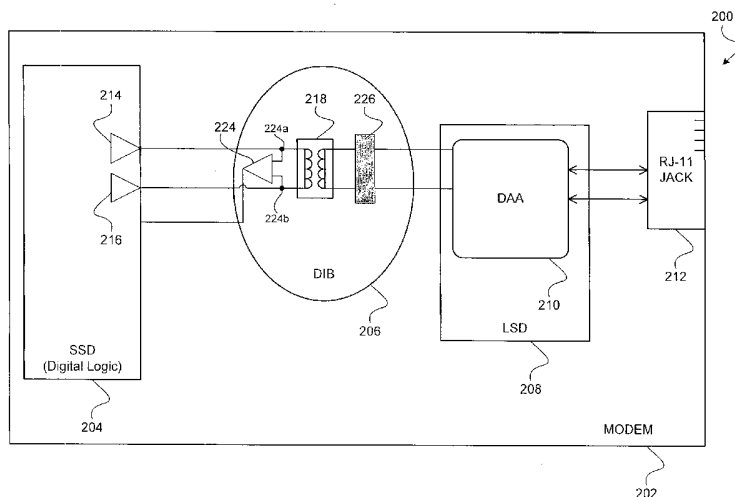
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(54) Title: ISOLATION BARRIER FOR INTERFACING A LINE SIDE DEVICE TO A SYSTEM SIDE DEVICE



(57) Abstract: According to one embodiment, an isolation barrier comprises a transformer and a controlled impedance module. The system side device generates an amplitude modulated clock signal when the system side device is in a transmit mode, and generates an unmodulated clock signal when the system side device is in a receive mode. When the system side device is in the receive mode, the controlled impedance module modulates, according to the data being transmitted by the line side device, the unmodulated clock signal incoming from the system side device. The transformer is utilized in bi-directional data transfer to provide the line side device with the amplitude modulated clock signal generated by the system side device, while providing the system side device with the amplitude modulated clock signal generated by the controlled impedance module and the line side device.

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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
Y,P	US 2004/0239487 A1 (HERSHBARGER) 02 December 2004, abstract; fig. 1; para. 0036-0039.	1-20
Y	US 2003/0215020 A1 (DONG et al.) 20 November 2003, para. 0011-0014.	1-20
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