



US00D798249S

(12) **United States Design Patent**
Vinciarelli et al.

(10) **Patent No.:** **US D798,249 S**

(45) **Date of Patent:** **** Sep. 26, 2017**

- (54) **ELECTRIC TERMINAL**
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- (73) Assignee: **VLТ, Inc.**, Sunnyvale, CA (US)
- (**) Term: **15 Years**
- (21) Appl. No.: **29/556,621**
- (22) Filed: **Mar. 2, 2016**

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Related U.S. Application Data

- (62) Division of application No. 29/470,060, filed on Oct. 17, 2013, now Pat. No. Des. 754,083.
- (51) **LOC (10) Cl.** **13-03**
- (52) **U.S. Cl.**
USPC **D13/182**
- (58) **Field of Classification Search**
USPC D13/182; 257/678, 690; 361/679.31,
361/719, 720, 752, 777, 820
CPC H01L 21/00; H01L 21/02; H01L 23/3675;
H01L 23/3677; H01L 23/48; H01L
23/481; H01L 23/50; H01L 23/482; H01L
23/52; H01L 23/498; H01L 23/49805;
H01L 23/49811; H05K 7/1422; H05K
7/1424; H05K 7/1427; H05K 7/1428
See application file for complete search history.

(57) **CLAIM**

The ornamental design for an electric terminal, as shown and described.

DESCRIPTION

FIG. 1 is a top, front, right perspective view of an electric terminal showing our new design;
FIG. 2 is a front elevation view thereof;
FIG. 3 is a rear elevation view thereof;
FIG. 4 is a right side elevation view thereof;
FIG. 5 is a left side elevation view thereof;
FIG. 6 is a top plan view thereof; and,
FIG. 7 is a bottom plan view thereof.

The ornamental design which is claimed is shown in the figures.

The dashed-dot-dashed lines represent the boundary line of the claimed design. The even dashed broken lines shown in the drawings represent portions of the electric terminal that form no part of the claimed design.

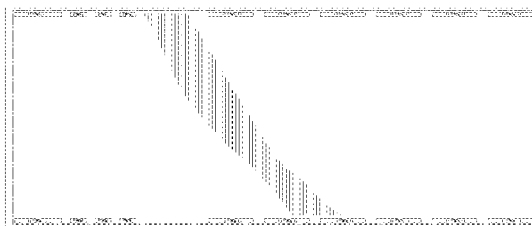
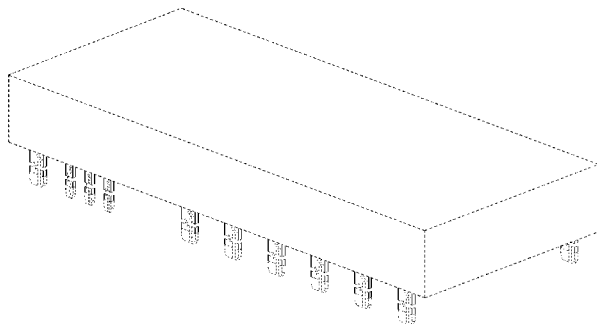
The claimed design should be understood to include the outer solid line edges of the protruding members shown in FIGS. 1-5, as well as the bottom surface area in FIG. 7.

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1 Claim, 5 Drawing Sheets



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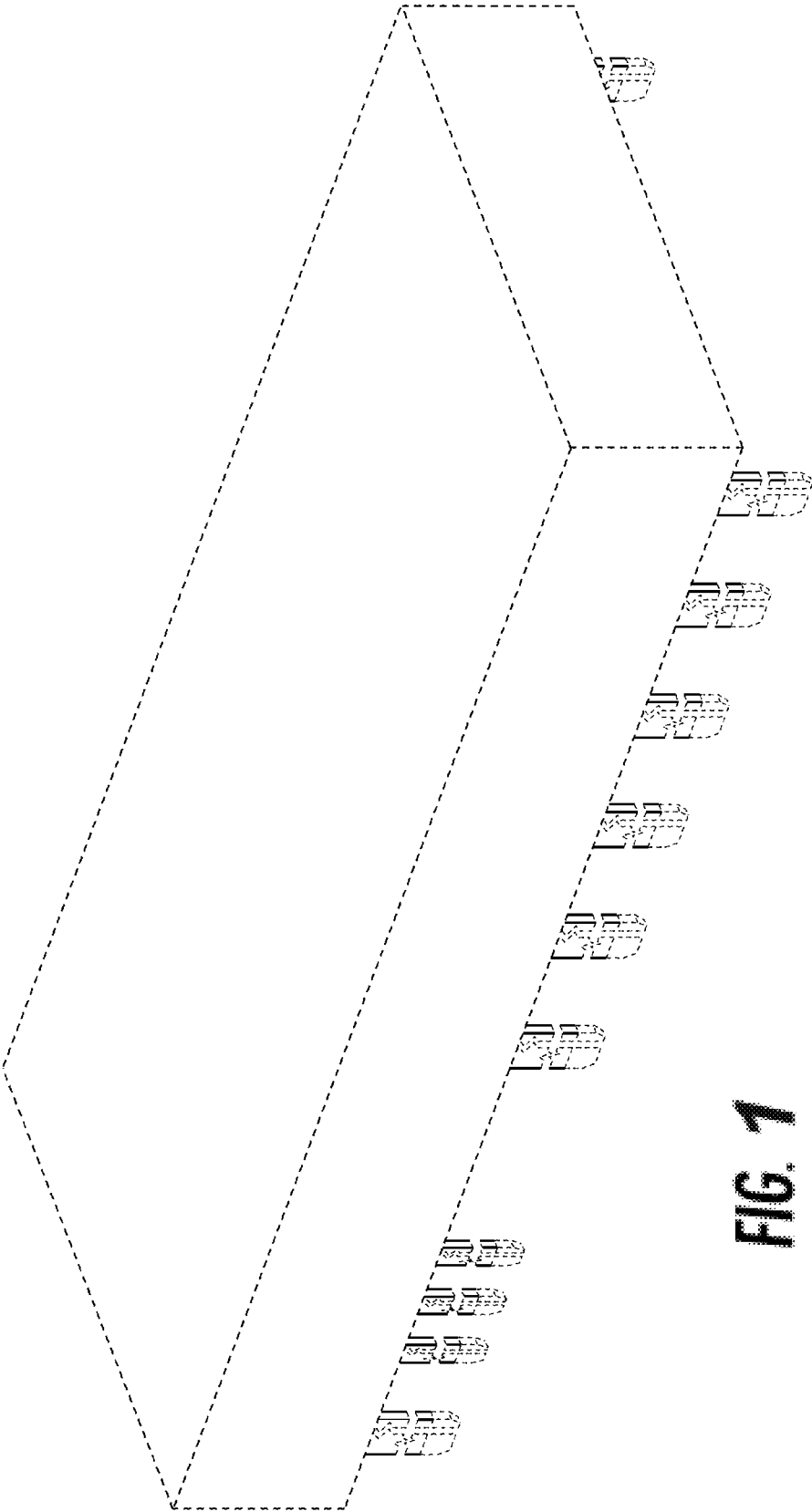


FIG. 1

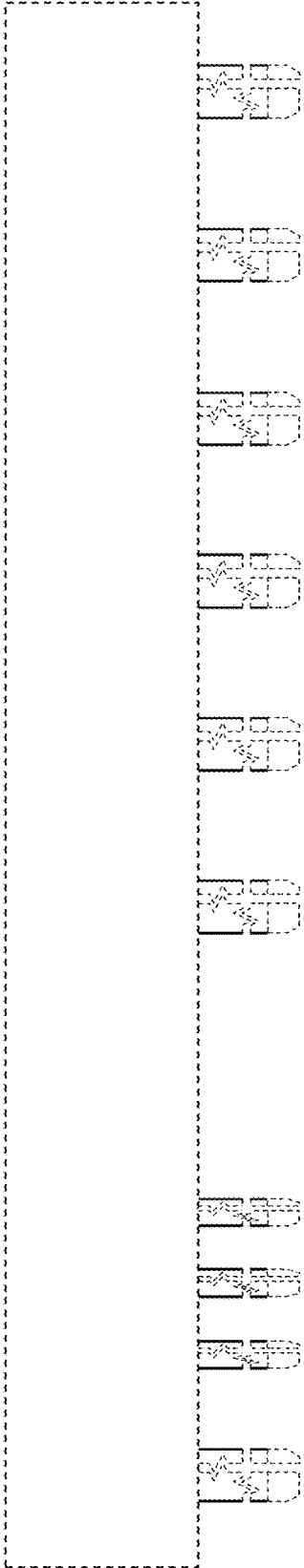


FIG. 2

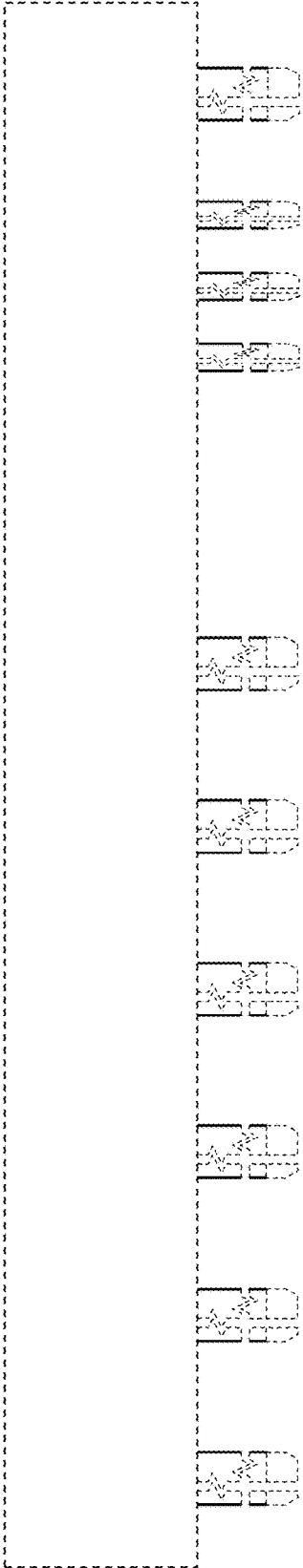


FIG. 3

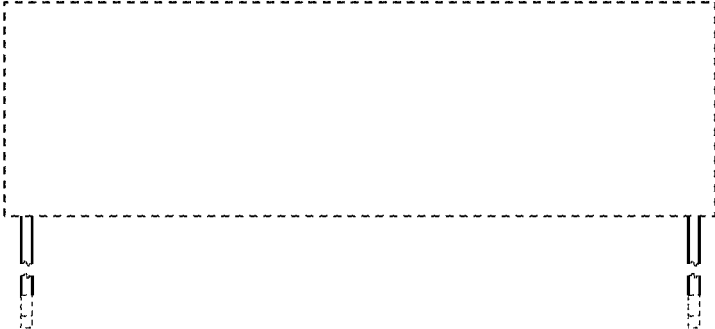


FIG. 4

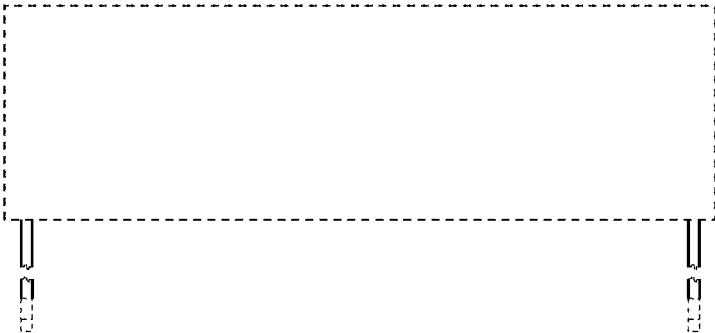


FIG. 5



FIG. 6



FIG. 7