



US00D482030S

(12) **United States Design Patent** (10) **Patent No.:** **US D482,030 S**  
**Kataoka** (45) **Date of Patent:** **\*\* Nov. 11, 2003**

(54) **COMPUTER**

(75) Inventor: **Tetsu Kataoka**, Tokyo (JP)  
(73) Assignee: **Sony Corporation**, Tokyo (JP)  
(\*\*) Term: **14 Years**

(21) Appl. No.: **29/166,690**  
(22) Filed: **Sep. 4, 2002**

(51) **LOC (7) Cl.** ..... **14-02**  
(52) **U.S. Cl.** ..... **D14/345**  
(58) **Field of Search** ..... D14/341-346;  
D18/1, 2, 7; 235/145 A, 145 R; 341/22,  
23; 345/156, 168, 169, 172, 173; 349/12;  
361/680-686

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(57) **CLAIM**

The ornamental design for a computer, as shown.

**DESCRIPTION**

FIG. 1 is a perspective view of a computer showing my new design;  
FIG. 2 is a front elevational view thereof;  
FIG. 3 is a rear elevational view thereof;  
FIG. 4 is a left side elevational view thereof;  
FIG. 5 is a right side elevational view thereof;  
FIG. 6 is a top plan view thereof; and,  
FIG. 7 is a bottom plan view thereof.

**1 Claim, 4 Drawing Sheets**

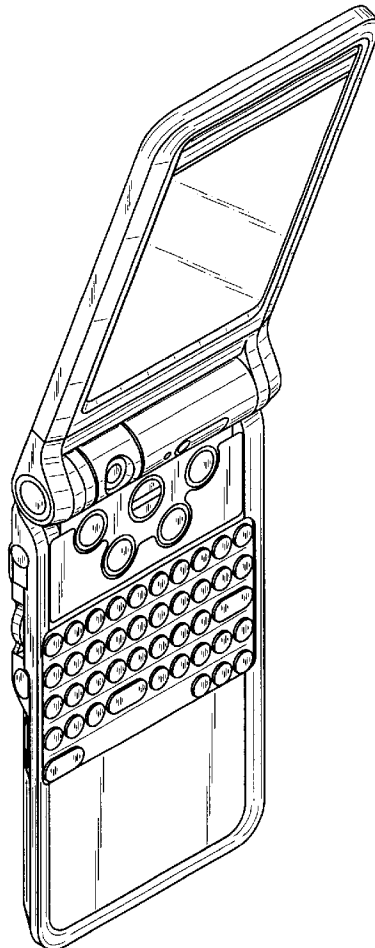
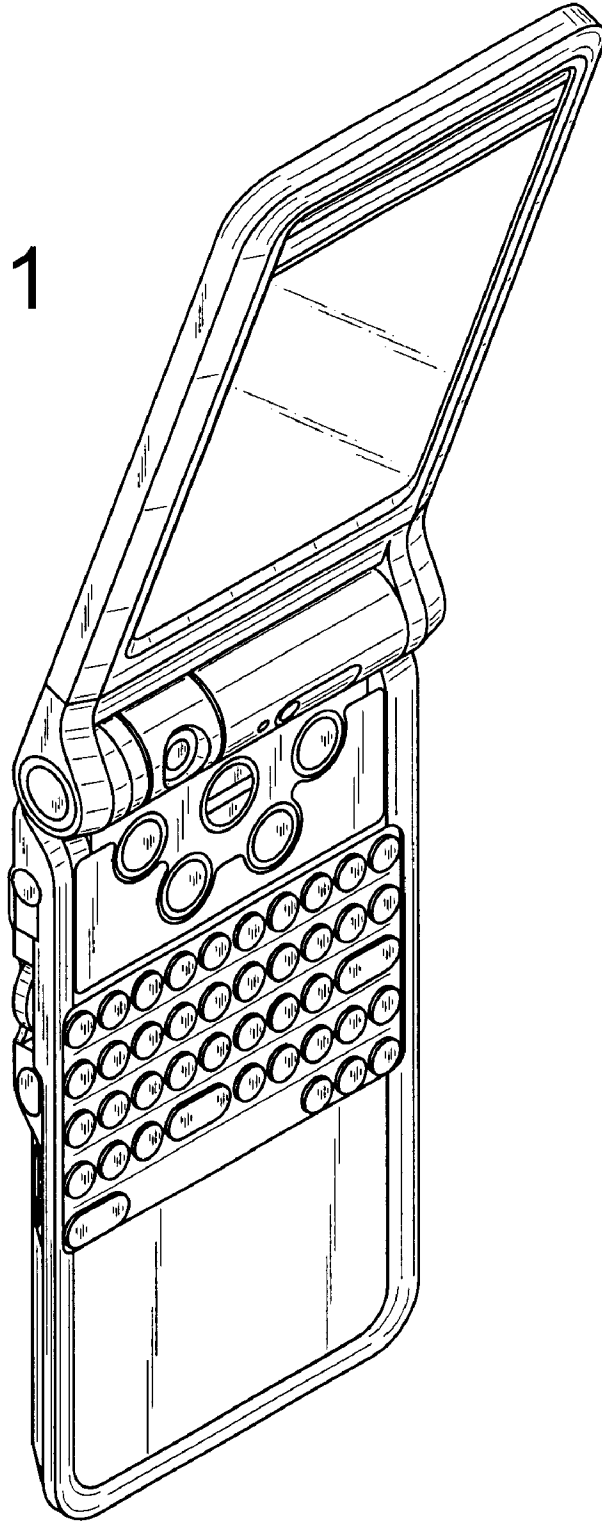


Fig. 1



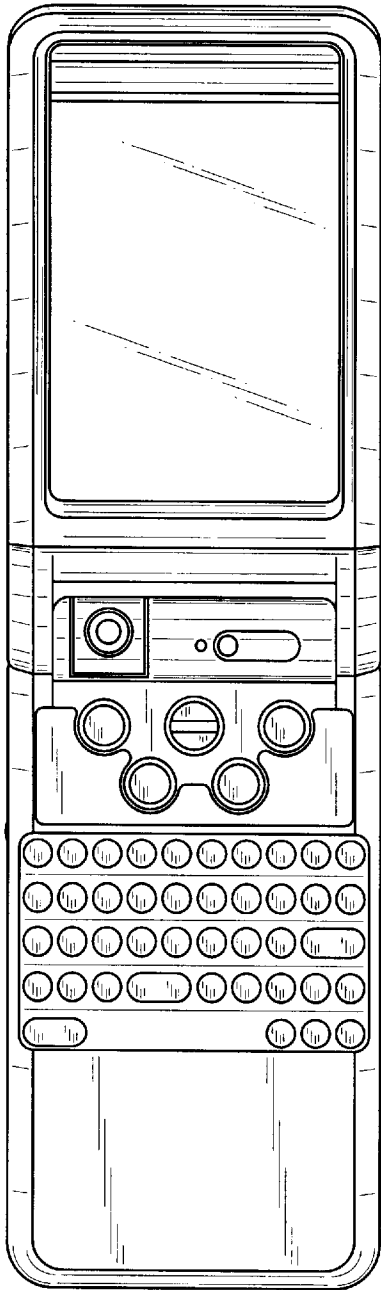


Fig. 2

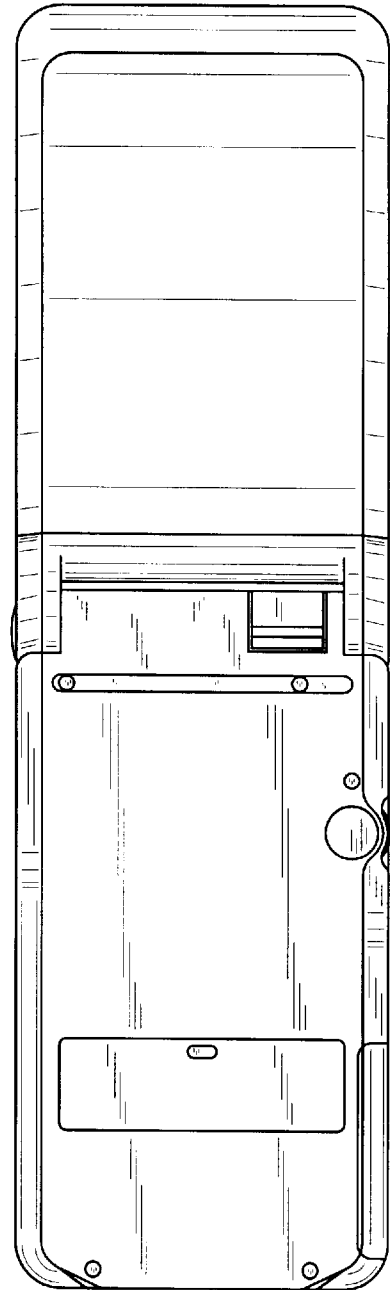


Fig. 3

Fig. 4

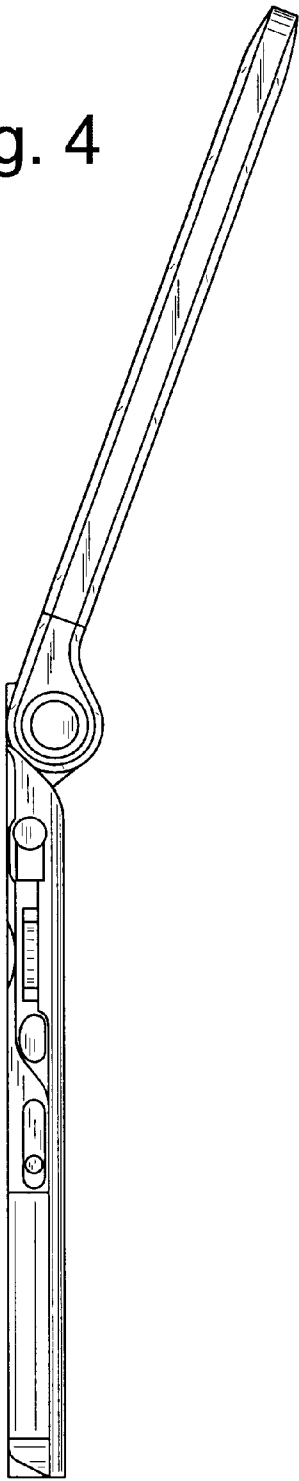


Fig. 5

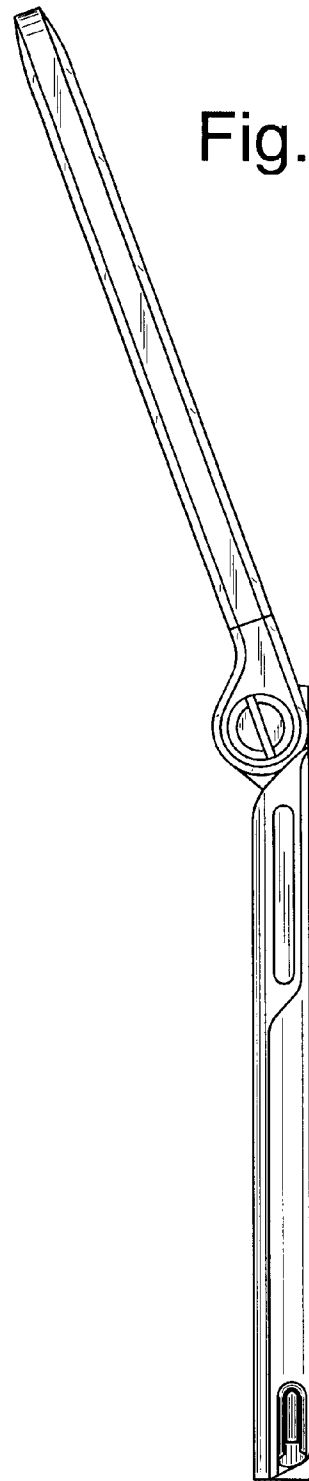


Fig. 6

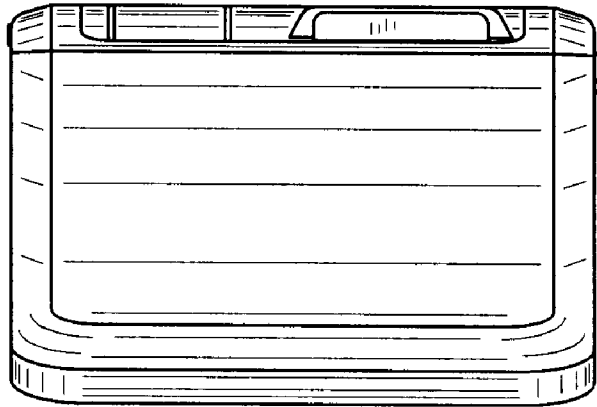


Fig. 7

