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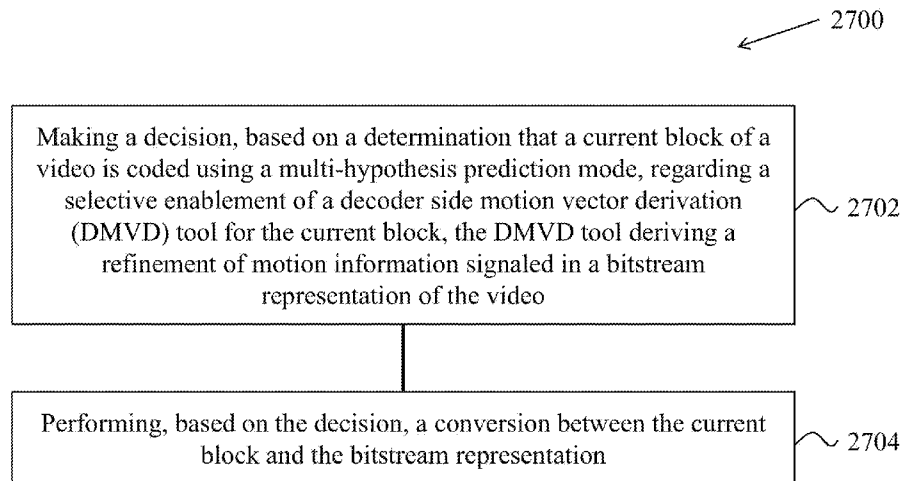


FIG. 27A

(57) Abstract: Devices, systems and methods for digital video coding, which include decoder side motion vector derivation (DMVD) tools, are described. An exemplary method for video processing includes making a decision, based on a determination that a current block of a video is coded using a multi-hypothesis prediction mode, regarding a selective enablement of a DMVD tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing, based on the decision, a conversion between the current block and the bitstream representation.



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DECODER SIDE MOTION VECTOR DERIVATION IN THE PRESENCE OF MULTI-HYPOTHESIS PREDICTION

CROSS-REFERENCE TO RELATED APPLICATION

[0001] Under the applicable patent law and/or rules pursuant to the Paris Convention, this application is made to timely claim the priority to and benefits of International Patent Application No. PCT/CN2018/111224, filed on October 22, 2018. For all purposes under the U.S. law, the entire disclosure of the aforementioned application is incorporated by reference as part of the disclosure of this application.

TECHNICAL FIELD

[0002] This patent document relates to video coding techniques, devices and systems.

BACKGROUND

[0003] In spite of the advances in video compression, digital video still accounts for the largest bandwidth use on the internet and other digital communication networks. As the number of connected user devices capable of receiving and displaying video increases, it is expected that the bandwidth demand for digital video usage will continue to grow.

SUMMARY

[0004] Devices, systems and methods related to digital video coding, and specifically, to decoder-side motion vector derivation (DMVD) tools are described. The described methods may be applied to both the existing video coding standards (e.g., High Efficiency Video Coding (HEVC)) and future video coding standards or video codecs.

[0005] In one representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes making a decision, based on a determination that a current block of a video is coded using a multi-hypothesis prediction mode, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing, based on the decision, a conversion between the current block and the bitstream representation, wherein the multi-hypothesis prediction mode is configured to generate a final prediction of the current block by applying at

least one intermediate prediction value.

[0006] In another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes determining that a current block of video is associated with asymmetric weighting factors for different reference blocks; enabling a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video, and wherein the DMVD process is based on the asymmetric weighting factors; and performing, based on the enabling, a conversion between the current block and the bitstream representation.

[0007] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes determining that a current block of video is coded using an advanced motion vector prediction (AMVP) mode; and applying, as part of a conversion between a bitstream representation of the video and the current block, a decoder side motion vector derivation (DMVD) tool to the current block, wherein the DMVD tool derives a refinement of motion information signaled in the bitstream representation.

[0008] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes performing, based on a decoder side motion vector derivation (DMVD) tool, a refinement of translational motion parameters for a current block of video that is coded using a bi-directional affine mode or a bi-directional affine merge mode and motion vector differences that are indicated by a motion direction and a motion magnitude, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing, based on the refinement, a conversion between the current block and the bitstream representation of the video.

[0009] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes making a decision, based on a characteristic of a current block of video, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing, based on the decision, a conversion between the current block and the bitstream representation.

[0010] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes making a decision, based upon a

determination that a current block of video comprises a plurality of sub-blocks, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool at a sub-block level, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video for each sub-block; and performing, based on the decision, a conversion between the current block and the bitstream representation.

[0011] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes making a decision, based on at least one reference picture associated with a current block of video, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing, based on the decision, a conversion between the current block and the bitstream representation.

[0012] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes parsing a bin string from a bitstream representation of a current block of video, wherein the bin string comprises a plurality of bins that represent a generalized bi-prediction (GBI) index of a GBI mode, and wherein at least one bin of the plurality of bins is bypass coded; and performing, based on the parsed GBI index, a conversion between the current block and the bitstream representation.

[0013] In yet another representative aspect, the disclosed technology may be used to provide a method for video processing. This method includes encoding a bin string into a bitstream representation of a current block of video, wherein the bin string comprises a plurality of bins that represent a generalized bi-prediction (GBI) index of a GBI mode, and wherein at least one bin of the plurality of bins is bypass coded; and performing, based on the encoded bin string, a conversion between the current block and the bitstream representation.

[0014] In yet another representative aspect, the above-described method is embodied in the form of processor-executable code and stored in a computer-readable program medium.

[0015] In yet another representative aspect, a device that is configured or operable to perform the above-described method is disclosed. The device may include a processor that is programmed to implement this method.

[0016] In yet another representative aspect, a video decoder apparatus may implement a method as described herein.

[0017] The above and other aspects and features of the disclosed technology are described in greater detail in the drawings, the description and the claims.

BRIEF DESCRIPTION OF THE DRAWINGS

[0018] FIG. 1 shows an example of constructing a merge candidate list.

[0019] FIG. 2 shows an example of positions of spatial candidates.

[0020] FIG. 3 shows an example of candidate pairs subject to a redundancy check of spatial merge candidates.

[0021] FIGS. 4A and 4B show examples of the position of a second prediction unit (PU) based on the size and shape of the current block.

[0022] FIG. 5 shows an example of motion vector scaling for temporal merge candidates.

[0023] FIG. 6 shows an example of candidate positions for temporal merge candidates.

[0024] FIG. 7 shows an example of generating a combined bi-predictive merge candidate.

[0025] FIG. 8 shows an example of constructing motion vector prediction candidates.

[0026] FIG. 9 shows an example of motion vector scaling for spatial motion vector candidates.

[0027] FIG. 10 shows an example of motion prediction using the alternative temporal motion vector prediction (ATMVP) algorithm for a coding unit (CU).

[0028] FIG. 11 shows an example of a coding unit (CU) with sub-blocks and neighboring blocks used by the spatial-temporal motion vector prediction (STMVP) algorithm.

[0029] FIGS. 12A and 12B show example snapshots of sub-block when using the overlapped block motion compensation (OBMC) algorithm.

[0030] FIG. 13 shows an example of neighboring samples used to derive parameters for the local illumination compensation (LIC) algorithm.

[0031] FIG. 14 shows an example of a simplified affine motion model.

[0032] FIG. 15 shows an example of an affine motion vector field (MVF) per sub-block.

[0033] FIG. 16 shows an example of motion vector prediction (MVP) for the AF_INTER affine motion mode.

[0034] FIGS. 17A and 17B show example candidates for the AF_MERGE affine motion mode.

[0035] FIG. 18 shows an example of bilateral matching in pattern matched motion vector

derivation (PMMVD) mode, which is a special merge mode based on the frame-rate up conversion (FRUC) algorithm.

[0036] FIG. 19 shows an example of template matching in the FRUC algorithm.

[0037] FIG. 20 shows an example of unilateral motion estimation in the FRUC algorithm.

[0038] FIG. 21 shows an example of an ultimate motion vector expression (UMVE) search process for a current frame.

[0039] FIGS. 22A and 22B show examples of UMVE search points.

[0040] FIG. 23 shows an exemplary mapping between distance index and distance offset.

[0041] FIG. 24 shows an example of an optical flow trajectory used by the bi-directional optical flow (BIO) algorithm.

[0042] FIGS. 25A and 25B show example snapshots of using of the bi-directional optical flow (BIO) algorithm without block extensions.

[0043] FIG. 26 shows an example of the decoder-side motion vector refinement (DMVR) algorithm based on bilateral template matching.

[0044] FIGS. 27A–27I show flowcharts of example methods for video processing.

[0045] FIG. 28 is a block diagram of an example of a hardware platform for implementing a visual media decoding or a visual media encoding technique described in the present document.

[0046] FIG. 29 is a block diagram of an example video processing system in which disclosed techniques may be implemented.

DETAILED DESCRIPTION

[0047] Due to the increasing demand of higher resolution video, video coding methods and techniques are ubiquitous in modern technology. Video codecs typically include an electronic circuit or software that compresses or decompresses digital video, and are continually being improved to provide higher coding efficiency. A video codec converts uncompressed video to a compressed format or vice versa. There are complex relationships between the video quality, the amount of data used to represent the video (determined by the bit rate), the complexity of the encoding and decoding algorithms, sensitivity to data losses and errors, ease of editing, random access, and end-to-end delay (latency). The compressed format usually conforms to a standard video compression specification, e.g., the High Efficiency Video Coding (HEVC) standard (also known as H.265 or MPEG-H Part 2), the Versatile Video Coding standard to be finalized, or

other current and/or future video coding standards.

[0048] Embodiments of the disclosed technology may be applied to existing video coding standards (e.g., HEVC, H.265) and future standards to improve compression performance.

Section headings are used in the present document to improve readability of the description and do not in any way limit the discussion or the embodiments (and/or implementations) to the respective sections only.

1. Examples of inter-prediction in HEVC/H.265

[0049] Video coding standards have significantly improved over the years, and now provide, in part, high coding efficiency and support for higher resolutions. Recent standards such as HEVC and H.265 are based on the hybrid video coding structure wherein temporal prediction plus transform coding are utilized.

1.1 Examples of prediction modes

[0050] Each inter-predicted PU (prediction unit) has motion parameters for one or two reference picture lists. In some embodiments, motion parameters include a motion vector and a reference picture index. In other embodiments, the usage of one of the two reference picture lists may also be signaled using *inter_pred_idc*. In yet other embodiments, motion vectors may be explicitly coded as deltas relative to predictors.

[0051] When a CU is coded with skip mode, one PU is associated with the CU, and there are no significant residual coefficients, no coded motion vector delta or reference picture index. A merge mode is specified whereby the motion parameters for the current PU are obtained from neighboring PUs, including spatial and temporal candidates. The merge mode can be applied to any inter-predicted PU, not only for skip mode. The alternative to merge mode is the explicit transmission of motion parameters, where motion vector, corresponding reference picture index for each reference picture list and reference picture list usage are signaled explicitly per each PU.

[0052] When signaling indicates that one of the two reference picture lists is to be used, the PU is produced from one block of samples. This is referred to as 'uni-prediction'. Uni-prediction is available both for P-slices and B-slices.

[0053] When signaling indicates that both of the reference picture lists are to be used, the PU is produced from two blocks of samples. This is referred to as 'bi-prediction'. Bi-prediction is available for B-slices only.

1.1.1 Embodiments of constructing candidates for merge mode

[0054] When a PU is predicted using merge mode, an index pointing to an entry in the *merge candidates list* is parsed from the bitstream and used to retrieve the motion information. The construction of this list can be summarized according to the following sequence of steps:

[0055] Step 1: Initial candidates derivation

[0056] Step 1.1: Spatial candidates derivation

[0057] Step 1.2: Redundancy check for spatial candidates

[0058] Step 1.3: Temporal candidates derivation

[0059] Step 2: Additional candidates insertion

[0060] Step 2.1: Creation of bi-predictive candidates

[0061] Step 2.2: Insertion of zero motion candidates

[0062] FIG. 1 shows an example of constructing a merge candidate list based on the sequence of steps summarized above. For spatial merge candidate derivation, a maximum of four merge candidates are selected among candidates that are located in five different positions. For temporal merge candidate derivation, a maximum of one merge candidate is selected among two candidates. Since constant number of candidates for each PU is assumed at decoder, additional candidates are generated when the number of candidates does not reach to maximum number of merge candidate (*MaxNumMergeCand*) which is signalled in slice header. Since the number of candidates is constant, index of best merge candidate is encoded using truncated unary binarization (TU). If the size of CU is equal to 8, all the PUs of the current CU share a single merge candidate list, which is identical to the merge candidate list of the $2N \times 2N$ prediction unit.

1.1.2 Constructing spatial merge candidates

[0063] In the derivation of spatial merge candidates, a maximum of four merge candidates are selected among candidates located in the positions depicted in FIG. 2. The order of derivation is A_1 , B_1 , B_0 , A_0 and B_2 . Position B_2 is considered only when any PU of position A_1 , B_1 , B_0 , A_0 is not available (e.g. because it belongs to another slice or tile) or is intra coded. After candidate at position A_1 is added, the addition of the remaining candidates is subject to a redundancy check which ensures that candidates with same motion information are excluded from the list so that coding efficiency is improved.

[0064] To reduce computational complexity, not all possible candidate pairs are considered in the mentioned redundancy check. Instead only the pairs linked with an arrow in FIG. 3 are considered and a candidate is only added to the list if the corresponding candidate used for

redundancy check has not the same motion information. Another source of duplicate motion information is the “*second PU*” associated with partitions different from $2N \times 2N$. As an example, FIG. 4A and 4B depict the second PU for the case of $N \times 2N$ and $2N \times N$, respectively. When the current PU is partitioned as $N \times 2N$, candidate at position A_1 is not considered for list construction. In some embodiments, adding this candidate may lead to two prediction units having the same motion information, which is redundant to just have one PU in a coding unit. Similarly, position B_1 is not considered when the current PU is partitioned as $2N \times N$.

1.1.3 Constructing temporal merge candidates

[0065] In this step, only one candidate is added to the list. Particularly, in the derivation of this temporal merge candidate, a scaled motion vector is derived based on co-located PU belonging to the picture which has the smallest POC difference with current picture within the given reference picture list. The reference picture list to be used for derivation of the co-located PU is explicitly signaled in the slice header.

[0066] FIG. 5 shows an example of the derivation of the scaled motion vector for a temporal merge candidate (as the dotted line), which is scaled from the motion vector of the co-located PU using the POC distances, t_b and t_d , where t_b is defined to be the POC difference between the reference picture of the current picture and the current picture and t_d is defined to be the POC difference between the reference picture of the co-located picture and the co-located picture. The reference picture index of temporal merge candidate is set equal to zero. For a B-slice, two motion vectors, one is for reference picture list 0 and the other is for reference picture list 1, are obtained and combined to make the bi-predictive merge candidate.

[0067] In the co-located PU (Y) belonging to the reference frame, the position for the temporal candidate is selected between candidates C_0 and C_1 , as depicted in FIG. 6. If PU at position C_0 is not available, is intra coded, or is outside of the current CTU, position C_1 is used. Otherwise, position C_0 is used in the derivation of the temporal merge candidate.

1.1.4 Constructing additional types of merge candidates

[0068] Besides spatio-temporal merge candidates, there are two additional types of merge candidates: combined bi-predictive merge candidate and zero merge candidate. Combined bi-predictive merge candidates are generated by utilizing spatio-temporal merge candidates. Combined bi-predictive merge candidate is used for B-Slice only. The combined bi-predictive candidates are generated by combining the first reference picture list motion parameters of an

initial candidate with the second reference picture list motion parameters of another. If these two tuples provide different motion hypotheses, they will form a new bi-predictive candidate.

[0069] FIG. 7 shows an example of this process, wherein two candidates in the original list (710, on the left), which have $mvL0$ and $refIdxL0$ or $mvL1$ and $refIdxL1$, are used to create a combined bi-predictive merge candidate added to the final list (720, on the right).

[0070] Zero motion candidates are inserted to fill the remaining entries in the merge candidates list and therefore hit the $MaxNumMergeCand$ capacity. These candidates have zero spatial displacement and a reference picture index which starts from zero and increases every time a new zero motion candidate is added to the list. The number of reference frames used by these candidates is one and two for uni- and bi-directional prediction, respectively. In some embodiments, no redundancy check is performed on these candidates.

1.1.5 Examples of motion estimation regions for parallel processing

[0071] To speed up the encoding process, motion estimation can be performed in parallel whereby the motion vectors for all prediction units inside a given region are derived simultaneously. The derivation of merge candidates from spatial neighborhood may interfere with parallel processing as one prediction unit cannot derive the motion parameters from an adjacent PU until its associated motion estimation is completed. To mitigate the trade-off between coding efficiency and processing latency, a motion estimation region (MER) may be defined. The size of the MER may be signaled in the picture parameter set (PPS) using the “ $log2_parallel_merge_level_minus2$ ” syntax element. When a MER is defined, merge candidates falling in the same region are marked as unavailable and therefore not considered in the list construction.

1.2 Embodiments of advanced motion vector prediction (AMVP)

[0072] AMVP exploits spatio-temporal correlation of motion vector with neighboring PUs, which is used for explicit transmission of motion parameters. It constructs a motion vector candidate list by firstly checking availability of left, above temporally neighboring PU positions, removing redundant candidates and adding zero vector to make the candidate list to be constant length. Then, the encoder can select the best predictor from the candidate list and transmit the corresponding index indicating the chosen candidate. Similarly with merge index signaling, the index of the best motion vector candidate is encoded using truncated unary. The maximum value to be encoded in this case is 2 (see FIG. 8). In the following sections, details about derivation

process of motion vector prediction candidate are provided.

1.2.1 Examples of constructing motion vector prediction candidates

[0073] FIG. 8 summarizes derivation process for motion vector prediction candidate, and may be implemented for each reference picture list with refidx as an input.

[0074] In motion vector prediction, two types of motion vector candidates are considered: spatial motion vector candidate and temporal motion vector candidate. For spatial motion vector candidate derivation, two motion vector candidates are eventually derived based on motion vectors of each PU located in five different positions as previously shown in FIG. 2.

[0075] For temporal motion vector candidate derivation, one motion vector candidate is selected from two candidates, which are derived based on two different co-located positions. After the first list of spatio-temporal candidates is made, duplicated motion vector candidates in the list are removed. If the number of potential candidates is larger than two, motion vector candidates whose reference picture index within the associated reference picture list is larger than 1 are removed from the list. If the number of spatio-temporal motion vector candidates is smaller than two, additional zero motion vector candidates is added to the list.

1.2.2 Constructing spatial motion vector candidates

[0076] In the derivation of spatial motion vector candidates, a maximum of two candidates are considered among five potential candidates, which are derived from PUs located in positions as previously shown in FIG. 2, those positions being the same as those of motion merge. The order of derivation for the left side of the current PU is defined as A_0 , A_1 , and scaled A_0 , scaled A_1 . The order of derivation for the above side of the current PU is defined as B_0 , B_1 , B_2 , scaled B_0 , scaled B_1 , scaled B_2 . For each side there are therefore four cases that can be used as motion vector candidate, with two cases not required to use spatial scaling, and two cases where spatial scaling is used. The four different cases are summarized as follows:

[0077] -- No spatial scaling

[0078] (1) Same reference picture list, and same reference picture index (same POC)

[0079] (2) Different reference picture list, but same reference picture (same POC)

[0080] -- Spatial scaling

[0081] (3) Same reference picture list, but different reference picture (different POC)

[0082] (4) Different reference picture list, and different reference picture (different POC)

[0083] The no-spatial-scaling cases are checked first followed by the cases that allow spatial

scaling. Spatial scaling is considered when the POC is different between the reference picture of the neighbouring PU and that of the current PU regardless of reference picture list. If all PUs of left candidates are not available or are intra coded, scaling for the above motion vector is allowed to help parallel derivation of left and above MV candidates. Otherwise, spatial scaling is not allowed for the above motion vector.

[0084] As shown in the example in FIG. 9, for the spatial scaling case, the motion vector of the neighbouring PU is scaled in a similar manner as for temporal scaling. One difference is that the reference picture list and index of current PU is given as input; the actual scaling process is the same as that of temporal scaling.

1.2.3 Constructing temporal motion vector candidates

[0085] Apart from the reference picture index derivation, all processes for the derivation of temporal merge candidates are the same as for the derivation of spatial motion vector candidates (as shown in the example in FIG. 6). In some embodiments, the reference picture index is signaled to the decoder.

2. Example of inter prediction methods in Joint Exploration Model (JEM)

[0086] In some embodiments, future video coding technologies are explored using a reference software known as the Joint Exploration Model (JEM). In JEM, sub-block based prediction is adopted in several coding tools, such as affine prediction, alternative temporal motion vector prediction (ATMVP), spatial-temporal motion vector prediction (STMVP), bi-directional optical flow (BIO), Frame-Rate Up Conversion (FRUC), Locally Adaptive Motion Vector Resolution (LAMVR), Overlapped Block Motion Compensation (OBMC), Local Illumination Compensation (LIC), and Decoder-side Motion Vector Refinement (DMVR).

2.1 Examples of sub-CU based motion vector prediction

[0087] In the JEM with quadrees plus binary trees (QTBT), each CU can have at most one set of motion parameters for each prediction direction. In some embodiments, two sub-CU level motion vector prediction methods are considered in the encoder by splitting a large CU into sub-CUs and deriving motion information for all the sub-CUs of the large CU. Alternative temporal motion vector prediction (ATMVP) method allows each CU to fetch multiple sets of motion information from multiple blocks smaller than the current CU in the collocated reference picture. In spatial-temporal motion vector prediction (STMVP) method motion vectors of the sub-CUs are derived recursively by using the temporal motion vector predictor and spatial neighbouring

motion vector. In some embodiments, and to preserve more accurate motion field for sub-CU motion prediction, the motion compression for the reference frames may be disabled.

2.1.1 Examples of alternative temporal motion vector prediction (ATMVP)

[0088] In the ATMVP method, the temporal motion vector prediction (TMVP) method is modified by fetching multiple sets of motion information (including motion vectors and reference indices) from blocks smaller than the current CU.

[0089] FIG. 10 shows an example of ATMVP motion prediction process for a CU 1000. The ATMVP method predicts the motion vectors of the sub-CUs 1001 within a CU 1000 in two steps. The first step is to identify the corresponding block 1051 in a reference picture 1050 with a temporal vector. The reference picture 1050 is also referred to as the motion source picture. The second step is to split the current CU 1000 into sub-CUs 1001 and obtain the motion vectors as well as the reference indices of each sub-CU from the block corresponding to each sub-CU.

[0090] In the first step, a reference picture 1050 and the corresponding block is determined by the motion information of the spatial neighboring blocks of the current CU 1000. To avoid the repetitive scanning process of neighboring blocks, the first merge candidate in the merge candidate list of the current CU 1000 is used. The first available motion vector as well as its associated reference index are set to be the temporal vector and the index to the motion source picture. This way, the corresponding block may be more accurately identified, compared with TMVP, wherein the corresponding block (sometimes called collocated block) is always in a bottom-right or center position relative to the current CU.

[0091] In the second step, a corresponding block of the sub-CU 1051 is identified by the temporal vector in the motion source picture 1050, by adding to the coordinate of the current CU the temporal vector. For each sub-CU, the motion information of its corresponding block (e.g., the smallest motion grid that covers the center sample) is used to derive the motion information for the sub-CU. After the motion information of a corresponding $N \times N$ block is identified, it is converted to the motion vectors and reference indices of the current sub-CU, in the same way as TMVP of HEVC, wherein motion scaling and other procedures apply. For example, the decoder checks whether the low-delay condition (e.g. the POCs of all reference pictures of the current picture are smaller than the POC of the current picture) is fulfilled and possibly uses motion vector MV_x (e.g., the motion vector corresponding to reference picture list X) to predict motion vector MV_y (e.g., with X being equal to 0 or 1 and Y being equal to $1-X$) for each sub-CU.

2.1.2 Examples of spatial-temporal motion vector prediction (STMVP)

[0092] In the STMVP method, the motion vectors of the sub-CUs are derived recursively, following raster scan order. FIG. 11 shows an example of one CU with four sub-blocks and neighboring blocks. Consider an 8×8 CU 1100 that includes four 4×4 sub-CUs A (1101), B (1102), C (1103), and D (1104). The neighboring 4×4 blocks in the current frame are labelled as a (1111), b (1112), c (1113), and d (1114).

[0093] The motion derivation for sub-CU A starts by identifying its two spatial neighbors. The first neighbor is the $N \times N$ block above sub-CU A 1101 (block c 1113). If this block c (1113) is not available or is intra coded the other $N \times N$ blocks above sub-CU A (1101) are checked (from left to right, starting at block c 1113). The second neighbor is a block to the left of the sub-CU A 1101 (block b 1112). If block b (1112) is not available or is intra coded other blocks to the left of sub-CU A 1101 are checked (from top to bottom, starting at block b 1112). The motion information obtained from the neighboring blocks for each list is scaled to the first reference frame for a given list. Next, temporal motion vector predictor (TMVP) of sub-block A 1101 is derived by following the same procedure of TMVP derivation as specified in HEVC. The motion information of the collocated block at block D 1104 is fetched and scaled accordingly. Finally, after retrieving and scaling the motion information, all available motion vectors are averaged separately for each reference list. The averaged motion vector is assigned as the motion vector of the current sub-CU.

2.1.3 Examples of sub-CU motion prediction mode signaling

[0094] In some embodiments, the sub-CU modes are enabled as additional merge candidates and there is no additional syntax element required to signal the modes. Two additional merge candidates are added to merge candidates list of each CU to represent the ATMVP mode and STMVP mode. In other embodiments, up to seven merge candidates may be used, if the sequence parameter set indicates that ATMVP and STMVP are enabled. The encoding logic of the additional merge candidates is the same as for the merge candidates in the HM, which means, for each CU in P or B slice, two more RD checks may be needed for the two additional merge candidates. In some embodiments, e.g., JEM, all bins of the merge index are context coded by CABAC (Context-based Adaptive Binary Arithmetic Coding). In other embodiments, e.g., HEVC, only the first bin is context coded and the remaining bins are context by-pass coded.

2.2 Examples of adaptive motion vector difference resolution

[0095] In some embodiments, motion vector differences (MVDs) (between the motion vector and predicted motion vector of a PU) are signalled in units of quarter luma samples when `use_integer_mv_flag` is equal to 0 in the slice header. In the JEM, a locally adaptive motion vector resolution (LAMVR) is introduced. In the JEM, MVD can be coded in units of quarter luma samples, integer luma samples or four luma samples. The MVD resolution is controlled at the coding unit (CU) level, and MVD resolution flags are conditionally signalled for each CU that has at least one non-zero MVD components.

[0096] For a CU that has at least one non-zero MVD components, a first flag is signalled to indicate whether quarter luma sample MV precision is used in the CU. When the first flag (equal to 1) indicates that quarter luma sample MV precision is not used, another flag is signalled to indicate whether integer luma sample MV precision or four luma sample MV precision is used.

[0097] When the first MVD resolution flag of a CU is zero, or not coded for a CU (meaning all MVDs in the CU are zero), the quarter luma sample MV resolution is used for the CU. When a CU uses integer-luma sample MV precision or four-luma-sample MV precision, the MVPs in the AMVP candidate list for the CU are rounded to the corresponding precision.

[0098] In the encoder, CU-level RD checks are used to determine which MVD resolution is to be used for a CU. That is, the CU-level RD check is performed three times for each MVD resolution. To accelerate encoder speed, the following encoding schemes are applied in the JEM:

[0099] -- During RD check of a CU with normal quarter luma sample MVD resolution, the motion information of the current CU (integer luma sample accuracy) is stored. The stored motion information (after rounding) is used as the starting point for further small range motion vector refinement during the RD check for the same CU with integer luma sample and 4 luma sample MVD resolution so that the time-consuming motion estimation process is not duplicated three times.

[00100] -- RD check of a CU with 4 luma sample MVD resolution is conditionally invoked. For a CU, when RD cost integer luma sample MVD resolution is much larger than that of quarter luma sample MVD resolution, the RD check of 4 luma sample MVD resolution for the CU is skipped.

2.3 Examples of higher motion vector storage accuracy

[00101] In HEVC, motion vector accuracy is one-quarter pel (one-quarter luma sample and one-eighth chroma sample for 4:2:0 video). In the JEM, the accuracy for the internal motion

vector storage and the merge candidate increases to 1/16 pel. The higher motion vector accuracy (1/16 pel) is used in motion compensation inter prediction for the CU coded with skip/merge mode. For the CU coded with normal AMVP mode, either the integer-pel or quarter-pel motion is used.

[00102] SHVC upsampling interpolation filters, which have same filter length and normalization factor as HEVC motion compensation interpolation filters, are used as motion compensation interpolation filters for the additional fractional pel positions. The chroma component motion vector accuracy is 1/32 sample in the JEM, the additional interpolation filters of 1/32 pel fractional positions are derived by using the average of the filters of the two neighbouring 1/16 pel fractional positions.

2.4 Examples of overlapped block motion compensation (OBMC)

[00103] In the JEM, OBMC can be switched on and off using syntax at the CU level. When OBMC is used in the JEM, the OBMC is performed for all motion compensation (MC) block boundaries except the right and bottom boundaries of a CU. Moreover, it is applied for both the luma and chroma components. In the JEM, an MC block corresponds to a coding block. When a CU is coded with sub-CU mode (includes sub-CU merge, affine and FRUC mode), each sub-block of the CU is a MC block. To process CU boundaries in a uniform fashion, OBMC is performed at sub-block level for all MC block boundaries, where sub-block size is set equal to 4×4, as shown in FIGS. 12A and 12B.

[00104] FIG. 12A shows sub-blocks at the CU/PU boundary, and the hatched sub-blocks are where OBMC applies. Similarly, FIG. 12B shows the sub-Pus in ATMVP mode.

[00105] When OBMC applies to the current sub-block, besides current motion vectors, motion vectors of four connected neighboring sub-blocks, if available and are not identical to the current motion vector, are also used to derive prediction block for the current sub-block. These multiple prediction blocks based on multiple motion vectors are combined to generate the final prediction signal of the current sub-block.

[00106] Prediction block based on motion vectors of a neighboring sub-block is denoted as PN, with N indicating an index for the neighboring above, below, left and right sub-blocks and prediction block based on motion vectors of the current sub-block is denoted as PC. When PN is based on the motion information of a neighboring sub-block that contains the same motion information to the current sub-block, the OBMC is not performed from PN. Otherwise, every

sample of PN is added to the same sample in PC, i.e., four rows/columns of PN are added to PC. The weighting factors $\{1/4, 1/8, 1/16, 1/32\}$ are used for PN and the weighting factors $\{3/4, 7/8, 15/16, 31/32\}$ are used for PC. The exception are small MC blocks, (i.e., when height or width of the coding block is equal to 4 or a CU is coded with sub-CU mode), for which only two rows/columns of PN are added to PC. In this case weighting factors $\{1/4, 1/8\}$ are used for PN and weighting factors $\{3/4, 7/8\}$ are used for PC. For PN generated based on motion vectors of vertically (horizontally) neighboring sub-block, samples in the same row (column) of PN are added to PC with a same weighting factor.

[00107] In the JEM, for a CU with size less than or equal to 256 luma samples, a CU level flag is signaled to indicate whether OBMC is applied or not for the current CU. For the CUs with size larger than 256 luma samples or not coded with AMVP mode, OBMC is applied by default. At the encoder, when OBMC is applied for a CU, its impact is taken into account during the motion estimation stage. The prediction signal formed by OBMC using motion information of the top neighboring block and the left neighboring block is used to compensate the top and left boundaries of the original signal of the current CU, and then the normal motion estimation process is applied.

2.5 Examples of local illumination compensation (LIC)

[00108] LIC is based on a linear model for illumination changes, using a scaling factor a and an offset b . And it is enabled or disabled adaptively for each inter-mode coded coding unit (CU).

[00109] When LIC applies for a CU, a least square error method is employed to derive the parameters a and b by using the neighboring samples of the current CU and their corresponding reference samples. FIG. 13 shows an example of neighboring samples used to derive parameters of the IC algorithm. Specifically, and as shown in FIG. 13, the subsampled (2:1 subsampling) neighbouring samples of the CU and the corresponding samples (identified by motion information of the current CU or sub-CU) in the reference picture are used. The IC parameters are derived and applied for each prediction direction separately.

[00110] When a CU is coded with merge mode, the LIC flag is copied from neighboring blocks, in a way similar to motion information copy in merge mode; otherwise, an LIC flag is signaled for the CU to indicate whether LIC applies or not.

[00111] When LIC is enabled for a picture, an additional CU level RD check is needed to determine whether LIC is applied or not for a CU. When LIC is enabled for a CU, the mean-

removed sum of absolute difference (MR-SAD) and mean-removed sum of absolute Hadamard-transformed difference (MR-SATD) are used, instead of SAD and SATD, for integer pel motion search and fractional pel motion search, respectively.

[00112] To reduce the encoding complexity, the following encoding scheme is applied in the JEM:

[00113] -- LIC is disabled for the entire picture when there is no obvious illumination change between a current picture and its reference pictures. To identify this situation, histograms of a current picture and every reference picture of the current picture are calculated at the encoder. If the histogram difference between the current picture and every reference picture of the current picture is smaller than a given threshold, LIC is disabled for the current picture; otherwise, LIC is enabled for the current picture.

2.6 Examples of affine motion compensation prediction

[00114] In HEVC, only a translation motion model is applied for motion compensation prediction (MCP). However, the camera and objects may have many kinds of motion, e.g. zoom in/out, rotation, perspective motions, and/or other irregular motions. JEM, on the other hand, applies a simplified affine transform motion compensation prediction. FIG. 14 shows an example of an affine motion field of a block 1400 described by two control point motion vectors V_0 and V_1 . The motion vector field (MVF) of the block 1400 can be described by the following equation:

$$[00115] \quad \begin{cases} v_x = \frac{(v_{1x} - v_{0x})}{w} x - \frac{(v_{1y} - v_{0y})}{w} y + v_{0x} \\ v_y = \frac{(v_{1y} - v_{0y})}{w} x + \frac{(v_{1x} - v_{0x})}{w} y + v_{0y} \end{cases} \quad \text{Eq. (1)}$$

[00116] As shown in FIG. 14, (v_{0x}, v_{0y}) is motion vector of the top-left corner control point, and (v_{1x}, v_{1y}) is motion vector of the top-right corner control point. To simplify the motion compensation prediction, sub-block based affine transform prediction can be applied. The sub-block size $M \times N$ is derived as follows:

$$[00117] \quad \begin{cases} M = \text{clip3}(4, w, \frac{w \times MvPre}{\max(\text{abs}(v_{1x} - v_{0x}), \text{abs}(v_{1y} - v_{0y}))}) \\ N = \text{clip3}(4, h, \frac{h \times MvPre}{\max(\text{abs}(v_{2x} - v_{0x}), \text{abs}(v_{2y} - v_{0y}))}) \end{cases} \quad \text{Eq. (2)}$$

[00118] Here, $MvPre$ is the motion vector fraction accuracy (e.g., 1/16 in JEM). (v_{2x}, v_{2y}) is

motion vector of the bottom-left control point, calculated according to Eq. (1). M and N can be adjusted downward if necessary to make it a divisor of w and h, respectively.

[00119] FIG. 15 shows an example of affine MVF per sub-block for a block 1500. To derive motion vector of each M×N sub-block, the motion vector of the center sample of each sub-block can be calculated according to Eq. (1), and rounded to the motion vector fraction accuracy (e.g., 1/16 in JEM). Then the motion compensation interpolation filters can be applied to generate the prediction of each sub-block with derived motion vector. After the MCP, the high accuracy motion vector of each sub-block is rounded and saved as the same accuracy as the normal motion vector.

2.6.1 Embodiments of the AF_INTER mode

[00120] In the JEM, there are two affine motion modes: AF_INTER mode and AF_MERGE mode. For CUs with both width and height larger than 8, AF_INTER mode can be applied. An affine flag in CU level is signaled in the bitstream to indicate whether AF_INTER mode is used. In the AF_INTER mode, a candidate list with motion vector pair $\{(v_0, v_1) | v_0 = \{v_A, v_B, v_C\}, v_1 = \{v_D, v_E\}\}$ is constructed using the neighboring blocks.

[00121] FIG. 16 shows an example of motion vector prediction (MVP) for a block 1600 in the AF_INTER mode. As shown in FIG. 16, v_0 is selected from the motion vectors of the sub-block A, B, or C. The motion vectors from the neighboring blocks can be scaled according to the reference list. The motion vectors can also be scaled according to the relationship among the Picture Order Count (POC) of the reference for the neighboring block, the POC of the reference for the current CU, and the POC of the current CU. The approach to select v_1 from the neighboring sub-block D and E is similar. If the number of candidate list is smaller than 2, the list is padded by the motion vector pair composed by duplicating each of the AMVP candidates. When the candidate list is larger than 2, the candidates can be firstly sorted according to the neighboring motion vectors (e.g., based on the similarity of the two motion vectors in a pair candidate). In some implementations, the first two candidates are kept. In some embodiments, a Rate Distortion (RD) cost check is used to determine which motion vector pair candidate is selected as the control point motion vector prediction (CPMVP) of the current CU. An index indicating the position of the CPMVP in the candidate list can be signaled in the bitstream. After the CPMVP of the current affine CU is determined, affine motion estimation is applied and the

control point motion vector (CPMV) is found. Then the difference of the CPMV and the CPMVP is signaled in the bitstream.

2.6.3 Embodiments of the AF_MERGE mode

[00122] When a CU is applied in AF_MERGE mode, it gets the first block coded with an affine mode from the valid neighboring reconstructed blocks. FIG. 17A shows an example of the selection order of candidate blocks for a current CU 1700. As shown in FIG. 17A, the selection order can be from left (1701), above (1702), above right (1703), left bottom (1704) to above left (1705) of the current CU 1700. FIG. 17B shows another example of candidate blocks for a current CU 1700 in the AF_MERGE mode. If the neighboring left bottom block 1801 is coded in affine mode, as shown in FIG. 17B, the motion vectors v_2 , v_3 and v_4 of the top left corner, above right corner, and left bottom corner of the CU containing the sub-block 1701 are derived. The motion vector v_0 of the top left corner on the current CU 1700 is calculated based on v_2 , v_3 and v_4 . The motion vector v_1 of the above right of the current CU can be calculated accordingly.

[00123] After the CPMV of the current CU v_0 and v_1 are computed according to the affine motion model in Eq. (1), the MVF of the current CU can be generated. In order to identify whether the current CU is coded with AF_MERGE mode, an affine flag can be signaled in the bitstream when there is at least one neighboring block is coded in affine mode.

2.7 Examples of pattern matched motion vector derivation (PMMVD)

[00124] The PMMVD mode is a special merge mode based on the Frame-Rate Up Conversion (FRUC) method. With this mode, motion information of a block is not signaled but derived at decoder side.

[00125] A FRUC flag can be signaled for a CU when its merge flag is true. When the FRUC flag is false, a merge index can be signaled and the regular merge mode is used. When the FRUC flag is true, an additional FRUC mode flag can be signaled to indicate which method (e.g., bilateral matching or template matching) is to be used to derive motion information for the block.

[00126] At the encoder side, the decision on whether using FRUC merge mode for a CU is based on RD cost selection as done for normal merge candidate. For example, multiple matching modes (e.g., bilateral matching and template matching) are checked for a CU by using RD cost selection. The one leading to the minimal cost is further compared to other CU modes. If a FRUC matching mode is the most efficient one, FRUC flag is set to true for the CU and the related matching mode is used.

[00127] Typically, motion derivation process in FRUC merge mode has two steps: a CU-level motion search is first performed, then followed by a Sub-CU level motion refinement. At CU level, an initial motion vector is derived for the whole CU based on bilateral matching or template matching. First, a list of MV candidates is generated and the candidate that leads to the minimum matching cost is selected as the starting point for further CU level refinement. Then a local search based on bilateral matching or template matching around the starting point is performed. The MV results in the minimum matching cost is taken as the MV for the whole CU. Subsequently, the motion information is further refined at sub-CU level with the derived CU motion vectors as the starting points.

[00128] For example, the following derivation process is performed for a $W \times H$ CU motion information derivation. At the first stage, MV for the whole $W \times H$ CU is derived. At the second stage, the CU is further split into $M \times M$ sub-CUs. The value of M is calculated as in Eq. (3), D is a predefined splitting depth which is set to 3 by default in the JEM. Then the MV for each sub-CU is derived.

$$[00129] \quad M = \max\{4, \min\left\{\frac{M}{2^D}, \frac{N}{2^D}\right\}\} \quad \text{Eq. (3)}$$

[00130] FIG. 18 shows an example of bilateral matching used in the Frame-Rate Up Conversion (FRUC) method. The bilateral matching is used to derive motion information of the current CU by finding the closest match between two blocks along the motion trajectory of the current CU (1800) in two different reference pictures (1810, 1811). Under the assumption of continuous motion trajectory, the motion vectors MV0 (1801) and MV1 (1802) pointing to the two reference blocks are proportional to the temporal distances, e.g., TD0 (1803) and TD1 (1804), between the current picture and the two reference pictures. In some embodiments, when the current picture 1800 is temporally between the two reference pictures (1810, 1811) and the temporal distance from the current picture to the two reference pictures is the same, the bilateral matching becomes mirror based bi-directional MV.

[00131] FIG. 19 shows an example of template matching used in the Frame-Rate Up Conversion (FRUC) method. Template matching can be used to derive motion information of the current CU 1900 by finding the closest match between a template (e.g., top and/or left neighboring blocks of the current CU) in the current picture and a block (e.g., same size to the template) in a reference picture 1910. Except the aforementioned FRUC merge mode, the

template matching can also be applied to AMVP mode. In both JEM and HEVC, AMVP has two candidates. With the template matching method, a new candidate can be derived. If the newly derived candidate by template matching is different to the first existing AMVP candidate, it is inserted at the very beginning of the AMVP candidate list and then the list size is set to two (e.g., by removing the second existing AMVP candidate). When applied to AMVP mode, only CU level search is applied.

[00132] The MV candidate set at CU level can include the following: (1) original AMVP candidates if the current CU is in AMVP mode, (2) all merge candidates, (3) several MVs in the interpolated MV field (described later), and top and left neighboring motion vectors.

[00133] When using bilateral matching, each valid MV of a merge candidate can be used as an input to generate a MV pair with the assumption of bilateral matching. For example, one valid MV of a merge candidate is (MV_a, ref_a) at reference list A. Then the reference picture ref_b of its paired bilateral MV is found in the other reference list B so that ref_a and ref_b are temporally at different sides of the current picture. If such a ref_b is not available in reference list B, ref_b is determined as a reference which is different from ref_a and its temporal distance to the current picture is the minimal one in list B. After ref_b is determined, MV_b is derived by scaling MV_a based on the temporal distance between the current picture and ref_a, ref_b .

[00134] In some implementations, four MVs from the interpolated MV field can also be added to the CU level candidate list. More specifically, the interpolated MVs at the position $(0, 0)$, $(W/2, 0)$, $(0, H/2)$ and $(W/2, H/2)$ of the current CU are added. When FRUC is applied in AMVP mode, the original AMVP candidates are also added to CU level MV candidate set. In some implementations, at the CU level, 15 MVs for AMVP CUs and 13 MVs for merge CUs can be added to the candidate list.

[00135] The MV candidate set at sub-CU level includes an MV determined from a CU-level search, (2) top, left, top-left and top-right neighboring MVs, (3) scaled versions of collocated MVs from reference pictures, (4) one or more ATMVP candidates (e.g., up to four), and (5) one or more STMVP candidates (e.g., up to four). The scaled MVs from reference pictures are derived as follows. The reference pictures in both lists are traversed. The MVs at a collocated position of the sub-CU in a reference picture are scaled to the reference of the starting CU-level MV. ATMVP and STMVP candidates can be the four first ones. At the sub-CU level, one or more MVs (e.g., up to 17) are added to the candidate list.

[00136] Generation of an interpolated MV field. Before coding a frame, interpolated motion field is generated for the whole picture based on unilateral ME. Then the motion field may be used later as CU level or sub-CU level MV candidates.

[00137] In some embodiments, the motion field of each reference pictures in both reference lists is traversed at 4×4 block level. FIG. 20 shows an example of unilateral Motion Estimation (ME) 2000 in the FRUC method. For each 4×4 block, if the motion associated to the block passing through a 4×4 block in the current picture and the block has not been assigned any interpolated motion, the motion of the reference block is scaled to the current picture according to the temporal distance TD0 and TD1 (the same way as that of MV scaling of TMVP in HEVC) and the scaled motion is assigned to the block in the current frame. If no scaled MV is assigned to a 4×4 block, the block's motion is marked as unavailable in the interpolated motion field.

[00138] Interpolation and matching cost. When a motion vector points to a fractional sample position, motion compensated interpolation is needed. To reduce complexity, bi-linear interpolation instead of regular 8-tap HEVC interpolation can be used for both bilateral matching and template matching.

[00139] The calculation of matching cost is a bit different at different steps. When selecting the candidate from the candidate set at the CU level, the matching cost can be the absolute sum difference (SAD) of bilateral matching or template matching. After the starting MV is determined, the matching cost C of bilateral matching at sub-CU level search is calculated as follows:

$$[00140] \quad C = SAD + w \cdot (|MV_x - MV_x^s| + |MV_y - MV_y^s|) \quad \text{Eq. (4)}$$

[00141] Here, w is a weighting factor. In some embodiments, w can be empirically set to 4. MV and MV^s indicate the current MV and the starting MV, respectively. SAD may still be used as the matching cost of template matching at sub-CU level search.

[00142] In FRUC mode, MV is derived by using luma samples only. The derived motion will be used for both luma and chroma for MC inter prediction. After MV is decided, final MC is performed using 8-taps interpolation filter for luma and 4-taps interpolation filter for chroma.

[00143] MV refinement is a pattern based MV search with the criterion of bilateral matching cost or template matching cost. In the JEM, two search patterns are supported – an unrestricted center-biased diamond search (UCBDS) and an adaptive cross search for MV refinement at the CU level and sub-CU level, respectively. For both CU and sub-CU level MV refinement, the MV

is directly searched at quarter luma sample MV accuracy, and this is followed by one-eighth luma sample MV refinement. The search range of MV refinement for the CU and sub-CU step are set equal to 8 luma samples.

[00144] In the bilateral matching merge mode, bi-prediction is applied because the motion information of a CU is derived based on the closest match between two blocks along the motion trajectory of the current CU in two different reference pictures. In the template matching merge mode, the encoder can choose among uni-prediction from list0, uni-prediction from list1, or bi-prediction for a CU. The selection can be based on a template matching cost as follows:

[00145] If $\text{costBi} \leq \text{factor} * \min(\text{cost0}, \text{cost1})$

[00146] bi-prediction is used;

[00147] Otherwise, if $\text{cost0} \leq \text{cost1}$

[00148] uni-prediction from list0 is used;

[00149] Otherwise,

[00150] uni-prediction from list1 is used;

[00151] Here, cost0 is the SAD of list0 template matching, cost1 is the SAD of list1 template matching and costBi is the SAD of bi-prediction template matching. For example, when the value of factor is equal to 1.25, it means that the selection process is biased toward bi-prediction. The inter prediction direction selection can be applied to the CU-level template matching process.

2.8 Examples of generalized bi-prediction (GBI)

[00152] In conventional bi-prediction, the predictors from L0 and L1 are averaged to generate the final predictor using the equal weight 0.5. The predictor generation formula is shown as in Equation (5).

[00153]
$$P_{\text{TraditionalBiPred}} = (P_{L0} + P_{L1} + \text{RoundingOffset}) \gg \text{shiftNum} \quad \text{Eq. (5)}$$

[00154] In Equation (5), $P_{\text{TraditionalBiPred}}$ is the final predictor for the conventional bi-prediction, P_{L0} and P_{L1} are predictors from L0 and L1, respectively, and RoundingOffset and shiftNum are used to normalize the final predictor.

[00155] Generalized bi-prediction (GBI) proposes to allow applying different weights to predictors from L0 and L1. GBI is also referred to as bi-prediction with CU weights (BCW). The predictor generation is shown in Equation (6).

[00156]
$$P_{\text{GBi}} = ((1-w_1) * P_{L0} + w_1 * P_{L1} + \text{RoundingOffset}_{\text{GBi}}) \gg \text{shiftNum}_{\text{GBi}}, \quad \text{Eq. (6)}$$

[00157] In Equation (6), P_{GBi} is the final predictor of GBi. $(1-w_l)$ and w_l are the selected GBI weights applied to the predictors of L0 and L1, respectively. $RoundingOffset_{GBi}$ and $shiftNum_{GBi}$ are used to normalize the final predictor in GBi.

[00158] The supported weights of w_l is $\{-1/4, 3/8, 1/2, 5/8, 5/4\}$. One equal-weight set and four unequal-weight sets are supported. For the equal-weight case, the process to generate the final predictor is exactly the same as that in the conventional bi-prediction mode. For the true bi-prediction cases in random access (RA) condition, the number of candidate weight sets is reduced to three.

[00159] For advanced motion vector prediction (AMVP) mode, the weight selection in GBI is explicitly signaled at CU-level if this CU is coded by bi-prediction. For merge mode, the weight selection is inherited from the merge candidate. In this proposal, GBI supports DMVR to generate the weighted average of template as well as the final predictor for BMS-1.0.

2.9 Examples of multi-hypothesis inter prediction

[00160] In the multi-hypothesis inter prediction mode, one or more additional prediction signals are signaled, in addition to the conventional uni/bi prediction signal. The resulting overall prediction signal is obtained by sample-wise weighted superposition. With the uni/bi prediction signal $p_{uni/bi}$ and the first additional inter prediction signal/hypothesis h_3 , the resulting prediction signal p_3 is obtained as follows:

$$[00161] \quad p_3 = (1 - \alpha)p_{uni/bi} + \alpha h_3$$

[00162] The changes to the prediction unit syntax structure are shown below:

prediction_unit(x0, y0, nPbW, nPbH) {	Descriptor
...	
if(! cu_skip_flag[x0][y0]) {	
i = 0	
readMore = 1	
while(i < MaxNumAdditionalHypotheses && readMore) {	
additional_hypothesis_flag [x0][y0][i]	ae(v)
if(additional_hypothesis_flag[x0][y0][i]) {	
ref_idx_add_hyp [x0][y0][i]	ae(v)
mvd_coding(x0, y0, 2+i)	
mvp_add_hyp_flag [x0][y0][i]	ae(v)
add_hyp_weight_idx [x0][y0][i]	ae(v)
}	
readMore = additional_hypothesis_flag[x0][y0][i]	
i++	
}	
}	
}	

[00163] The weighting factor α is specified by the syntax element *add_hyp_weight_idx*, according to the following mapping:

add_hyp_weight_idx	α
0	1/4
1	-1/8

[00164] In some embodiments, and for the additional prediction signals, the concept of prediction list0/list1 is abolished, and instead one combined list is used. This combined list is generated by alternately inserting reference frames from list0 and list1 with increasing reference index, omitting reference frames which have already been inserted, such that double entries are avoided.

[00165] In some embodiments, and analogously to the above, more than one additional prediction signals can be used. The resulting overall prediction signal is accumulated iteratively with each additional prediction signal.

[00166]
$$p_{n+1} = (1 - \alpha_{n+1})p_n + \alpha_{n+1}h_{n+1}$$

[00167] The resulting overall prediction signal is obtained as the last p_n (i.e., the p_n having the largest index n).

[00168] Note that also for inter prediction blocks using MERGE mode (but not SKIP mode),

additional inter prediction signals can be specified. Further note, that in case of MERGE, not only the uni/bi prediction parameters, but also the additional prediction parameters of the selected merging candidate can be used for the current block.

2.10 Examples of multi-hypothesis prediction for uni-prediction of AMVP mode

[00169] In some embodiments, when the multi-hypothesis prediction is applied to improve uni-prediction of AMVP mode, one flag is signaled to enable or disable multi-hypothesis prediction for `inter_dir` equal to 1 or 2, where 1, 2, and 3 represent list 0, list 1, and bi-prediction, respectively. Moreover, one more merge index is signaled when the flag is true. In this way, multi-hypothesis prediction turns uni-prediction into bi-prediction, where one motion is acquired using the original syntax elements in AMVP mode while the other is acquired using the merge scheme. The final prediction uses 1:1 weights to combine these two predictions as in bi-prediction. The merge candidate list is first derived from merge mode with sub-CU candidates (e.g., affine, alternative temporal motion vector prediction (ATMVP)) excluded. Next, it is separated into two individual lists, one for list 0 (L0) containing all L0 motions from the candidates, and the other for list 1 (L1) containing all L1 motions. After removing redundancy and filling vacancy, two merge lists are generated for L0 and L1 respectively. There are two constraints when applying multi-hypothesis prediction for improving AMVP mode. First, it is enabled for those CUs with the luma coding block (CB) area larger than or equal to 64. Second, it is only applied to L1 when in low delay B pictures.

2.11 Examples of multi-hypothesis prediction for skip/merge mode

[00170] In some embodiments, when the multi-hypothesis prediction is applied to skip or merge mode, whether to enable multi-hypothesis prediction is explicitly signaled. An extra merge indexed prediction is selected in addition to the original one. Therefore, each candidate of multi-hypothesis prediction implies a pair of merge candidates, containing one for the 1st merge indexed prediction and the other for the 2nd merge indexed prediction. However, in each pair, the merge candidate for the 2nd merge indexed prediction is implicitly derived as the succeeding merge candidate (i.e., the already signaled merge index plus one) without signaling any additional merge index. After removing redundancy by excluding those pairs, containing similar merge candidates and filling vacancy, the candidate list for multi-hypothesis prediction is formed. Then, motions from a pair of two merge candidates are acquired to generate the final prediction, where 5:3 weights are applied to the 1st and 2nd merge indexed predictions,

respectively. Moreover, a merge or skip CU with multi-hypothesis prediction enabled can save the motion information of the additional hypotheses for reference of the following neighboring CUs in addition to the motion information of the existing hypotheses.

[00171] Note that sub-CU candidates (e.g., affine, ATMVP) are excluded from the candidate list, and for low delay B pictures, multi-hypothesis prediction is not applied to skip mode.

Moreover, when multi-hypothesis prediction is applied to merge or skip mode, for those CUs with CU width or CU height less than 16, or those CUs with both CU width and CU height equal to 16, bi-linear interpolation filter is used in motion compensation for multiple hypotheses.

Therefore, the worst-case bandwidth (required access samples per sample) for each merge or skip CU with multi-hypothesis prediction enabled is less than half of the worst-case bandwidth for each 4x4 CU with multi-hypothesis prediction disabled.

2.12 Examples of ultimate motion vector expression (UMVE)

[00172] In some embodiments, ultimate motion vector expression (UMVE) is presented.

UMVE is used for either skip or merge modes with a proposed motion vector expression method.

[00173] UMVE re-uses merge candidate as same as using in VVC. Among the merge candidates, a candidate can be selected, and is further expanded by the proposed motion vector expression method.

[00174] UMVE provides a new motion vector expression with simplified signaling. The expression method includes starting point, motion magnitude, and motion direction. This proposed technique uses a merge candidate list as it is. But only candidates which are default merge type (MRG_TYPE_DEFAULT_N) are considered for UMVE's expansion.

[00175] Base candidate index defines the starting point. Base candidate index indicates the best candidate among candidates in the list as follows.

Base candidate IDX	0	1	2	3
Nth MVP	1 st MVP	2 nd MVP	3 rd MVP	4 th MVP

[00176] If the number of base candidate is equal to 1, Base candidate IDX is not signaled.

[00177] Distance index is motion magnitude information. Distance index indicates the pre-defined distance from the starting point information. Pre-defined distance is as follows:

Distance IDX	0	1	2	3	4	5	6	7
Pixel distance	1/4-pel	1/2-pel	1-pel	2-pel	4-pel	8-pel	16-pel	32-pel

[00178] Direction index represents the direction of the MVD relative to the starting point. The direction index can represent of the four directions as shown below:

Direction IDX	00	01	10	11
x-axis	+	-	N/A	N/A
y-axis	N/A	N/A	+	-

[00179] UMVE flag is signaled right after sending a skip flag and merge flag. If skip and merge flag is true, UMVE flag is parsed. If UMVE flag is equal to 1, UMVE syntaxes are parsed. But, if not 1, AFFINE flag is parsed. If AFFINE flag is equal to 1, that is AFFINE mode, But, if not 1, skip/merge index is parsed for VTM's skip/merge mode.

[00180] Additional line buffer due to UMVE candidates is not needed. Because a skip/merge candidate of software is directly used as a base candidate. Using input UMVE index, the supplement of MV is decided right before motion compensation. There is no need to hold long line buffer for this.

2.13 Examples of affine merge mode with prediction offsets

[00181] In some embodiments, UMVE is extended to affine merge mode, we will call this UMVE affine mode thereafter. The proposed method selects the first available affine merge candidate as a base predictor. Then it applies a motion vector offset to each control point's motion vector value from the base predictor. If there's no affine merge candidate available, this proposed method will not be used.

[00182] The selected base predictor's inter prediction direction, and the reference index of each direction is used without change.

[00183] In the current implementation, the current block's affine model is assumed to be a 4-parameter model, only 2 control points need to be derived. Thus, only the first 2 control points of the base predictor will be used as control point predictors.

[00184] For each control point, a zero_MVD flag is used to indicate whether the control point of current block has the same MV value as the corresponding control point predictor. If zero_MVD flag is true, there's no other signaling needed for the control point. Otherwise, a distance index and an offset direction index is signaled for the control point.

[00185] A distance offset table with size of 5 is used as shown in the table below.

Distance IDX	0	1	2	3	4
Distance-offset	1/2-pel	1-pel	2-pel	4-pel	8-pel

[00186] Distance index is signaled to indicate which distance offset to use. The mapping of distance index and distance offset values is shown in FIG. 23.

[00187] The direction index can represent four directions as shown below, where only x or y direction may have an MV difference, but not in both directions.

Offset Direction IDX	00	01	10	11
x-dir-factor	+1	-1	0	0
y-dir-factor	0	0	+1	-1

[00188] If the inter prediction is uni-directional, the signaled distance offset is applied on the offset direction for each control point predictor. Results will be the MV value of each control point.

[00189] For example, when base predictor is uni-directional, and the motion vector values of a control point is MVP (v_{px} , v_{py}). When distance offset and direction index are signaled, the motion vectors of current block's corresponding control points will be calculated as below.

$$[00190] \quad MV(v_x, v_y) = MVP(v_{px}, v_{py}) \\ + MV(\text{x-dir-factor} * \text{distance-offset}, \text{y-dir-factor} * \text{distance-offset})$$

[00191] If the inter prediction is bi-directional, the signaled distance offset is applied on the signaled offset direction for control point predictor's L0 motion vector; and the same distance offset with opposite direction is applied for control point predictor's L1 motion vector. Results will be the MV values of each control point, on each inter prediction direction.

[00192] For example, when base predictor is uni-directional, and the motion vector values of a control point on L0 is MVP_{L0} (v_{0px} , v_{0py}), and the motion vector of that control point on L1 is MVP_{L1} (v_{1px} , v_{1py}). When distance offset and direction index are signaled, the motion vectors of current block's corresponding control points will be calculated as below.

$$[00193] \quad MV_{L0}(v_{0x}, v_{0y}) = MVP_{L0}(v_{0px}, v_{0py}) \\ + MV(\text{x-dir-factor} * \text{distance-offset}, \text{y-dir-factor} * \text{distance-offset})$$

$$[00194] \quad MV_{L1}(v_{1x}, v_{1y}) = MVP_{L1}(v_{1px}, v_{1py}) \\ + MV(-\text{x-dir-factor} * \text{distance-offset}, -\text{y-dir-factor} * \text{distance-offset})$$

2.14 Examples of bi-directional optical flow (BIO)

[00195] The bi-directional optical flow (BIO) method is a sample-wise motion refinement performed on top of block-wise motion compensation for bi-prediction. In some implementations, the sample-level motion refinement does not use signaling.

[00196] Let $I^{(k)}$ be the luma value from reference k ($k=0, 1$) after block motion compensation, and denote $\partial I^{(k)}/\partial x$ and $\partial I^{(k)}/\partial y$ as the horizontal and vertical components of the $I^{(k)}$ gradient, respectively. Assuming the optical flow is valid, the motion vector field (v_x, v_y) is given by:

$$[00197] \quad \partial I^{(k)}/\partial t + v_x \partial I^{(k)}/\partial x + v_y \partial I^{(k)}/\partial y = 0. \quad \text{Eq. (5)}$$

[00198] Combining this optical flow equation with Hermite interpolation for the motion trajectory of each sample results in a unique third-order polynomial that matches both the function values $I^{(k)}$ and derivatives $\partial I^{(k)}/\partial x$ and $\partial I^{(k)}/\partial y$ at the ends. The value of this polynomial at $t=0$ is the BIO prediction:

$$[00199] \quad pred_{BIO} = 1/2 \cdot (I^{(0)} + I^{(1)} + v_x/2 \cdot (\tau_1 \partial I^{(1)}/\partial x - \tau_0 \partial I^{(0)}/\partial x) + v_y/2 \cdot (\tau_1 \partial I^{(1)}/\partial y - \tau_0 \partial I^{(0)}/\partial y)) \quad \text{Eq. (6)}$$

[00200] FIG. 24 shows an example optical flow trajectory in the Bi-directional Optical flow (BIO) method. Here, τ_0 and τ_1 denote the distances to the reference frames. Distances τ_0 and τ_1 are calculated based on POC for Ref₀ and Ref₁: $\tau_0 = \text{POC}(\text{current}) - \text{POC}(\text{Ref}_0)$, $\tau_1 = \text{POC}(\text{Ref}_1) - \text{POC}(\text{current})$. If both predictions come from the same time direction (either both from the past or both from the future) then the signs are different (e.g., $\tau_0 \cdot \tau_1 < 0$). In this case, BIO is applied if the prediction is not from the same time moment (e.g., $\tau_0 \neq \tau_1$). Both referenced regions have non-zero motion (e.g., $MVx_0, MVy_0, MVx_1, MVy_1 \neq 0$) and the block motion vectors are proportional to the time distance (e.g., $MVx_0/MVx_1 = MVy_0/MVy_1 = -\tau_0/\tau_1$).

[00201] The motion vector field (v_x, v_y) is determined by minimizing the difference Δ between values in points A and B. FIGS. 9A-9B show an example of intersection of motion trajectory and reference frame planes. Model uses only first linear term of a local Taylor expansion for Δ :

$$[00202] \quad \Delta = (I^{(0)} - I^{(1)}_0 + v_x (\tau_1 \partial I^{(1)}/\partial x + \tau_0 \partial I^{(0)}/\partial x) + v_y (\tau_1 \partial I^{(1)}/\partial y + \tau_0 \partial I^{(0)}/\partial y)) \quad \text{Eq. (7)}$$

[00203] All values in the above equation depend on the sample location, denoted as (i', j') . Assuming the motion is consistent in the local surrounding area, Δ can be minimized inside the $(2M+1) \times (2M+1)$ square window Ω centered on the currently predicted point (i, j) , where M is equal to 2:

$$[00204] \quad (v_x, v_y) = \underset{v_x, v_y}{\operatorname{argmin}} \sum_{[i', j'] \in \Omega} \Delta^2 [i', j'] \quad \text{Eq. (8)}$$

[00205] For this optimization problem, the JEM uses a simplified approach making first a minimization in the vertical direction and then in the horizontal direction. This results in the

following:

$$[00206] \quad v_x = (s_1 + r) > m? \text{clip3} \left(-thBIO, thBIO, -\frac{s_3}{(s_1+r)} \right) : 0 \quad \text{Eq. (9)}$$

$$[00207] \quad v_y = (s_5 + r) > m? \text{clip3} \left(-thBIO, thBIO, -\frac{s_6 - v_x s_2 / 2}{(s_5+r)} \right) : 0 \quad \text{Eq. (10)}$$

[00208] where,

$$[00209] \quad \begin{aligned} s_1 &= \sum_{[i,j] \in \Omega} (\tau_1 \partial I^{(1)} / \partial x + \tau_0 \partial I^{(0)} / \partial x)^2; s_3 = \sum_{[i,j] \in \Omega} (I^{(1)} - I^{(0)}) (\tau_1 \partial I^{(1)} / \partial x + \tau_0 \partial I^{(0)} / \partial x); \\ s_2 &= \sum_{[i,j] \in \Omega} (\tau_1 \partial I^{(1)} / \partial x + \tau_0 \partial I^{(0)} / \partial x) (\tau_1 \partial I^{(1)} / \partial y + \tau_0 \partial I^{(0)} / \partial y); \\ s_5 &= \sum_{[i,j] \in \Omega} (\tau_1 \partial I^{(1)} / \partial y + \tau_0 \partial I^{(0)} / \partial y)^2; s_6 = \sum_{[i,j] \in \Omega} (I^{(1)} - I^{(0)}) (\tau_1 \partial I^{(1)} / \partial y + \tau_0 \partial I^{(0)} / \partial y) \end{aligned} \quad \text{Eq. (11)}$$

[00210] In order to avoid division by zero or a very small value, regularization parameters r and m can be introduced in Eq. (9) and Eq. (10), where:

$$[00211] \quad r = 500 \cdot 4^{d-8} \quad \text{Eq. (12)}$$

$$[00212] \quad m = 700 \cdot 4^{d-8} \quad \text{Eq. (13)}$$

[00213] Here, d is bit depth of the video samples.

[00214] In order to keep the memory access for BIO the same as for regular bi-predictive motion compensation, all prediction and gradients values, $I^{(k)}$, $\partial I^{(k)} / \partial x$, $\partial I^{(k)} / \partial y$, are calculated for positions inside the current block. FIG. 25A shows an example of access positions outside of a block 2500. As shown in FIG. 25A, in Eq. (9), $(2M+1) \times (2M+1)$ square window Ω centered in currently predicted point on a boundary of predicted block needs to access positions outside of the block. In the JEM, values of $I^{(k)}$, $\partial I^{(k)} / \partial x$, $\partial I^{(k)} / \partial y$ outside of the block are set to be equal to the nearest available value inside the block. For example, this can be implemented as a padding area 2501, as shown in FIG. 25B.

[00215] With BIO, it is possible that the motion field can be refined for each sample. To reduce the computational complexity, a block-based design of BIO is used in the JEM. The motion refinement can be calculated based on a 4×4 block. In the block-based BIO, the values of s_n in Eq. (9) of all samples in a 4×4 block can be aggregated, and then the aggregated values of s_n are used to derived BIO motion vectors offset for the 4×4 block. More specifically, the following formula can be used for block-based BIO derivation:

$$\begin{aligned}
& s_{1,b_k} = \sum_{(x,y) \in b_k} \sum_{[i',j'] \in \Omega(x,y)} (\tau_1 \partial I^{(1)} / \partial x + \tau_0 \partial I^{(0)} / \partial x)^2; s_{3,b_k} = \sum_{(x,y) \in b_k} \sum_{[i',j'] \in \Omega} (I^{(1)} - I^{(0)}) (\tau_1 \partial I^{(1)} / \partial x + \tau_0 \partial I^{(0)} / \partial x), \\
\text{[00216]} \quad & s_{2,b_k} = \sum_{(x,y) \in b_k} \sum_{[i',j'] \in \Omega} (\tau_1 \partial I^{(1)} / \partial x + \tau_0 \partial I^{(0)} / \partial x) (\tau_1 \partial I^{(1)} / \partial y + \tau_0 \partial I^{(0)} / \partial y), \\
& s_{5,b_k} = \sum_{(x,y) \in b_k} \sum_{[i',j'] \in \Omega} (\tau_1 \partial I^{(1)} / \partial y + \tau_0 \partial I^{(0)} / \partial y)^2; s_{6,b_k} = \sum_{(x,y) \in b_k} \sum_{[i',j'] \in \Omega} (I^{(1)} - I^{(0)}) (\tau_1 \partial I^{(1)} / \partial y + \tau_0 \partial I^{(0)} / \partial y)
\end{aligned} \tag{Eq. (14)}$$

[00217] Here, b_k denotes the set of samples belonging to the k -th 4×4 block of the predicted block. s_n in Eq (9) and Eq (10) are replaced by $((s_{n,b_k}) \gg 4)$ to derive the associated motion vector offsets.

[00218] In some scenarios, MV regiment of BIO may be unreliable due to noise or irregular motion. Therefore, in BIO, the magnitude of MV regiment is clipped to a threshold value. The threshold value is determined based on whether the reference pictures of the current picture are all from one direction. For example, if all the reference pictures of the current picture are from one direction, the value of the threshold is set to $12 \times 2^{14-d}$; otherwise, it is set to $12 \times 2^{13-d}$.

[00219] Gradients for BIO can be calculated at the same time with motion compensation interpolation using operations consistent with HEVC motion compensation process (e.g., 2D separable Finite Impulse Response (FIR)). In some embodiments, the input for the 2D separable FIR is the same reference frame sample as for motion compensation process and fractional position $(fracX, fracY)$ according to the fractional part of block motion vector. For horizontal gradient $\partial I / \partial x$, a signal is first interpolated vertically using *BIOfilterS* corresponding to the fractional position $fracY$ with de-scaling shift $d-8$. Gradient filter *BIOfilterG* is then applied in horizontal direction corresponding to the fractional position $fracX$ with de-scaling shift by $18-d$. For vertical gradient $\partial I / \partial y$, a gradient filter is applied vertically using *BIOfilterG* corresponding to the fractional position $fracY$ with de-scaling shift $d-8$. The signal displacement is then performed using *BIOfilterS* in horizontal direction corresponding to the fractional position $fracX$ with de-scaling shift by $18-d$. The length of interpolation filter for gradients calculation *BIOfilterG* and signal displacement *BIOfilterF* can be shorter (e.g., 6-tap) in order to maintain reasonable complexity. Table 1 shows example filters that can be used for gradients calculation of different fractional positions of block motion vector in BIO. Table 2 shows example interpolation filters that can be used for prediction signal generation in BIO.

Table 1: Exemplary filters for gradient calculations in BIO

Fractional pel position	Interpolation filter for gradient(BIOfilterG)
0	{ 8, -39, -3, 46, -17, 5}
1/16	{ 8, -32, -13, 50, -18, 5}
1/8	{ 7, -27, -20, 54, -19, 5}
3/16	{ 6, -21, -29, 57, -18, 5}
1/4	{ 4, -17, -36, 60, -15, 4}
5/16	{ 3, -9, -44, 61, -15, 4}
3/8	{ 1, -4, -48, 61, -13, 3}
7/16	{ 0, 1, -54, 60, -9, 2}
1/2	{ -1, 4, -57, 57, -4, 1}

Table 2: Exemplary interpolation filters for prediction signal generation in BIO

Fractional pel position	Interpolation filter for prediction signal(BIOfilterS)
0	{ 0, 0, 64, 0, 0, 0}
1/16	{ 1, -3, 64, 4, -2, 0}
1/8	{ 1, -6, 62, 9, -3, 1}
3/16	{ 2, -8, 60, 14, -5, 1}
1/4	{ 2, -9, 57, 19, -7, 2}
5/16	{ 3, -10, 53, 24, -8, 2}
3/8	{ 3, -11, 50, 29, -9, 2}
7/16	{ 3, -11, 44, 35, -10, 3}
1/2	{ 3, -10, 35, 44, -11, 3}

[00220] In the JEM, BIO can be applied to all bi-predicted blocks when the two predictions are from different reference pictures. When Local Illumination Compensation (LIC) is enabled for a CU, BIO can be disabled.

[00221] In some embodiments, OBMC is applied for a block after normal MC process. To reduce the computational complexity, BIO may not be applied during the OBMC process. This means that BIO is applied in the MC process for a block when using its own MV and is not applied in the MC process when the MV of a neighboring block is used during the OBMC process.

2.15 Examples of decoder-side motion vector refinement (DMVR)

[00222] In a bi-prediction operation, for the prediction of one block region, two prediction blocks, formed using a motion vector (MV) of list0 and a MV of list1, respectively, are combined to form a single prediction signal. In the decoder-side motion vector refinement

(DMVR) method, the two motion vectors of the bi-prediction are further refined by a bilateral template matching process. The bilateral template matching applied in the decoder to perform a distortion-based search between a bilateral template and the reconstruction samples in the reference pictures in order to obtain a refined MV without transmission of additional motion information.

[00223] In DMVR, a bilateral template is generated as the weighted combination (i.e. average) of the two prediction blocks, from the initial MV0 of list0 and MV1 of list1, respectively, as shown in FIG. 26. The template matching operation consists of calculating cost measures between the generated template and the sample region (around the initial prediction block) in the reference picture. For each of the two reference pictures, the MV that yields the minimum template cost is considered as the updated MV of that list to replace the original one. In the JEM, nine MV candidates are searched for each list. The nine MV candidates include the original MV and 8 surrounding MVs with one luma sample offset to the original MV in either the horizontal or vertical direction, or both. Finally, the two new MVs, i.e., MV0' and MV1' as shown in FIG. 26, are used for generating the final bi-prediction results. A sum of absolute differences (SAD) is used as the cost measure.

[00224] DMVR is applied for the merge mode of bi-prediction with one MV from a reference picture in the past and another from a reference picture in the future, without the transmission of additional syntax elements. In the JEM, when LIC, affine motion, FRUC, or sub-CU merge candidate is enabled for a CU, DMVR is not applied.

3. Exemplary embodiments related to the disclosed technology

[00225] Some embodiments, for example, include an MV update method and a two-step inter prediction method. The derived MV between reference block 0 and reference block 1 in BIO are scaled and added to the original motion vector of list 0 and list 1. Meanwhile, the updated MV is used to perform motion compensation and a second inter prediction is generated as the final prediction. Other embodiments include modifying the temporal gradient by removing the mean difference between reference block 0 and reference block 1. In yet other embodiments, the MV update method and the two-step inter prediction method are extended to be performed multiple times.

4. Drawbacks of existing implementations

[00226] In some existing implementations, how to apply BIO or/and DMVR or any other

decoder side motion vector derivation/refinement tools to CUs coded with multi-hypothesis prediction mode or GBI mode is not well defined.

[00227] In other existing implementations that use generalized bi-prediction (GBI), when encoding the weighting factor index (e.g., the GBI index), all bins are coded with context, which is computationally complex.

5. Example methods for harmonization of DMVD tools with other tools

[00228] Embodiments of the presently disclosed technology overcome the drawbacks of existing implementations, thereby providing video coding with higher coding efficiencies. The harmonization of DMVD tools with other video coding tools, based on the disclosed technology, may enhance both existing and future video coding standards, is elucidated in the following examples described for various implementations. The examples of the disclosed technology provided below explain general concepts, and are not meant to be interpreted as limiting. In an example, unless explicitly indicated to the contrary, the various features described in these examples may be combined.

[00229] The disclosed technology describes how to apply BIO or/and DMVR or any other decoder side motion vector derivation/refinement tools to blocks coded with multi-hypothesis prediction mode or GBI mode. Hereinafter, DMVD (decoder side motion vector derivation) is used to represent BIO or/and DMVR or/and FRUC or/and any other decoder side motion vector derivation/refinement technologies. In addition, extensions of DMVD technologies to other coding methods are also proposed in this document.

[00230] Example 1. It is proposed that DMVD may be performed for blocks coded with multi-hypothesis prediction modes (e.g., as described in Section 2.9, 2.10 and 2.11).

[00231] (a) In one example, for a block coded with multi-hypothesis and uni-predicted AMVP, if it is predicted with two reference blocks from different prediction directions, DMVD may be performed.

[00232] (b) In one example, if a block is predicted with three reference blocks, DMVD may be performed by selecting two of the reference blocks. Let's denoted the three reference blocks as ref0, ref1 and ref2, suppose ref0 is from prediction direction X and ref1 and ref2 are from prediction direction $1 - X$, DMVD may be performed for the reference block pair (ref0, ref1) or/and (ref0, ref2).

[00233] (i) In one example, DMVD may be only performed once. In this case,

either (ref0, ref1) or (ref0, ref2) may be utilized in the DMVD process.

[00234] (ii) Alternatively, DMVD may be performed twice. In this case, (ref0, ref1) and (ref0, ref2) may be utilized in the 1st and 2nd DMVD process, respectively.

Alternatively, (ref0, ref2) and (ref0, ref1) may be utilized in the 1st and 2nd DMVD process, respectively.

[00235] (1) In one example, the refined information (e.g., refined motion information due to BIO or DMVR) of the one DMVD process (e.g., 1st DMVD) may be used as inputs to another DMVD process (e.g., 2nd DMVD). That is, sequential processes of the two DMVD processes may be applied (sequential mode).

[00236] (2) Alternatively, the multiple DMVD processes may be invoked with the same inputs such that the multiple processes could be done in parallel (parallel mode).

[00237] (iii) In one example, if ref0 is refined twice by BIO, denoted as ref0_r1 and ref0_r2, then ref0_r1 and ref0_r2 may be used jointly (e.g., averaged or weighted averaged) to generate the final refined value of ref0. For example, $\text{ref0}' = (\text{ref0_r1} + \text{ref0_r2} + 1) \gg 1$ is used as the refined ref0. Alternatively, either ref0_r1 or ref0_r2 is used as the refined ref0.

[00238] (iv) In one example, if motion information (denoted as MV0) of ref0 is refined twice by BIO or DMVR, denoted as MV0_r1 and MV0_r2, then MV0_r1 and MV0_r2 may be used jointly (e.g., averaged or weighted averaged) to generate the final refined values of MV0. For example, $\text{MV0}' = (\text{MV0_r1} + \text{MV0_r2} + 1) \gg 1$ is used as the refined MV0.

Alternatively, either MV0_r1 or MV0_r2 is used as the refined MV0.

[00239] (c) In one example, if a block is predicted with N reference blocks, and M reference blocks are from prediction direction X and N – M reference blocks are from prediction direction 1 – X, DMVD may be performed for any of /some of two reference blocks wherein one is from prediction direction X and another is from prediction direction 1 – X.

[00240] (i) Similar to (b)(ii), multiple times of DMVD processes may be invoked either in parallel mode or sequential mode.

[00241] (ii) In one example, if a reference block is refined T times by BIO, then partial or all of these T refined values may be jointly used to derive the final refined value of the reference block (e.g., using average or weighted average).

[00242] (iii) Alternatively, if a reference block is refined T times by BIO, then partial or all of PT refined values may be jointly used to derive the final refined value of the

reference block (e.g., using average or weighted average). For example, PT is equal to 1, 2, 3, 4, ..., T - 1.

[00243] (iv) In one example, if motion information of a reference block is refined T times by e.g., BIO or DMVR, then partial or all of these T refined MVs may be jointly used to derive the final refined MV of the reference block (e.g., using average or weighted average).

[00244] (v) Alternatively, if motion information of a reference block is refined T times by e.g., BIO or DMVR, then partial or all of PT refined MVs may be jointly used (e.g., using average or weighted average) to derive the final refined values of the reference block. For example, PT is equal to 1, 2, 3, 4, ..., T - 1.

[00245] (d) In one example, if a block is predicted with multiple sets of motion information (for example, one set of motion information is from AMVP mode and another set of motion information is from merge candidate (e.g., as described in Section 2.10) or both sets of motion information are from merge candidates (e.g., as described in Section 2.11)), DMVD may be performed for each/some set of motion information.

[00246] (i) In one example, DMVD is invoked when the motion information is bi-directional motion information.

[00247] (ii) In one example, in multi-hypothesis inter prediction mode (as described in Section 2.9), DMVD is only performed for non-additional motion information.

[00248] (e) In one example, DMVD may be performed at most once.

[00249] (i) For example, if a block is coded with multi-hypothesis merge/skip mode (e.g., as described in Section 2.11), DMVD is performed only for the 1st selected merge candidate.

[00250] (1) Alternatively, DMVD is performed only for the 2nd selected merge candidate.

[00251] (2) Alternatively, the two/N selected merge candidates are checked in order, and DMVD is only performed for the first available bi-directional merge candidate.

[00252] (ii) For example, if a block is coded with multi-hypothesis AMVP mode (as described in Section 2.10), DMVD is performed only for the merge candidate.

[00253] (iii) For example, if a block is coded with multi-hypothesis inter prediction mode (e.g., as described in Section 2.9), the first available reference block from List 0 and List 1 (if there is any) are identified according to the signaling order of the corresponding syntax

elements, and DMVD is performed only for these two reference blocks.

[00254] Example 2. It is proposed that when asymmetric weighting factors are used for bi-directional prediction (like GBI, LIC etc.) or multi-hypothesis prediction, such weighting factors may be used in DMVD process.

[00255] (a) In one example, before calculating temporal gradients or/and spatial gradients in BIO, each reference block may be scaled by its corresponding weighting factor.

[00256] (b) In one example, when performing the bi-lateral matching or template matching in DMVR, each reference block may be scaled by its corresponding weighting factor.

[00257] (c) In one example, if template matching is used in DMVR, such weighting factors may be used when generating the template.

[00258] Example 3. It is proposed that DMVD may be used in AMVP mode when all MVD components are zero. Alternatively, if MVD component is zero in prediction direction X and is non-zero in prediction direction $1 - X$, DMVD may be used to refine motion vector in prediction direction X. In one example, in DMVR, prediction signal in list $1 - X$ is used as a template to find the best motion vector in list X.

[00259] Example 4. It is proposed that DMVD may be used to refine the translational motion parameters in bi-directional affine mode or UMVE affine mode.

[00260] (a) Alternatively, in bi-directional affine inter mode, DMVD is used to refine the translational motion parameters only when MVD of the translational motion parameters are all zero.

[00261] (b) Alternatively, in UMVE affine mode, DMVD is used to refine the translational motion parameters only when MVD of the translational motion parameters are all zero.

[00262] Example 5. It is proposed that DMVD may be enabled in UMVE mode.

[00263] (a) Alternatively, DMVD is disabled when there is non-zero MVD component in UMVE mode.

[00264] (b) Alternatively, DMVD is disabled in UMVE mode.

[00265] Example 6. It is proposed that DMVD may be applied under certain conditions, such as based on block sizes, encoding mode, motion information, slice/picture/tile types.

[00266] (a) In one example, when a block size contains less than $M \times H$ samples, e.g., 16 or 32 or 64 luma samples, DMVD is not allowed.

[00267] (b) In one example, when a block size contains more than $M \cdot H$ samples, e.g., 16 or 32 or 64 luma samples, DMVD is not allowed.

[00268] (c) Alternatively, when minimum size of a block's width or height is smaller than or no larger than X , DMVD is not allowed. In one example, X is set to 8.

[00269] (d) Alternatively, when a block's width $> th1$ or $\geq th1$ and/or a block's height $> th2$ or $\geq th2$, DMVD is not allowed. In one example, X is set to 64.

[00270] (i) For example, DMVD is disabled for 128x128 block.

[00271] (ii) For example, DMVD is disabled for $N \times 128 / 128 \times N$ block, for $N \geq 64$.

[00272] (iii) For example, DMVD is disabled for $N \times 128 / 128 \times N$ block, for $N \geq 4$.

[00273] (e) Alternatively, when a block's width $< th1$ or $\leq th1$ and/or a block's height $< th2$ or $\leq th2$, DMVD is not allowed. In one example $th1$ or $th2$ is set to 8.

[00274] (f) In one example, DMVD is disabled for blocks coded in AMVP mode.

[00275] (g) In one example, DMVD is disabled for blocks coded in skip mode.

[00276] (h) In one example, DMVD is disabled for the block if GBI is used.

[00277] (i) In one example, DMVD is disabled for a block if multi-hypothesis inter prediction (as described in Sections 2.9, 2.10 and 2.11) is used, e.g., if the CU is predicted from more than 2 reference blocks.

[00278] (j) In one example, DMVD is disabled for the block/sub-block when absolute mean difference of the two reference blocks/sub-blocks is larger than a threshold.

[00279] Example 7. It is proposed that DMVD may be applied in a sub-block level.

[00280] (a) In one example, DMVD may be invoked for each sub-block.

[00281] (b) In one example, when a block with either width or height or both width and height are both larger than (or equal to) a threshold L , the block may be split into multiple sub-blocks. Each sub-block is treated in the same way as a normal coding block with size equal to the sub-block size.

[00282] (i) In one example, L is 64, a $64 \times 128 / 128 \times 64$ block is split into two 64×64 sub-blocks, and a 128×128 block is split into four 64×64 sub-blocks. However, $N \times 128 / 128 \times N$ block, wherein $N < 64$, is not split into sub-blocks.

[00283] (ii) In one example, L is 64, a $64 \times 128 / 128 \times 64$ block is split into two 64×64 sub-blocks, and a 128×128 block is split into four 64×64 sub-blocks. Meanwhile, $N \times 128 / 128 \times N$ block, wherein $N < 64$, is split into two $N \times 64 / 64 \times N$ sub-blocks.

[00284] (c) The threshold L may be pre-defined or signaled in SPS/PPS/picture/slice/tile group/tile level.

[00285] (d) Alternatively, the thresholds may depend on certain coded information, such as block size, picture type, temporal layer index, etc.

[00286] Example 8. In one example, whether to and how to apply the above methods (e.g., motion refinement methods such as DMVR or/and BIO and/or other decoder side motion refinement technologies) depends on the reference picture.

[00287] (a) In one example, motion refinement methods are not applied if the reference picture is the current coding picture.

[00288] (b) In one example, multi-time motion refinement methods claimed in previous bullets are not applied if the reference picture is the current coding picture.

[00289] Example 9. The above methods as well as the existing DMVD methods (e.g., BIO/DMVR) may be also applied even two reference blocks/reference pictures are from the same reference picture list.

[00290] (a) Alternatively, furthermore, when two reference blocks are from same reference picture list, it is required that the two reference pictures are crossing the current picture covering current block. That is, one reference picture has a smaller POC value and the other has a larger POC value compared to POC of the current picture.

[00291] (b) In one example, the condition check of the prediction direction is bi-prediction for enabling/disabling BIO is removed. That is, whether BIO or DMVD is enabled or not is independent from the prediction direction value.

[00292] (c) When product of the POC differences between current picture and its two reference pictures (either from same reference picture list, or different reference picture lists) is smaller than 0, BIO or DMVD may be enabled.

[00293] (d) When product of the POC differences between current picture and its two reference pictures (either from same reference picture list, or different reference picture lists) is smaller than or equal to 0 (e.g., one or two reference picture is the current picture), BIO or DMVD may be enabled.

[00294] Example 10. It is proposed that when encoding the GBI index, some bins are bypass coded. Denote the maximum length of the code bins of GBI index as maxGBIdxLen.

[00295] (a) In one example, only the first bin is coded with context and all other bins are

bypass coded.

[00296] (i) In one example, one context is used for encoding the first bin.

[00297] (ii) In one example, more than 1 contexts are used for encoding the first bin. For example, 3 contexts are used as follows:

[00298] (1) $ctxIdx = aboveBlockIsGBIMode + leftBlockIsGBIMode$;

[00299] (2) $aboveBlockIsGBIMode$ equals to 1 if the above neighboring block is coded in GBI mode, otherwise it equals to 0; and

[00300] (3) $leftBlockIsGBIMode$ equals to 1 if the left neighboring block is coded in GBI mode, otherwise it equals to 0.

[00301] (b) In one example, only the first K bins are coded with contexts and all other bins are bypass coded, wherein $0 \leq K \leq \maxGBIIdxLen$.

[00302] (i) In one example, one context is shared for all context coded bins except the first bin.

[00303] (ii) In one example, one context is used for each context coded bin except the first bin.

[00304] The examples described above may be incorporated in the context of the method described below, e.g., methods 2700, 2710, 2720, 2730, 2740, 2750, 2760 and 2770, which may be implemented at a video decoder or a video encoder.

[00305] FIG. 27A shows a flowchart of an exemplary method for video decoding. The method 2700 includes, at operation 2702, making a decision, based on a determination that a current block of a video is coded using a multi-hypothesis prediction mode, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, the DMVD tool deriving a refinement of motion information signaled in a bitstream representation of the video. In some embodiments, the multi-hypothesis prediction mode is configured to generate a final prediction of the current block by applying at least one intermediate prediction value.

[00306] The method 2700 includes, at operation 2704, performing, based on the decision, a conversion between the current block and the bitstream representation.

[00307] FIG. 27B shows a flowchart of an exemplary method for video decoding. The method 2710 includes, at operation 2712, determining that a current block of video is associated with asymmetric weighting factors for different reference blocks.

[00308] The method 2710 includes, at operation 2714, enabling a decoder side motion vector derivation (DMVD) tool for the current block, the DMVD tool deriving a refinement of motion information signaled in a bitstream representation of the video, and the DMVD process being based on the asymmetric weighting factors.

[00309] The method 2710 includes, at operation 2716, performing, based on the enabling, a conversion between the current block and the bitstream representation.

[00310] FIG. 27C shows a flowchart of an exemplary method for video decoding. The method 2720 includes, at operation 2722, determining that a current block of video is coded using an advanced motion vector prediction (AMVP) mode.

[00311] The method 2720 includes, at operation 2724, applying, as part of a conversion between a bitstream representation of the video and the current block, a decoder side motion vector derivation (DMVD) tool to the current block, the DMVD tool deriving a refinement of motion information signaled in the bitstream representation.

[00312] FIG. 27D shows a flowchart of an exemplary method for video decoding. The method 2730 includes, at operation 2732, performing, based on a decoder side motion vector derivation (DMVD) tool, a refinement of translational motion parameters for a current block of video that is coded using a bi-directional affine mode or a bi-directional affine merge mode and motion vector differences that are indicated by a motion direction and a motion magnitude, the DMVD tool deriving a refinement of motion information signaled in a bitstream representation of the video.

[00313] The method 2730 includes, at operation 2734, performing, based on the refinement, a conversion between the current block and the bitstream representation of the video.

[00314] FIG. 27E shows a flowchart of an exemplary method for video decoding. The method 2740 includes, at operation 2742, making a decision, based on a characteristic of a current block of video, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, the DMVD tool deriving a refinement of motion information signaled in a bitstream representation of the video.

[00315] The method 2740 includes, at operation 2744, performing, based on the decision, a conversion between the current block and the bitstream representation.

[00316] FIG. 27F shows a flowchart of an exemplary method for video decoding. The method 2750 includes, at operation 2752, making a decision, based upon a determination that a current block of video comprises a plurality of sub-blocks, regarding a selective enablement of a decoder

side motion vector derivation (DMVD) tool at a sub-block level, the DMVD tool deriving a refinement of motion information signaled in a bitstream representation of the video for each sub-block.

[00317] The method 2750 includes, at operation 2754, performing, based on the decision, a conversion between the current block and the bitstream representation.

[00318] FIG. 27G shows a flowchart of an exemplary method for video decoding. The method 2760 includes, at operation 2762, making a decision, based on at least one reference picture associated with a current block of video, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, the DMVD tool deriving a refinement of motion information signaled in a bitstream representation of the video.

[00319] The method 2760 includes, at operation 2764, performing, based on the decision, a conversion between the current block and the bitstream representation.

[00320] FIG. 27H shows a flowchart of an exemplary method for video decoding. The method 2770 includes, at operation 2772, parsing a bin string from a bitstream representation of a current block of video, the bin string comprising a plurality of bins that represent a GBI index of a GBI mode, and at least one bin of the plurality of bins being bypass coded.

[00321] The method 2770 includes, at operation 2774, performing, based on the parsed GBI index, a conversion between the current block and the bitstream representation.

[00322] FIG. 27I shows a flowchart of an exemplary method for video decoding. The method 2780 includes, at operation 2782, encoding a bin string into a bitstream representation of a current block of video, the bin string comprising a plurality of bins that represent a generalized bi-prediction (GBI) index of a GBI mode, and wherein at least one bin of the plurality of bins is bypass coded.

[00323] The method 2780 includes, at operation 2784, performing, based on the encoded bin string, a conversion between the current block and the bitstream representation.

[00324] In some embodiments, the following technical solutions may be implemented:

[00325] A1. A method (e.g., method 2700 in FIG. 27A) for video processing, comprising: making (2702) a decision, based on a determination that a current block of a video is coded using a multi-hypothesis prediction mode, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and

performing (2704), based on the decision, a conversion between the current block and the bitstream representation, wherein the multi-hypothesis prediction mode is configured to generate a final prediction of the current block by applying at least one intermediate prediction value.

[00326] A2. The method of solution A1, wherein the DMVD tool is enabled upon a determination that the current block is predicted with a uni-prediction of an advanced motion vector prediction (AMVP) mode and the multi-hypothesis prediction mode with two reference blocks from different prediction directions.

[00327] A3. The method of solution A1, wherein performing the conversion is further based on N reference blocks, and wherein N is a positive integer.

[00328] A4. The method of solution A3, wherein the N reference blocks include M reference blocks from a first prediction direction and (N-M) reference blocks from a second prediction direction different from the first prediction direction, wherein M is a positive integer, and wherein $N > M$.

[00329] A5. The method of solution A4, wherein performing the conversion comprises applying the DMVD tool to one of the N reference blocks and one of the (N-M) reference blocks.

[00330] A6. The method of solution A4, wherein performing the conversion comprises applying the DMVD tool multiple times, and wherein for each of the multiple times, the DMVD tool is applied to two of the N reference blocks.

[00331] A7. The method of solution A6, wherein the DMVD tool is applied multiple times to a first block of the M reference blocks and consecutive blocks of the (N-M) reference blocks.

[00332] A8. The method of solution A6, wherein the DMVD tool is applied multiple times in parallel.

[00333] A9. The method of solution A6, wherein the DMVD tool is applied multiple times in sequence.

[00334] A10. The method of solution A4, further comprising: generating a (N+1)-th reference block based on a first bi-directional optical flow refinement with a first reference block from the M reference blocks and a second reference block from the (N-M) reference blocks as inputs; generating a (N+2)-th reference block based on using a second bi-directional optical flow refinement with the first reference block and a third reference block from the (N-M) reference blocks as inputs; and recomputing the first reference block as a weighted average of the (N+1)-th

reference block and the (N+2)-th reference blocks.

[00335] A11. The method of solution A1, wherein the multi-hypothesis prediction mode is a multi-hypothesis merge or skip mode, and wherein enabling the DMVD tool is further based on a first selected merge candidate.

[00336] A12. The method of solutions A1, wherein the multi-hypothesis prediction mode is a multi-hypothesis merge or skip mode, and wherein enabling the DMVD tool is further based on a second selected merge candidate.

[00337] A13. The method of solutions A1, wherein the multi-hypothesis prediction mode is a multi-hypothesis merge or skip mode, and wherein using enabling DMVD tool is further based on a first available bi-directional merge candidate.

[00338] A14. The method of solutions A1, wherein the multi-hypothesis prediction mode is a multi-hypothesis advanced motion picture prediction (AMVP) mode, and wherein enabling the DMVD tool is further based on a merge candidate.

[00339] A15. The method of any of solutions A4 to A10, wherein $N = 3$ and $M = 2$.

[00340] A16. A method for video (e.g., method 2710 in FIG. 27B) processing, comprising: determining (2712) that a current block of video is associated with asymmetric weighting factors for different reference blocks; enabling (2714) a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video, and wherein the DMVD process is based on the asymmetric weighting factors; and performing (2716), based on the enabling, a conversion between the current block and the bitstream representation.

[00341] A17. The method of solutions A16, wherein the current block is coded with a bi-prediction mode or a multi-hypothesis prediction mode that uses one or more of the asymmetric weighting factors.

[00342] A18. The method of solutions A16, wherein enabling the DMVD tool is further based on scaling two or more of the N reference blocks with corresponding weighting factors from one or more of the asymmetric weighting factors.

[00343] A19. The method of solution A16, wherein using the DMVD tool comprises using bi-lateral matching or template matching.

[00344] A20. A method (e.g., method 2720 in FIG. 27C) for video processing, comprising: determining (2722) that a current block of video is coded using an advanced motion vector

prediction (AMVP) mode; and applying (2724), as part of a conversion between a bitstream representation of the video and the current block, a decoder side motion vector derivation (DMVD) tool to the current block, wherein the DMVD tool derives a refinement of motion information signaled in the bitstream representation.

[00345] A21. The method of solution A20, further comprising: determining that at least one of a plurality of motion vector difference (MVD) components of the current block is zero.

[00346] A22. The method of solution A20, wherein each of the plurality of MVD components is zero.

[00347] A23. The method of solution A20, wherein a first of the plurality of MVD components is zero in a first prediction direction, wherein a second of the plurality of MVD components is non-zero in a second prediction direction, and wherein applying the DMVD tool comprises refining a motion vector in the first prediction direction.

[00348] A24. The method of solution A23, wherein a prediction signal in the first prediction direction is used to derive a motion vector in the second prediction direction.

[00349] A25. A method for video (e.g., method 2730 in FIG. 27D) processing, comprising: performing (2732), based on a decoder side motion vector derivation (DMVD) tool, a refinement of translational motion parameters for a current block of video that is coded using a bi-directional affine mode or a bi-directional affine merge mode and motion vector differences that are indicated by a motion direction and a motion magnitude, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing (2734), based on the refinement, a conversion between the current block and the bitstream representation of the video.

[00350] A26. The method of solution A25, wherein the bi-directional affine merge mode further comprises a starting point of motion information indicated by a merge index, and wherein a final motion information of the current block is based on the motion vector differences and the starting point.

[00351] A27. The method of solution A25 or A26, wherein each of the motion vector differences of the translational motion parameters is zero.

[00352] A28. The method of any of solutions A1 to A27, wherein the DMVD tool comprises a decoder side motion vector refinement (DMVR) tool, or a bi-directional optical flow (BDOF) tool, or a frame-rate up conversion (FRUC) tool.

[00353] A29. The method of any of solutions A1 to A28, wherein the conversion generates the current block from the bitstream representation.

[00354] A30. The method of any of solutions A1 to A28, wherein the conversion generates the bitstream representation from the current block.

[00355] A31. An apparatus in a video system comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to implement the method in any one of solutions A1 to A30.

[00356] A32. A computer program product stored on a non-transitory computer readable media, the computer program product including program code for carrying out the method in any one of solutions A1 to A30.

[00357] In some embodiments, the following technical solutions may be implemented:

[00358] B1. A method for video processing, comprising: making a decision, based on a characteristic of a current block of video, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing, based on the decision, a conversion between the current block and the bitstream representation.

[00359] B2. The method of solution B1, wherein the characteristic of the current block comprises a size or coding mode of the current block, motion information associated with the current block, a slice type, a picture type or a tile type.

[00360] B3. The method of solution B1, wherein the DMVD tool is disabled upon a determination that a number of luma samples in the current block is less than K, wherein K is a positive integer.

[00361] B4. The method of solution B1, wherein the DMVD tool is disabled upon a determination that a number of luma samples in the current block is greater than K, wherein K is a positive integer.

[00362] B5. The method of solution B1, wherein the DMVD tool is enabled upon a determination that a number of luma samples in the current block is greater than K, wherein K is a positive integer.

[00363] B6. The method of any of solutions B3 to B5, wherein $K = 16, 32, 64, \text{ or } 128$.

[00364] B7. The method of solution B1, wherein the DMVD tool is disabled upon a

determination that a minimum size of a height or width of the current block is less than or equal to K.

[00365] B8. The method of solution B7, wherein $K = 8$.

[00366] B9. The method of solution B1, wherein the DMVD tool is disabled upon a determination that either a height of the current block is greater than or equal to tH or a width of the current block is greater than or equal to tW , and wherein tH and tW are positive integers.

[00367] B10. The method of solution B1, wherein the DMVD tool is disabled upon a determination that a height of the current block is greater than or equal to tH and a width of the current block is greater than or equal to tW , and wherein tH and tW are positive integers.

[00368] B11. The method of solution B9 or B10, wherein $tH = 64$ and $tW = 64$.

[00369] B12. The method of solution B9 or B10, wherein $tH = 128$ and $tW = 128$.

[00370] B13. The method of solution B9 or B10, wherein $tH = 128$, and wherein $tW \geq 64$ or $tW \geq 4$.

[00371] B14. The method of solution B9 or B10, wherein $tH \geq 64$ or $tH \geq 4$, and wherein $tW = 128$.

[00372] B15. The method of solution B1, wherein the DMVD tool is disabled upon a determination that either a height of the current block is smaller than tH or a width of the current block is smaller than tW , and wherein tH and tW are positive integers.

[00373] B16. The method of solution B1, wherein the DMVD tool is disabled upon a determination that either a height of the current block is no smaller than tH or a width of the current block is no smaller than tW , and wherein tH and tW are positive integers.

[00374] B17. The method of solution B1, wherein the DMVD tool is disabled upon a determination that a height of the current block is less than or equal to tH and a width of the current block is less than or equal to tW , and wherein tH and tW are positive integers.

[00375] B18. The method of any of solutions B15 to B17, wherein $tH = 8$ and $tW = 8$.

[00376] B19. The method of solution B1, wherein the DMVD tool is disabled upon a determination that the coding mode of the current block is a generalized bi-prediction (GBI) mode, and wherein asymmetric weighting factors are applied to two reference blocks.

[00377] B20. The method of solution B1, wherein the DMVD tool is disabled upon a determination that the coding mode of the current block is an advanced motion vector prediction (AMVP) mode.

[00378] B21. The method of solution B1, wherein the DMVD tool is disabled upon a determination that the coding mode of the current block is a skip mode or a multi-hypothesis inter prediction mode.

[00379] B22. The method of solution B1, wherein the current block comprises at least one sub-block.

[00380] B23. The method of solution B22, wherein the DMVD tool is disabled for a sub-block upon a determination that an absolute mean difference of two reference blocks associated with a current sub-block is greater than a threshold.

[00381] B24. The method of solution B1, wherein the DMVD tool is disabled upon a determination that an absolute mean difference of two reference blocks associated with the current block is greater than a threshold.

[00382] B25. The method of any of solutions B1 to B24, wherein the DMVD tool comprises a decoder side motion vector refinement (DMVR) tool, or a bi-directional optical flow (BDOF) tool, or a frame-rate up conversion (FRUC) tool.

[00383] B26. The method of any of solutions B1 to B25, wherein the conversion generates the current block from the bitstream representation.

[00384] B27. The method of any of solutions B1 to B25, wherein the conversion generates the bitstream representation from the current block.

[00385] B28. An apparatus in a video system comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to implement the method in any one of solutions B1 to B27.

[00386] B29. A computer program product stored on a non-transitory computer readable media, the computer program product including program code for carrying out the method in any one of solutions B1 to B27.

[00387] In some embodiments, the following technical solutions may be implemented:

[00388] C1. A method (e.g., method 2750 in FIG. 27F) for video processing, comprising: making (2752) a decision, based upon a determination that a current block of video comprises a plurality of sub-blocks, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool at a sub-block level, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video for each sub-block; and performing (2754), based on the decision, a conversion between the current block and the

bitstream representation.

[00389] C2. The method of solution C1, wherein the DMVD tool is enabled for each of the plurality of sub-blocks of the current block.

[00390] C3. The method of solution C2, wherein the sub-block is treated as a block and all operations required in DMVD tool are performed for the sub-block.

[00391] C4. The method of solution C1 or C2, wherein the DMVD tool is enabled at a block-level upon a determination that a height and a width of the current block are no greater than a threshold (L), and wherein L is a positive integer.

[00392] C5. The method of solution C1 or C2, wherein a height (H) or a width (W) of the current block is greater than a threshold (L), and wherein L is a positive integer.

[00393] C6. The method of solution C5, wherein the width of the sub-block is $\min(L, W)$.

[00394] C7. The method of solution C5, wherein the height of the sub-block is $\min(L, H)$.

[00395] C8. The method of solution C5, wherein $L = 64$, wherein a size of the current block is 64×128 , 128×64 or 128×128 , and wherein a size of each of plurality of sub-blocks is 64×64 .

[00396] C9. The method of solution C5, wherein $L = 16$.

[00397] C10. The method of solution C5, wherein $L = 64$, wherein a size of the current block is $N \times 128$ or $128 \times N$, and wherein a size of both of the plurality of sub-blocks is $N \times 64$ or $64 \times N$, respectively.

[00398] C11. The method of solution C5, wherein L is predetermined.

[00399] C12. The method of solution C5, wherein L is signaled in the bitstream representation in a sequence parameter set (SPS), a picture parameter set (PPS), a picture header, a slice header, a tile group header, or a tile header.

[00400] C13. The method of solution C5, wherein L is based on at least one of a size or a coding mode of the current block, a picture type, or a temporal layer index.

[00401] C14. The method of any of solutions C1 to C13, wherein the conversion generates the current block from the bitstream representation.

[00402] C15. The method of any of solutions C1 to C13, wherein the conversion generates the bitstream representation from the current block.

[00403] C16. The method of any of solutions C1 to C15, wherein the DMVD tool comprises a decoder side motion vector refinement (DMVR) tool, or a bi-directional optical flow (BDOF) tool, or a frame-rate up conversion (FRUC) tool.

[00404] C17. An apparatus in a video system comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to implement the method in any one of solutions C1 to C16.

[00405] C18. A computer program product stored on a non-transitory computer readable media, the computer program product including program code for carrying out the method in any one of solutions C1 to C16.

[00406] In some embodiments, the following technical solutions may be implemented:

[00407] D1. A method (e.g., method 2760 in FIG. 27G) for video processing, comprising: making (2762) a decision, based on at least one reference picture associated with a current block of video, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and performing (2764), based on the decision, a conversion between the current block and the bitstream representation.

[00408] D2. The method of solution D1, wherein the DMVD tool is not enabled upon a determination that the at least one reference picture comprises a current coding picture.

[00409] D3. The method of solution D1, wherein the DMVD tool is enabled upon a determination that the at least one reference picture comprises a first reference picture from a first reference picture list and a second reference picture from the first reference picture list.

[00410] D4. The method of solution D3, wherein a picture order count (POC) value of the first reference picture is smaller than a POC value of a current picture comprising the current block, and wherein a POC value of the second reference picture is greater than the POC value of the current picture.

[00411] D5. The method of solution D3, wherein a product of picture order count (POC) differences between a POC value of the current picture comprising the current block and POC values of the first reference picture and the second reference picture is less than or equal to zero.

[00412] D6. The method of any of solutions D1 to D5, wherein the DMVD tool comprises a decoder side motion vector refinement (DMVR) tool, or a bi-directional optical flow (BDOF) tool, or a frame-rate up conversion (FRUC) tool.

[00413] D7. The method of any of solutions D1 to D6, wherein the conversion generates the current block from the bitstream representation.

[00414] D8. The method of any of solutions D1 to D6, wherein the conversion generates the

bitstream representation from the current block.

[00415] D9. An apparatus in a video system comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to implement the method in any one of solutions D1 to D8.

[00416] D10. A computer program product stored on a non-transitory computer readable media, the computer program product including program code for carrying out the method in any one of solutions D1 to D8.

[00417] In some embodiments, the following technical solutions may be implemented:

[00418] 1. A method for video processing, comprising: parsing a bin string from a bitstream representation of a current block of video, wherein the bin string comprises a plurality of bins that represent a generalized bi-prediction (GBI) index of a GBI mode, and wherein at least one bin of the plurality of bins is bypass coded; and performing, based on the parsed GBI index, a conversion between the current block and the bitstream representation.

[00419] 2. A method for video processing, comprising: encoding a bin string into a bitstream representation of a current block of video, wherein the bin string comprises a plurality of bins that represent a generalized bi-prediction (GBI) index of a GBI mode, and wherein at least one bin of the plurality of bins is bypass coded; and performing, based on the encoded bin string, a conversion between the current block and the bitstream representation.

[00420] 3. The method of solution E1 or E2, wherein the GBI mode is configured to select a weight from a set of weights to generate a bi-prediction signal for the current block, and wherein the set of weights comprise multiple weights that are different from 1/2.

[00421] 4. The method of any of solutions E1 to E3, wherein a first bin of the plurality of bins is coded with at least one context, and wherein all other bins of the plurality of bins are bypass coded.

[00422] 5. The method of solution E4, wherein the at least one context consists of one context.

[00423] 6. The method of solution E4, wherein the at least one context consists of three contexts.

[00424] 7. The method of solution E6, wherein the three contexts are defined as: $ctxIdx = aboveBlockIsGBIMode + leftBlockIsGBIMode$, wherein $aboveBlockIsGBIMode = 1$ if an above neighboring block to the current block is coded using the GBI mode and zero otherwise, wherein $leftBlockIsGBIMode = 1$ if a left neighboring block to the current block is coded using the GBI

mode and zero otherwise, and wherein using the GBI mode comprises using unequal weights for two reference blocks of a bi-predicted block.

[00425] 8. The method of any of solutions E1 to E3, wherein each of a first K bins of the plurality of bins is coded with at least one context, wherein all other bins of the plurality of bins are bypass coded, wherein K is a non-negative integer, wherein $0 \leq K \leq \text{maxGBIIdxLen}$, and wherein maxGBIIdxLen is a maximum length of the plurality of bins.

[00426] 9. The method of solution E8, wherein one context is shared for the first K bins except for a first bin of the first K bins.

[00427] 10. The method of solution E8, wherein one context is used for each of the first K bins except for a first bin of the first K bins.

[00428] 11. The method of any of solutions E1 to E10, wherein the conversion generates the current block from the bitstream representation.

[00429] 12. The method of any of solutions E1 to E10, wherein the conversion generates the bitstream representation from the current block.

[00430] 13. An apparatus in a video system comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to implement the method in any one of solutions E1 to E12.

[00431] 14. A computer program product stored on a non-transitory computer readable media, the computer program product including program code for carrying out the method in any one of solutions E1 to E12.

6. Example implementations of the disclosed technology

[00432] FIG. 28 is a block diagram of a video processing apparatus 2800. The apparatus 2800 may be used to implement one or more of the methods described herein. The apparatus 2800 may be embodied in a smartphone, tablet, computer, Internet of Things (IoT) receiver, and so on. The apparatus 2800 may include one or more processors 2802, one or more memories 2804 and video processing hardware 2806. The processor(s) 2802 may be configured to implement one or more methods (including, but not limited to, methods 2700, 2710, 2720, 2730, 2740, 2750, 2760 and 2770) described in the present document. The memory (memories) 2804 may be used for storing data and code used for implementing the methods and techniques described herein. The video processing hardware 2806 may be used to implement, in hardware circuitry, some techniques described in the present document.

[00433] In some embodiments, the video coding methods may be implemented using an apparatus that is implemented on a hardware platform as described with respect to FIG. 28.

[00434] Some embodiments of the disclosed technology include making a decision or determination to enable a video processing tool or mode. In an example, when the video processing tool or mode is enabled, the encoder will use or implement the tool or mode in the processing of a block of video, but may not necessarily modify the resulting bitstream based on the usage of the tool or mode. That is, a conversion from the block of video to the bitstream representation of the video will use the video processing tool or mode when it is enabled based on the decision or determination. In another example, when the video processing tool or mode is enabled, the decoder will process the bitstream with the knowledge that the bitstream has been modified based on the video processing tool or mode. That is, a conversion from the bitstream representation of the video to the block of video will be performed using the video processing tool or mode that was enabled based on the decision or determination.

[00435] Some embodiments of the disclosed technology include making a decision or determination to disable a video processing tool or mode. In an example, when the video processing tool or mode is disabled, the encoder will not use the tool or mode in the conversion of the block of video to the bitstream representation of the video. In another example, when the video processing tool or mode is disabled, the decoder will process the bitstream with the knowledge that the bitstream has not been modified using the video processing tool or mode that was enabled based on the decision or determination.

[00436] FIG. 29 is a block diagram showing an example video processing system 2900 in which various techniques disclosed herein may be implemented. Various implementations may include some or all of the components of the system 2900. The system 2900 may include input 2902 for receiving video content. The video content may be received in a raw or uncompressed format, e.g., 8 or 10 bit multi-component pixel values, or may be in a compressed or encoded format. The input 2902 may represent a network interface, a peripheral bus interface, or a storage interface. Examples of network interface include wired interfaces such as Ethernet, passive optical network (PON), etc. and wireless interfaces such as Wi-Fi or cellular interfaces.

[00437] The system 2900 may include a coding component 2904 that may implement the various coding or encoding methods described in the present document. The coding component 2904 may reduce the average bitrate of video from the input 2902 to the output of the coding

component 2904 to produce a coded representation of the video. The coding techniques are therefore sometimes called video compression or video transcoding techniques. The output of the coding component 2904 may be either stored, or transmitted via a communication connected, as represented by the component 2906. The stored or communicated bitstream (or coded) representation of the video received at the input 2902 may be used by the component 2908 for generating pixel values or displayable video that is sent to a display interface 2910. The process of generating user-viewable video from the bitstream representation is sometimes called video decompression. Furthermore, while certain video processing operations are referred to as “coding” operations or tools, it will be appreciated that the coding tools or operations are used at an encoder and corresponding decoding tools or operations that reverse the results of the coding will be performed by a decoder.

[00438] Examples of a peripheral bus interface or a display interface may include universal serial bus (USB) or high definition multimedia interface (HDMI) or Displayport, and so on. Examples of storage interfaces include SATA (serial advanced technology attachment), PCI, IDE interface, and the like. The techniques described in the present document may be embodied in various electronic devices such as mobile phones, laptops, smartphones or other devices that are capable of performing digital data processing and/or video display.

[00439] From the foregoing, it will be appreciated that specific embodiments of the presently disclosed technology have been described herein for purposes of illustration, but that various modifications may be made without deviating from the scope of the invention. Accordingly, the presently disclosed technology is not limited except as by the appended claims.

[00440] Implementations of the subject matter and the functional operations described in this patent document can be implemented in various systems, digital electronic circuitry, or in computer software, firmware, or hardware, including the structures disclosed in this specification and their structural equivalents, or in combinations of one or more of them. Implementations of the subject matter described in this specification can be implemented as one or more computer program products, i.e., one or more modules of computer program instructions encoded on a tangible and non-transitory computer readable medium for execution by, or to control the operation of, data processing apparatus. The computer readable medium can be a machine-readable storage device, a machine-readable storage substrate, a memory device, a composition of matter effecting a machine-readable propagated signal, or a combination of one or more of

them. The term “data processing unit” or “data processing apparatus” encompasses all apparatus, devices, and machines for processing data, including by way of example a programmable processor, a computer, or multiple processors or computers. The apparatus can include, in addition to hardware, code that creates an execution environment for the computer program in question, e.g., code that constitutes processor firmware, a protocol stack, a database management system, an operating system, or a combination of one or more of them.

[00441] A computer program (also known as a program, software, software application, script, or code) can be written in any form of programming language, including compiled or interpreted languages, and it can be deployed in any form, including as a stand-alone program or as a module, component, subroutine, or other unit suitable for use in a computing environment. A computer program does not necessarily correspond to a file in a file system. A program can be stored in a portion of a file that holds other programs or data (e.g., one or more scripts stored in a markup language document), in a single file dedicated to the program in question, or in multiple coordinated files (e.g., files that store one or more modules, sub programs, or portions of code). A computer program can be deployed to be executed on one computer or on multiple computers that are located at one site or distributed across multiple sites and interconnected by a communication network.

[00442] The processes and logic flows described in this specification can be performed by one or more programmable processors executing one or more computer programs to perform functions by operating on input data and generating output. The processes and logic flows can also be performed by, and apparatus can also be implemented as, special purpose logic circuitry, e.g., an FPGA (field programmable gate array) or an ASIC (application specific integrated circuit).

[00443] Processors suitable for the execution of a computer program include, by way of example, both general and special purpose microprocessors, and any one or more processors of any kind of digital computer. Generally, a processor will receive instructions and data from a read only memory or a random access memory or both. The essential elements of a computer are a processor for performing instructions and one or more memory devices for storing instructions and data. Generally, a computer will also include, or be operatively coupled to receive data from or transfer data to, or both, one or more mass storage devices for storing data, e.g., magnetic, magneto optical disks, or optical disks. However, a computer need not have such devices.

Computer readable media suitable for storing computer program instructions and data include all forms of nonvolatile memory, media and memory devices, including by way of example semiconductor memory devices, e.g., EPROM, EEPROM, and flash memory devices. The processor and the memory can be supplemented by, or incorporated in, special purpose logic circuitry.

[00444] It is intended that the specification, together with the drawings, be considered exemplary only, where exemplary means an example.

[00445] While this patent document contains many specifics, these should not be construed as limitations on the scope of any invention or of what may be claimed, but rather as descriptions of features that may be specific to particular embodiments of particular inventions. Certain features that are described in this patent document in the context of separate embodiments can also be implemented in combination in a single embodiment. Conversely, various features that are described in the context of a single embodiment can also be implemented in multiple embodiments separately or in any suitable subcombination. Moreover, although features may be described above as acting in certain combinations and even initially claimed as such, one or more features from a claimed combination can in some cases be excised from the combination, and the claimed combination may be directed to a subcombination or variation of a subcombination.

[00446] Similarly, while operations are depicted in the drawings in a particular order, this should not be understood as requiring that such operations be performed in the particular order shown or in sequential order, or that all illustrated operations be performed, to achieve desirable results. Moreover, the separation of various system components in the embodiments described in this patent document should not be understood as requiring such separation in all embodiments.

[00447] Only a few implementations and examples are described and other implementations, enhancements and variations can be made based on what is described and illustrated in this patent document.

CLAIMS

What is claimed is:

1. A method for video processing, comprising:
making a decision, based on a determination that a current block of a video is coded using a multi-hypothesis prediction mode, regarding a selective enablement of a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and
performing, based on the decision, a conversion between the current block and the bitstream representation,
wherein the multi-hypothesis prediction mode is configured to generate a final prediction of the current block by applying at least one intermediate prediction value.
2. The method of claim 1, wherein the DMVD tool is enabled upon a determination that the current block is predicted with a uni-prediction of an advanced motion vector prediction (AMVP) mode and the multi-hypothesis prediction mode with two reference blocks from different prediction directions.
3. The method of claim 1, wherein performing the conversion is further based on N reference blocks, and wherein N is a positive integer.
4. The method of claim 3, wherein the N reference blocks include M reference blocks from a first prediction direction and (N-M) reference blocks from a second prediction direction different from the first prediction direction, wherein M is a positive integer, and wherein $N > M$.
5. The method of claim 4, wherein performing the conversion comprises applying the DMVD tool to one of the N reference blocks and one of the (N-M) reference blocks.
6. The method of claim 4, wherein performing the conversion comprises applying the DMVD tool multiple times, and wherein for each of the multiple times, the DMVD tool is applied to two of the N reference blocks.

7. The method of claim 6, wherein the DMVD tool is applied multiple times to a first block of the M reference blocks and consecutive blocks of the (N-M) reference blocks.
8. The method of claim 6, wherein the DMVD tool is applied multiple times in parallel.
9. The method of claim 6, wherein the DMVD tool is applied multiple times in sequence.
10. The method of claim 4, further comprising:
 - generating a (N+1)-th reference block based on a first bi-directional optical flow refinement with a first reference block from the M reference blocks and a second reference block from the (N-M) reference blocks as inputs;
 - generating a (N+2)-th reference block based on using a second bi-directional optical flow refinement with the first reference block and a third reference block from the (N-M) reference blocks as inputs; and
 - recomputing the first reference block as a weighted average of the (N+1)-th reference block and the (N+2)-th reference blocks.
11. The method of claim 1, wherein the multi-hypothesis prediction mode is a multi-hypothesis merge or skip mode, and wherein enabling the DMVD tool is further based on a first selected merge candidate.
12. The method of claim 1, wherein the multi-hypothesis prediction mode is a multi-hypothesis merge or skip mode, and wherein enabling the DMVD tool is further based on a second selected merge candidate.
13. The method of claim 1, wherein the multi-hypothesis prediction mode is a multi-hypothesis merge or skip mode, and wherein using enabling DMVD tool is further based on a first available bi-directional merge candidate.
14. The method of claim 1, wherein the multi-hypothesis prediction mode is a multi-hypothesis advanced motion picture prediction (AMVP) mode, and wherein enabling the DMVD tool is further based on a merge candidate.
15. The method of any of claims 4 to 10, wherein $N = 3$ and $M = 2$.

16. A method for video processing, comprising:
 - determining that a current block of video is associated with asymmetric weighting factors for different reference blocks;
 - enabling a decoder side motion vector derivation (DMVD) tool for the current block, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video, and wherein the DMVD process is based on the asymmetric weighting factors; and
 - performing, based on the enabling, a conversion between the current block and the bitstream representation.
17. The method of claim 16, wherein the current block is coded with a bi-prediction mode or a multi-hypothesis prediction mode that uses one or more of the asymmetric weighting factors.
18. The method of claim 16, wherein enabling the DMVD tool is further based on scaling two or more of the N reference blocks with corresponding weighting factors from one or more of the asymmetric weighting factors.
19. The method of claim 16, wherein using the DMVD tool comprises using bi-lateral matching or template matching.
20. A method for video processing, comprising:
 - determining that a current block of video is coded using an advanced motion vector prediction (AMVP) mode; and
 - applying, as part of a conversion between a bitstream representation of the video and the current block, a decoder side motion vector derivation (DMVD) tool to the current block, wherein the DMVD tool derives a refinement of motion information signaled in the bitstream representation.
21. The method of claim 20, further comprising:
 - determining that at least one of a plurality of motion vector difference (MVD) components of the current block is zero.
22. The method of claim 20, wherein each of the plurality of MVD components is zero.

23. The method of claim 20, wherein a first of the plurality of MVD components is zero in a first prediction direction, wherein a second of the plurality of MVD components is non-zero in a second prediction direction, and wherein applying the DMVD tool comprises refining a motion vector in the first prediction direction.
24. The method of claim 23, wherein a prediction signal in the first prediction direction is used to derive a motion vector in the second prediction direction.
25. A method for video processing, comprising:
performing, based on a decoder side motion vector derivation (DMVD) tool, a refinement of translational motion parameters for a current block of video that is coded using a bi-directional affine mode or a bi-directional affine merge mode and motion vector differences that are indicated by a motion direction and a motion magnitude, wherein the DMVD tool derives a refinement of motion information signaled in a bitstream representation of the video; and
performing, based on the refinement, a conversion between the current block and the bitstream representation of the video.
26. The method of claim 25, wherein the bi-directional affine merge mode further comprises a starting point of motion information indicated by a merge index, and wherein a final motion information of the current block is based on the motion vector differences and the starting point.
27. The method of claim 25 or 26, wherein each of the motion vector differences of the translational motion parameters is zero.
28. The method of any of claims 1 to 27, wherein the DMVD tool comprises a decoder side motion vector refinement (DMVR) tool, or a bi-directional optical flow (BDOF) tool, or a frame-rate up conversion (FRUC) tool.
29. The method of any of claims 1 to 28, wherein the conversion generates the current block from the bitstream representation.
30. The method of any of claims 1 to 28, wherein the conversion generates the bitstream representation from the current block.

31. An apparatus in a video system comprising a processor and a non-transitory memory with instructions thereon, wherein the instructions upon execution by the processor, cause the processor to implement the method recited in one or more of claims 1 to 30.

32. A computer program product stored on a non-transitory computer readable media, the computer program product including program code for carrying out the method recited in one or more of claims 1 to 30.

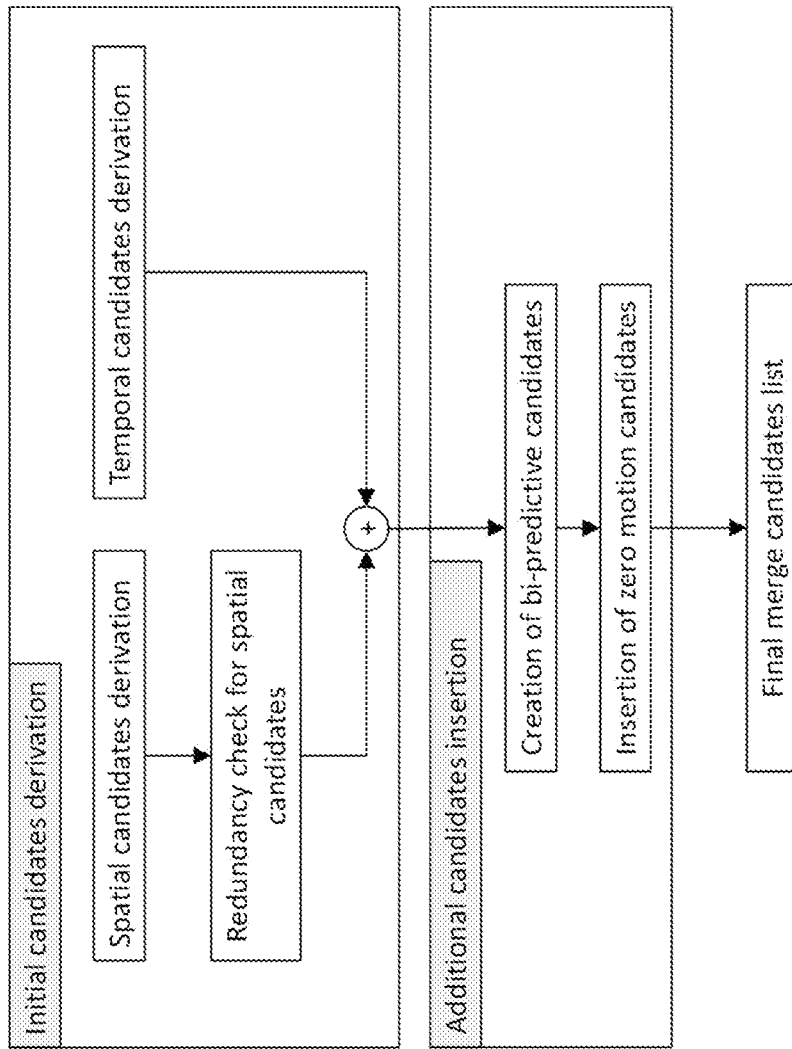


FIG. 1

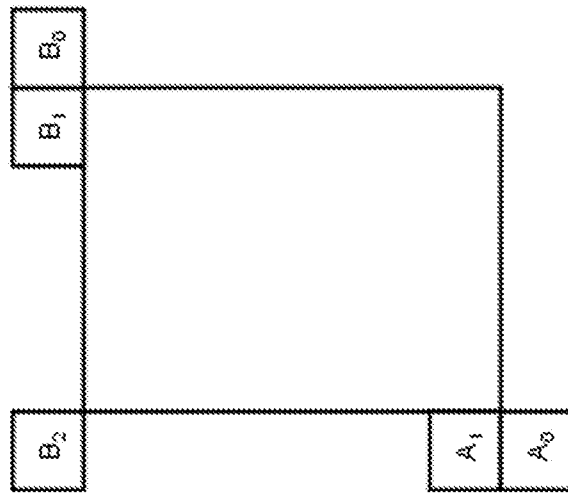


FIG. 2

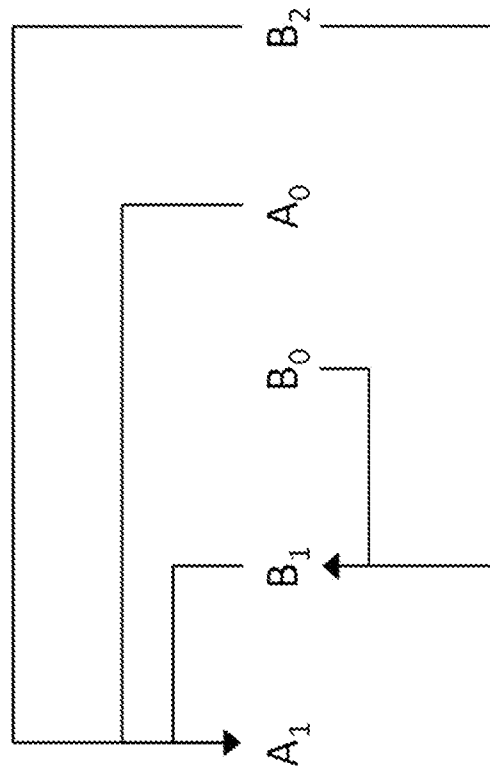


FIG. 3

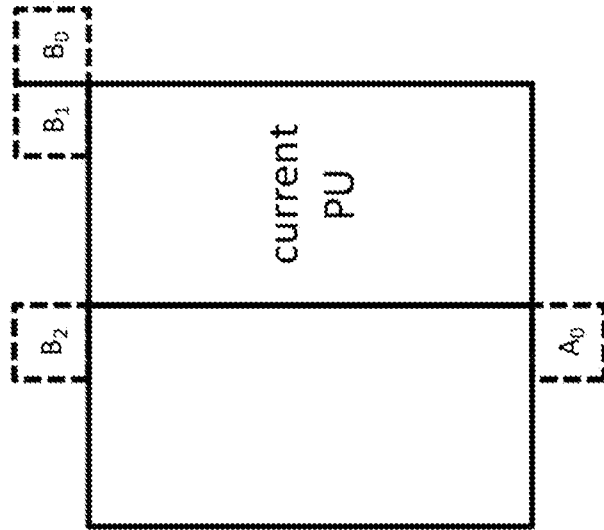


FIG. 4A

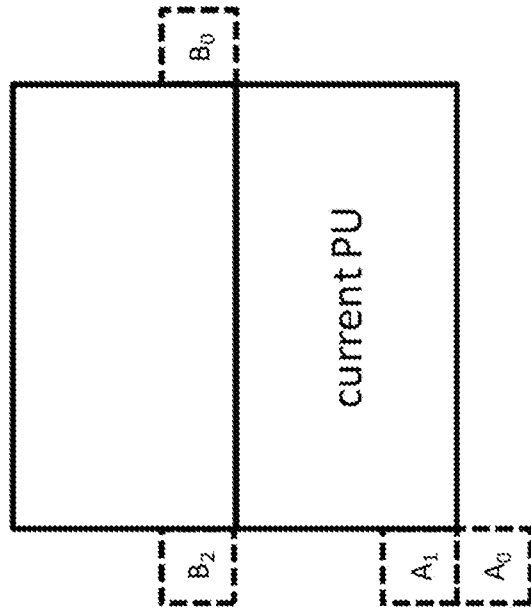


FIG. 4B

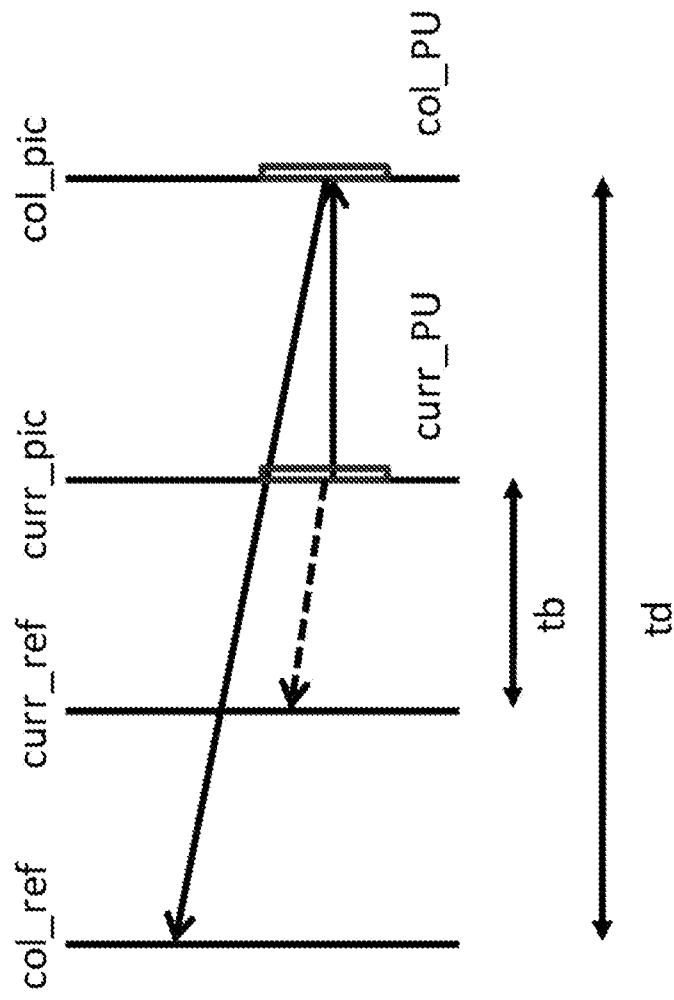


FIG. 5

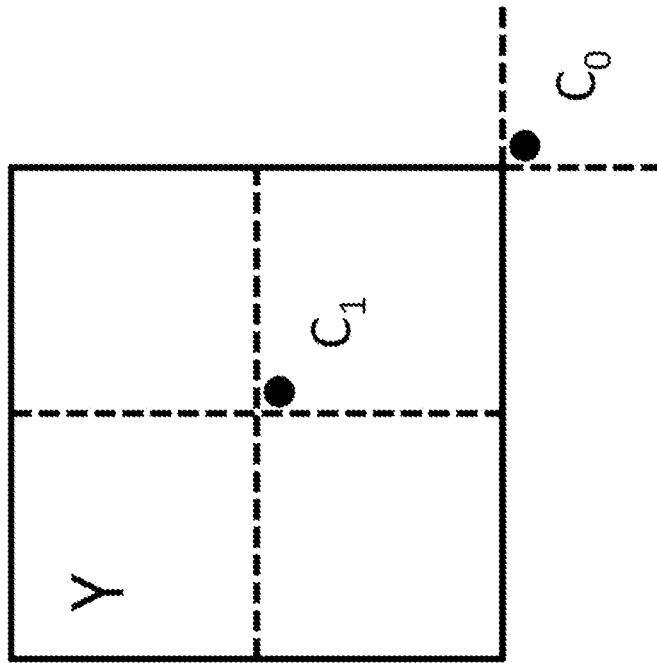


FIG. 6

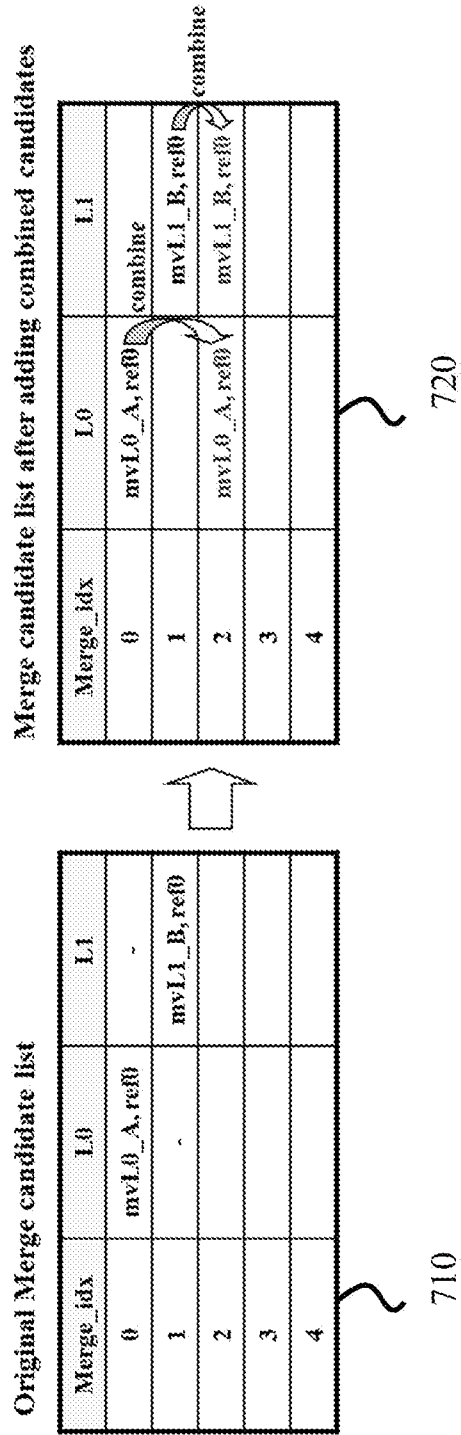


FIG. 7

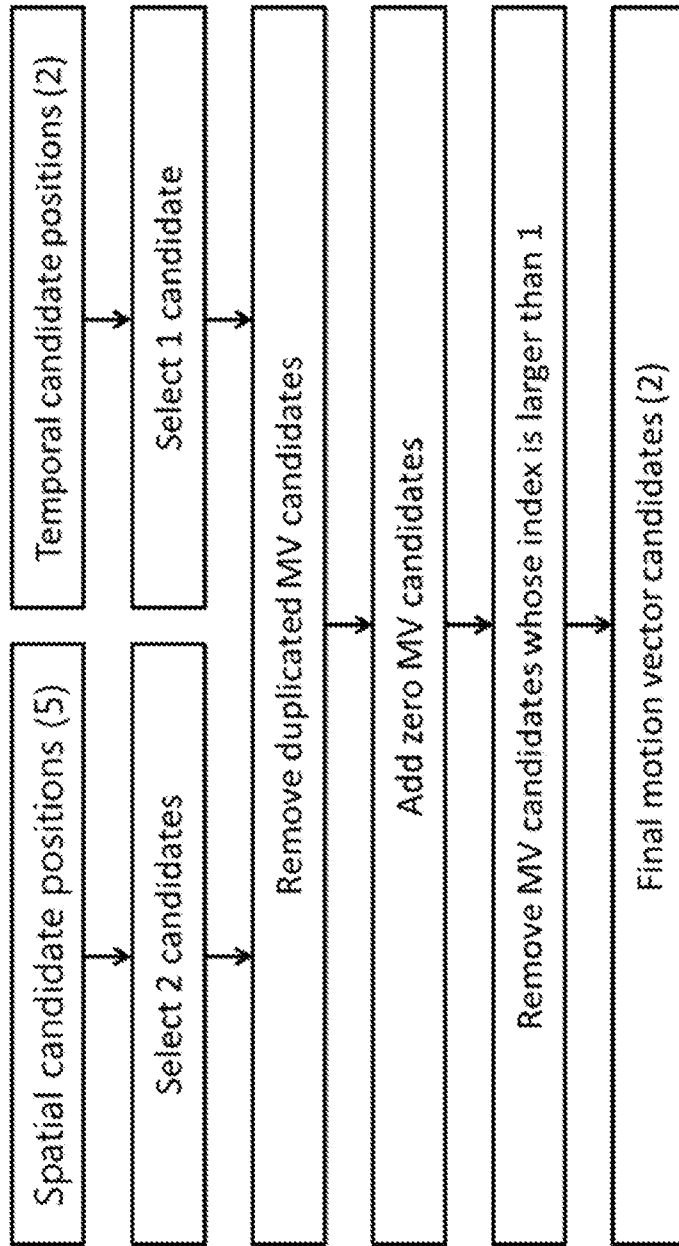


FIG. 8

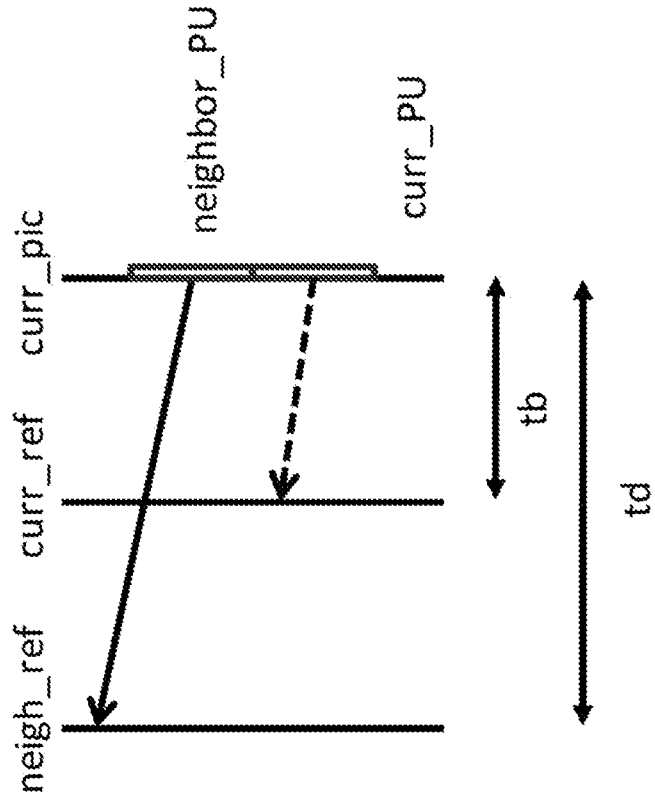


FIG. 9

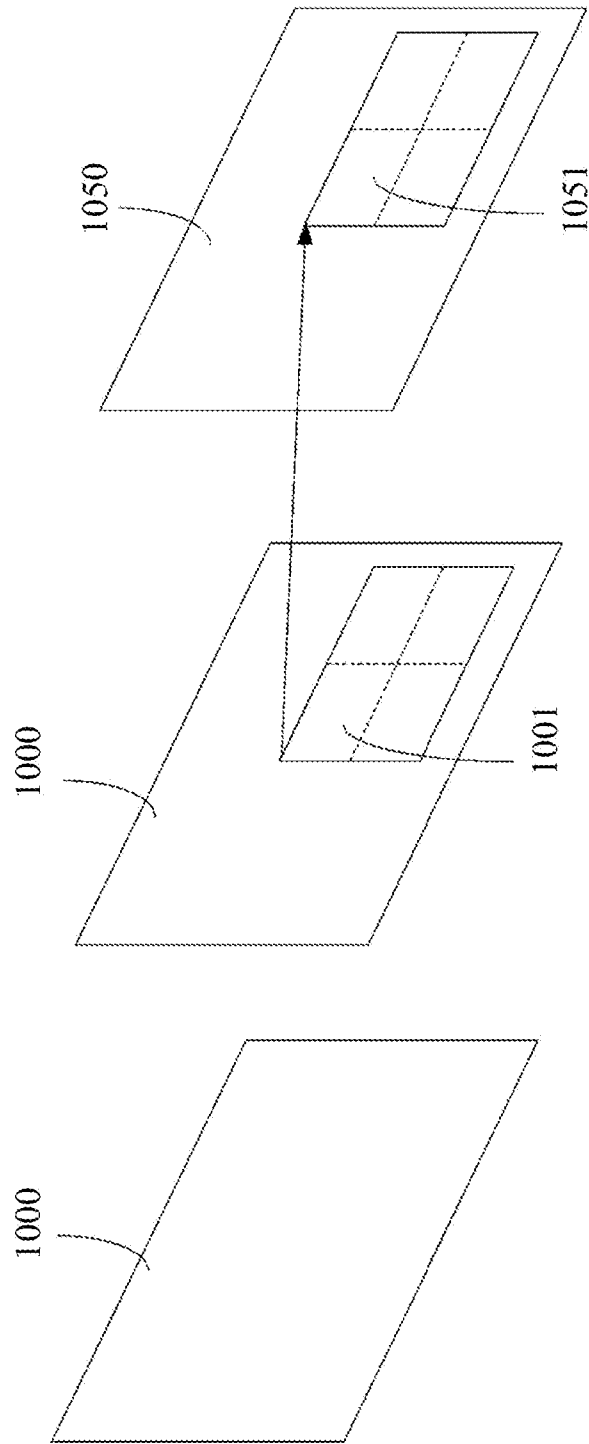


FIG. 10

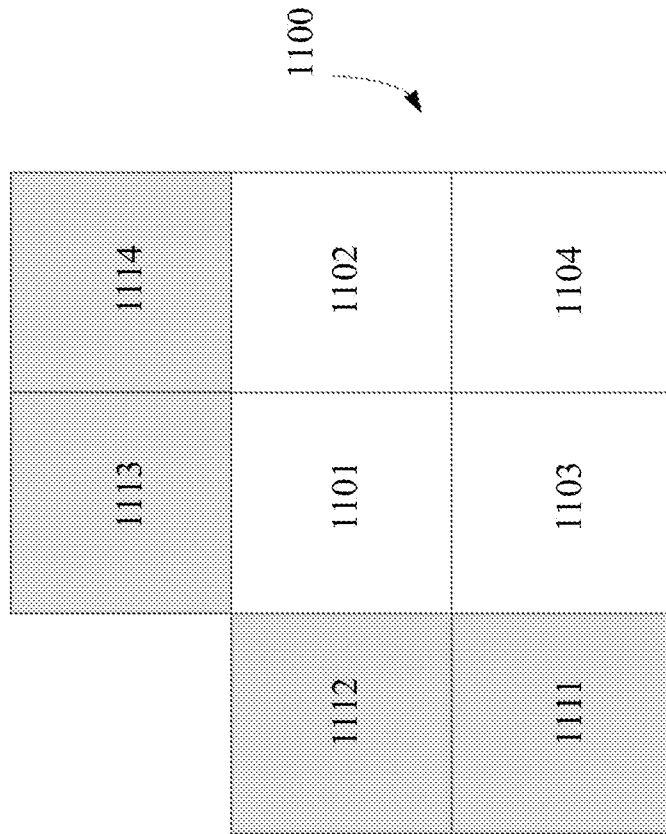


FIG. 11

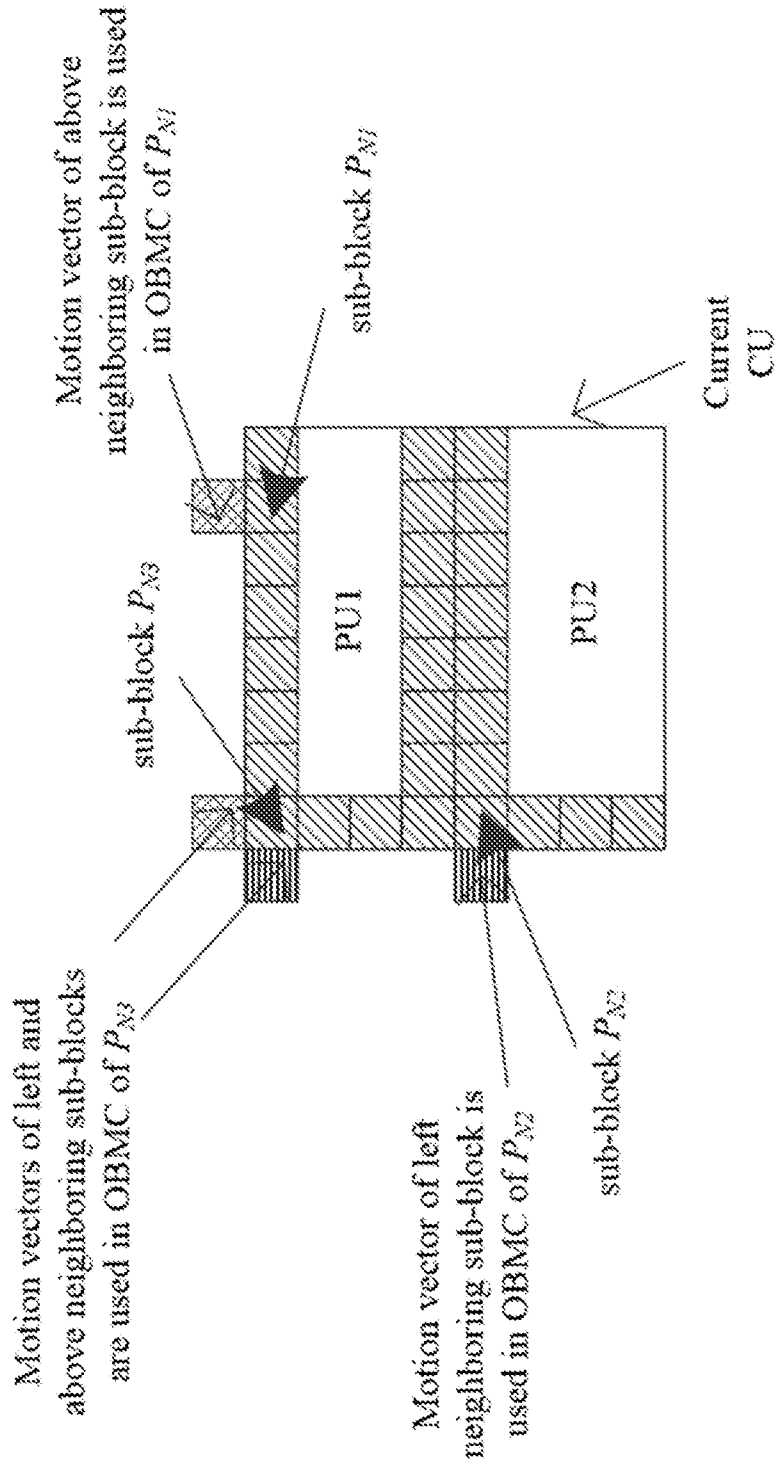


FIG. 12A

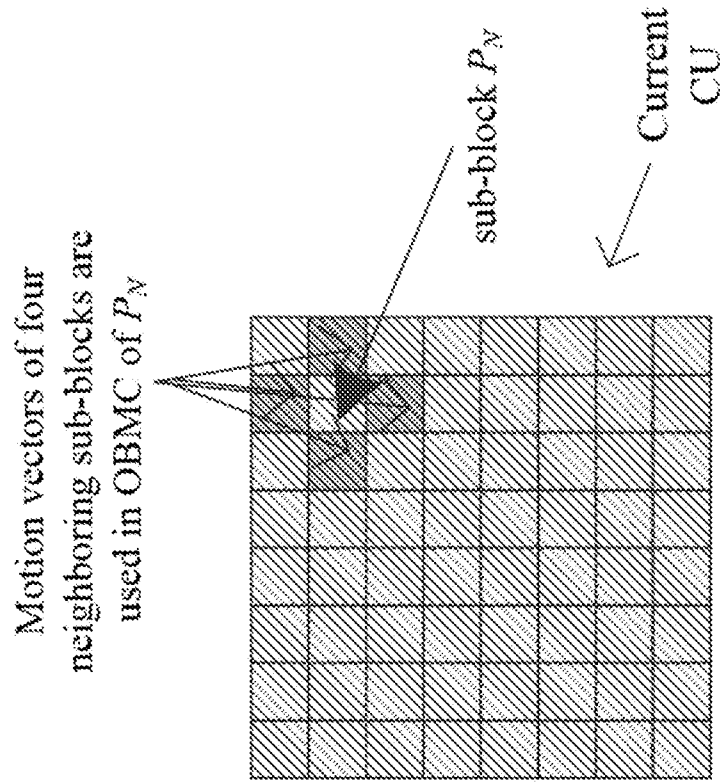


FIG. 12B

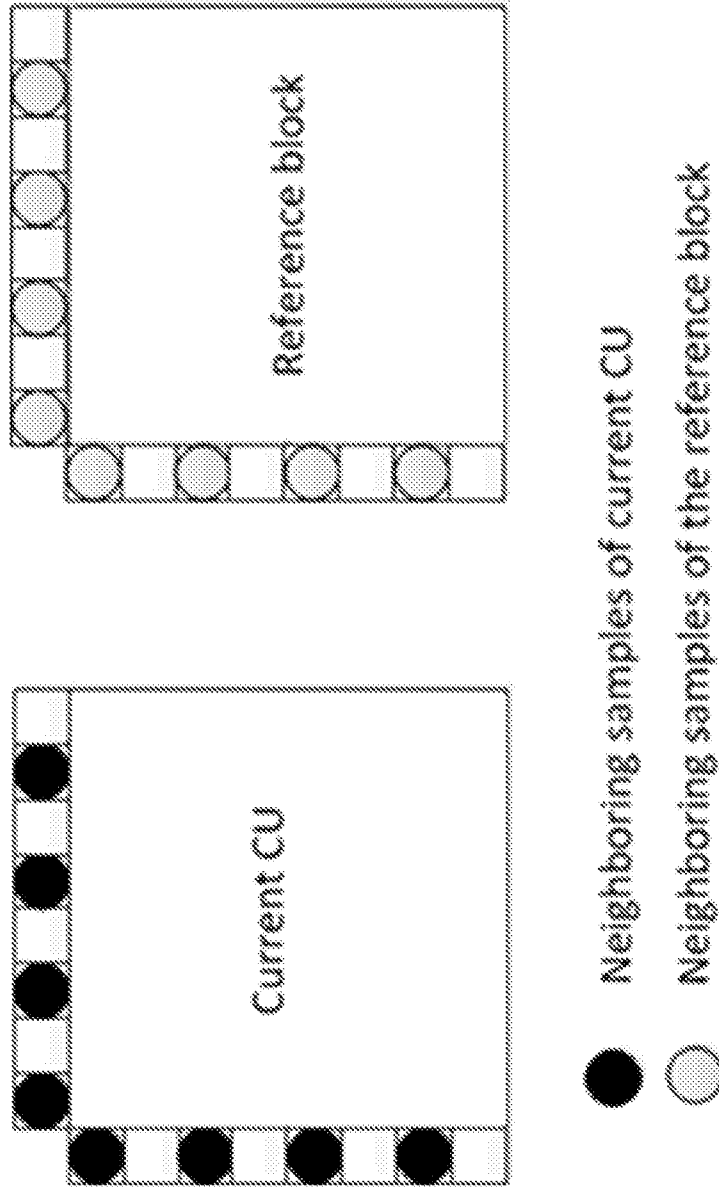


FIG. 13

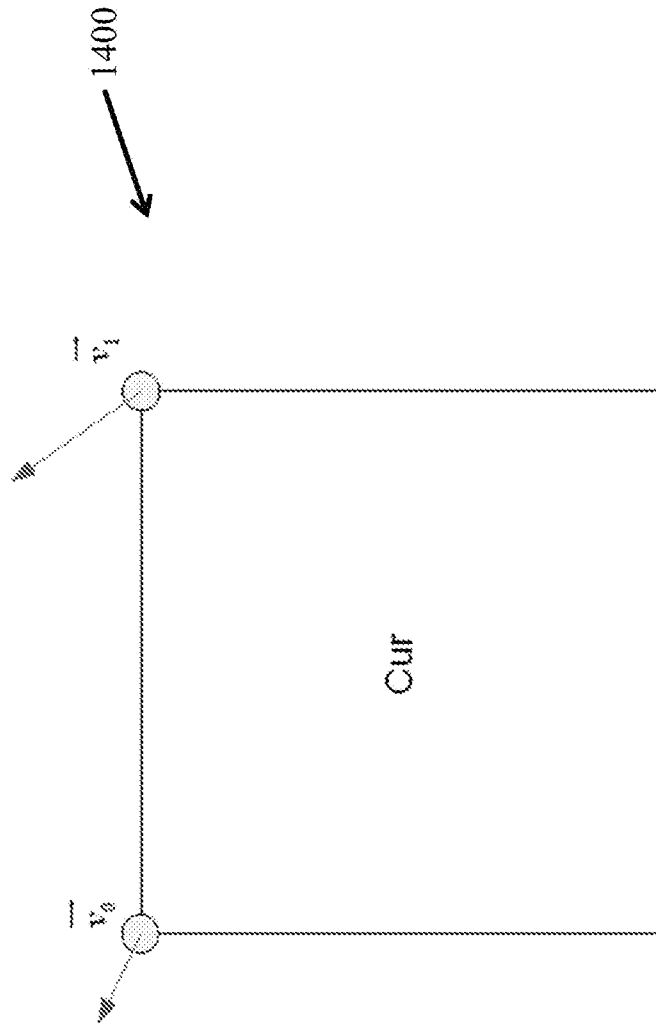


FIG. 14

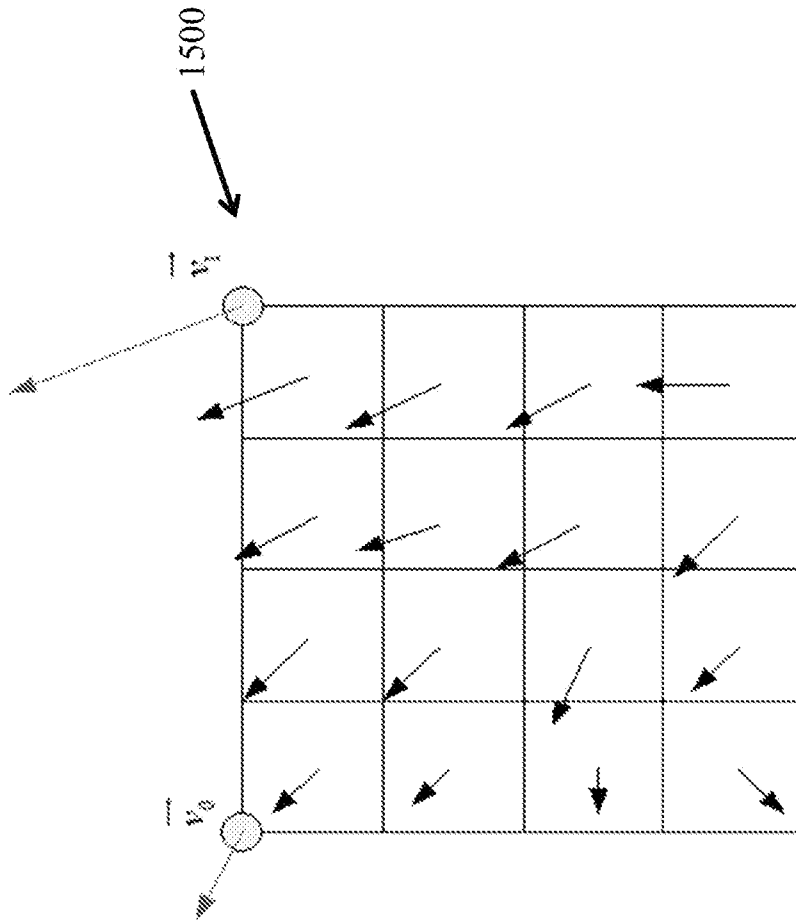


FIG. 15

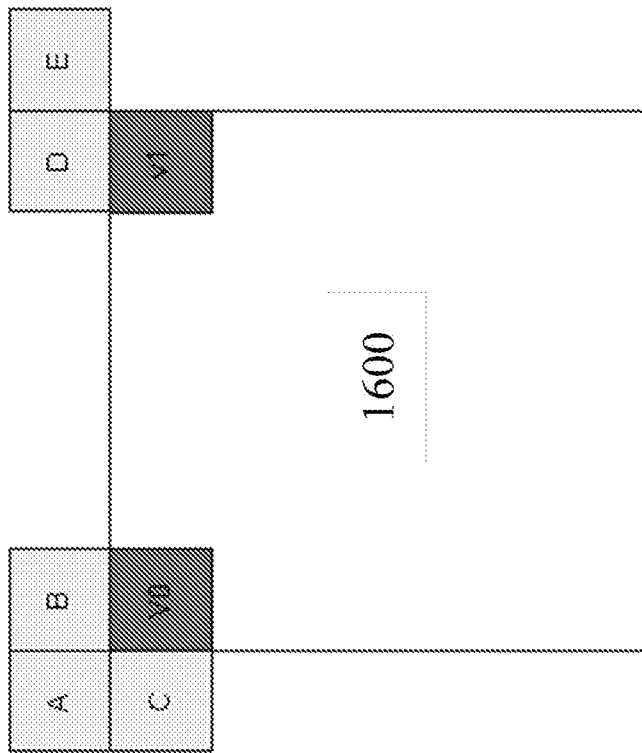


FIG. 16

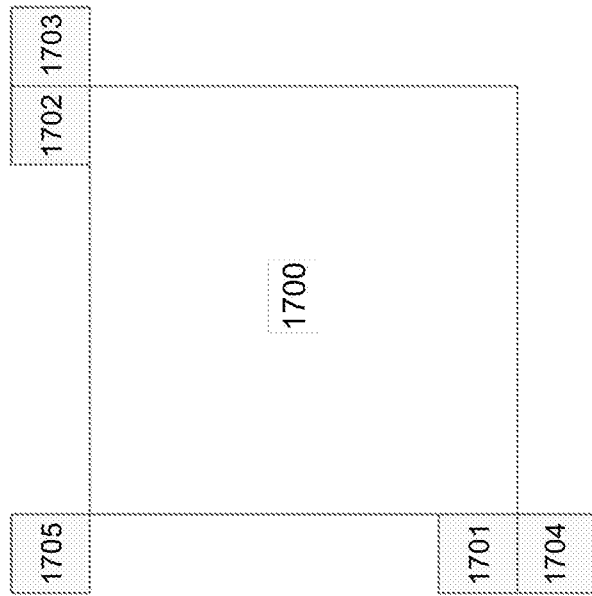


FIG. 17A

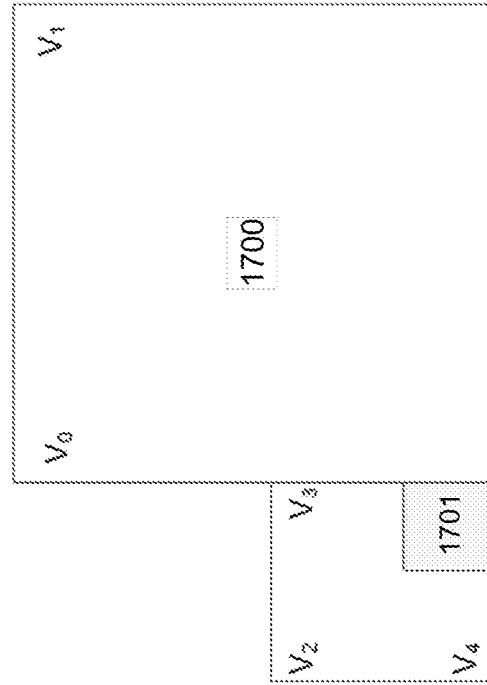


FIG. 17B

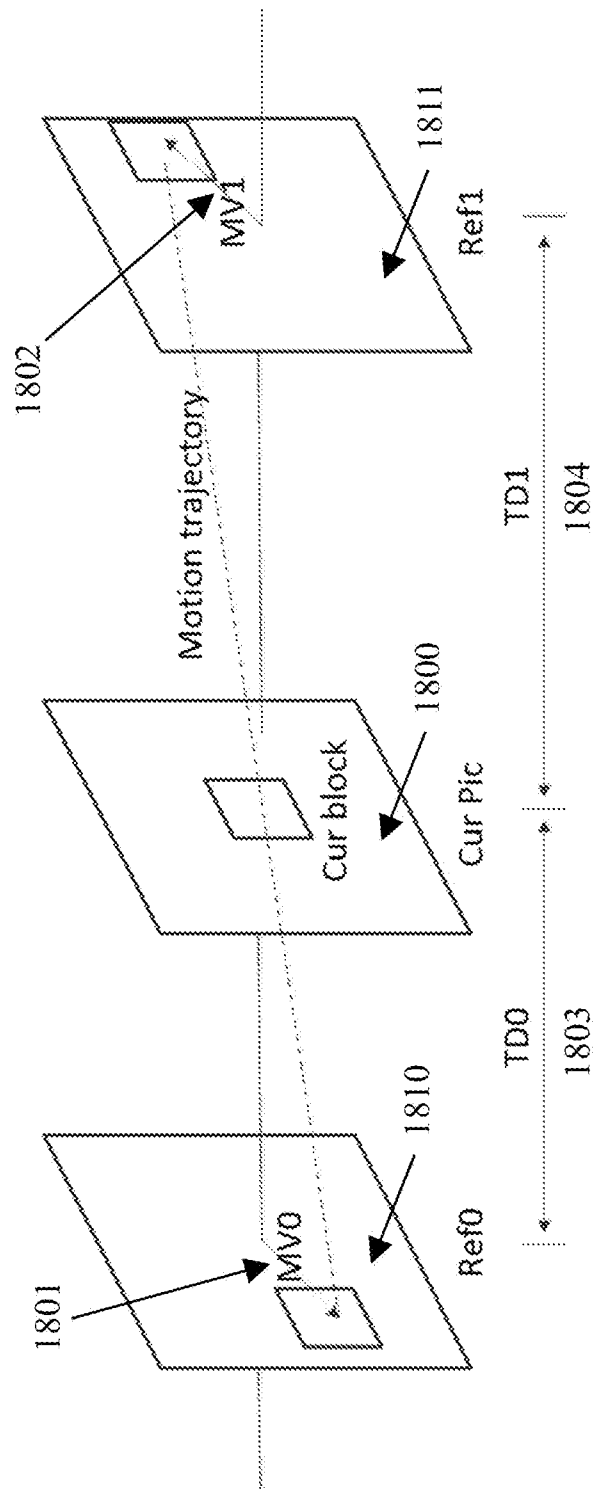


FIG. 18

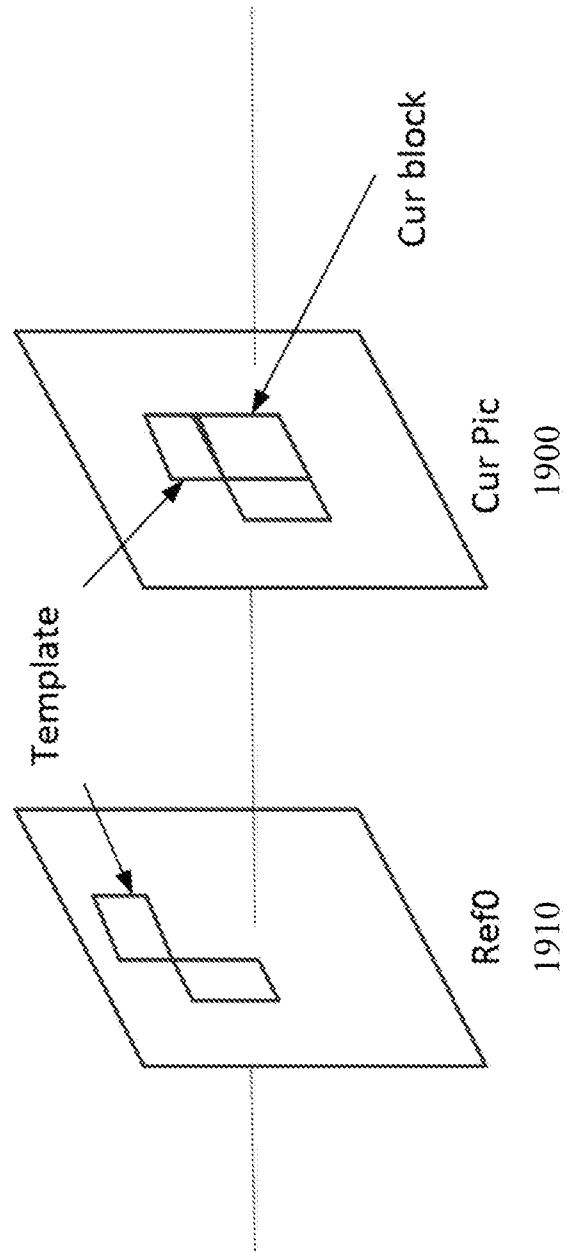


FIG. 19

2000

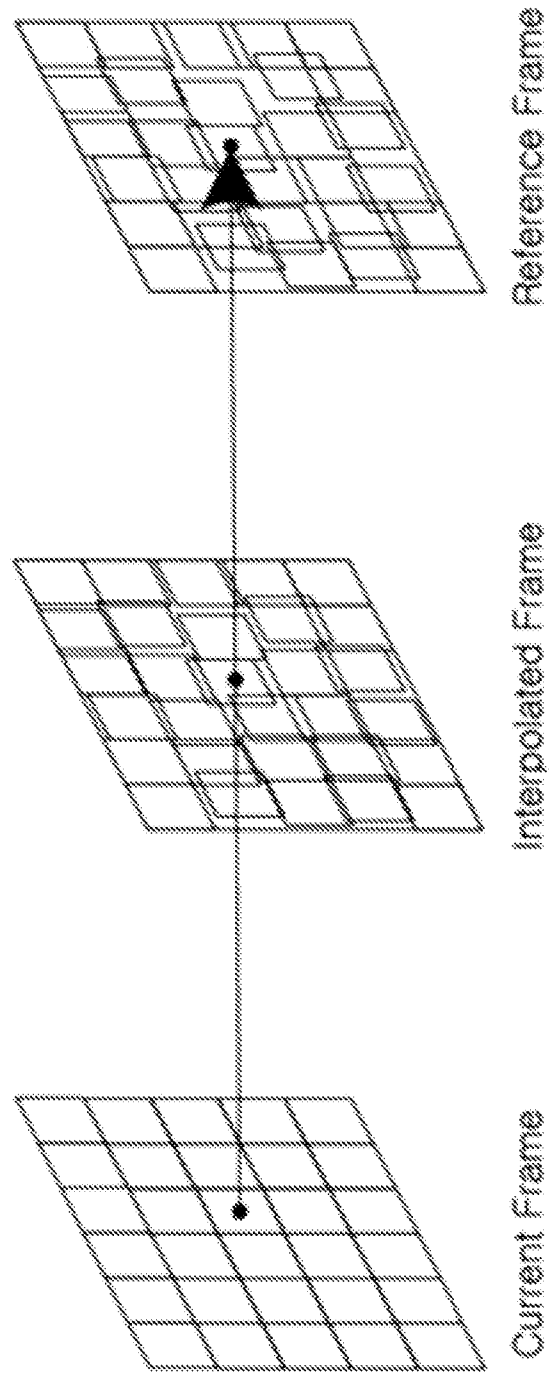


FIG. 20

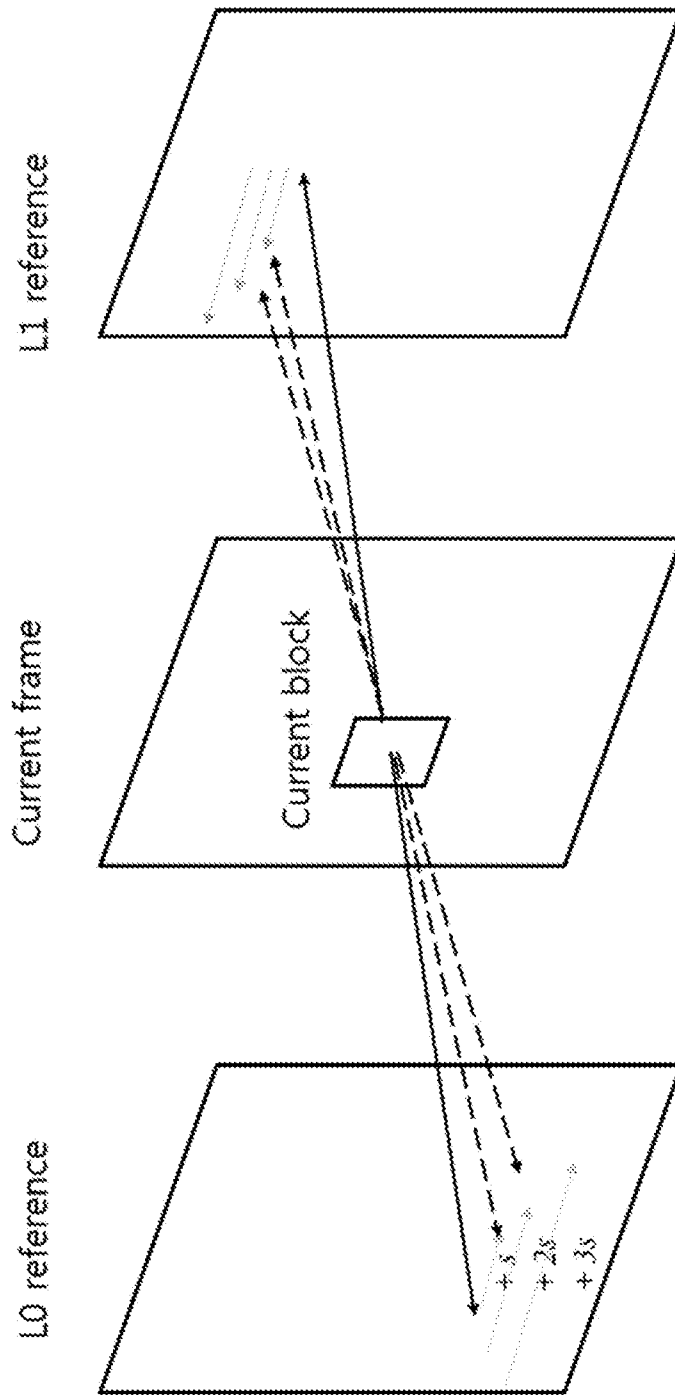
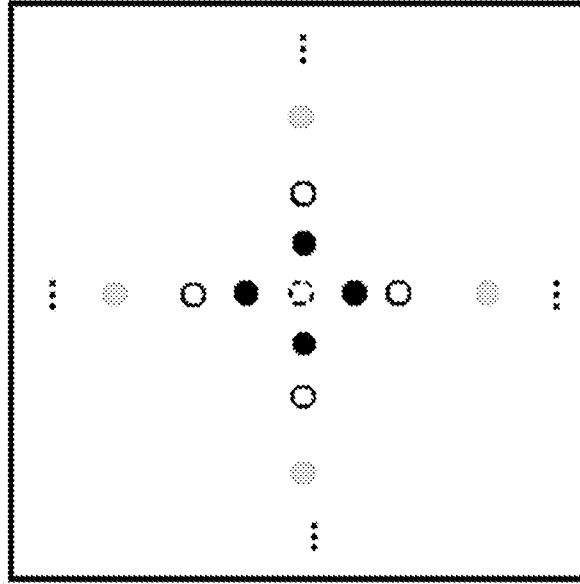


FIG. 21

L1 reference



L0 reference

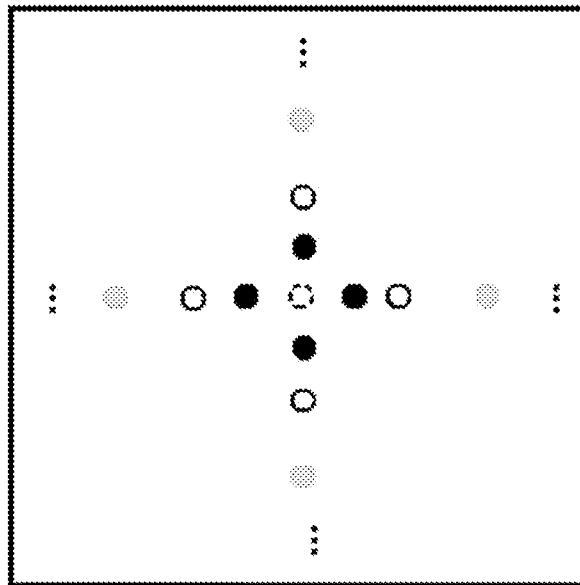


FIG. 22B

FIG. 22A

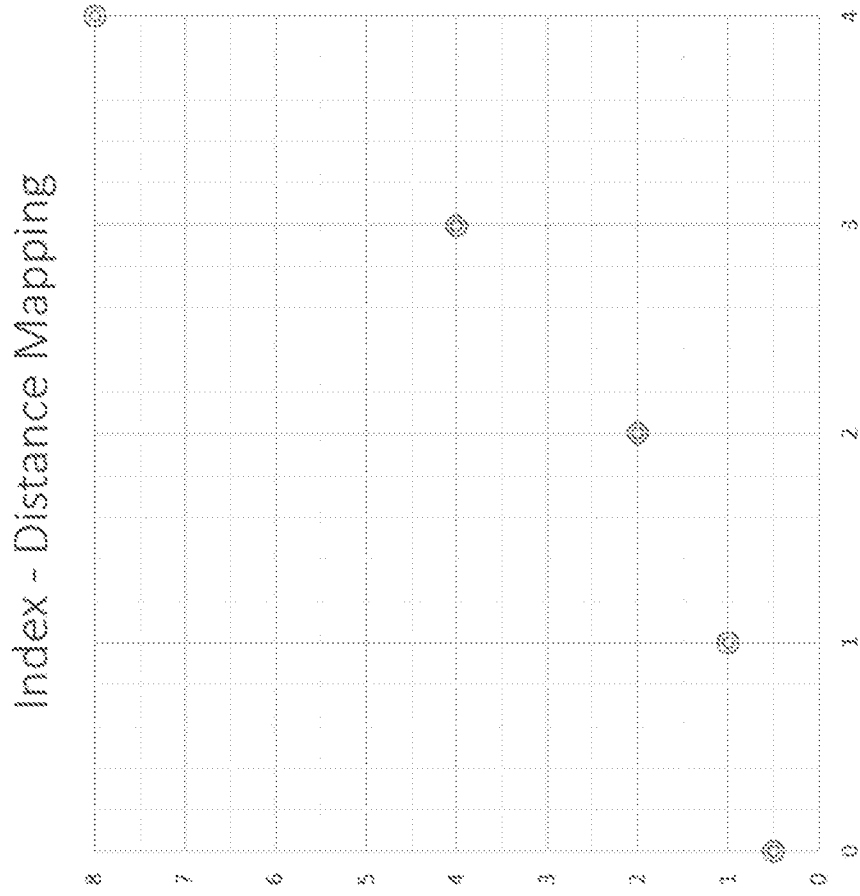


FIG. 23

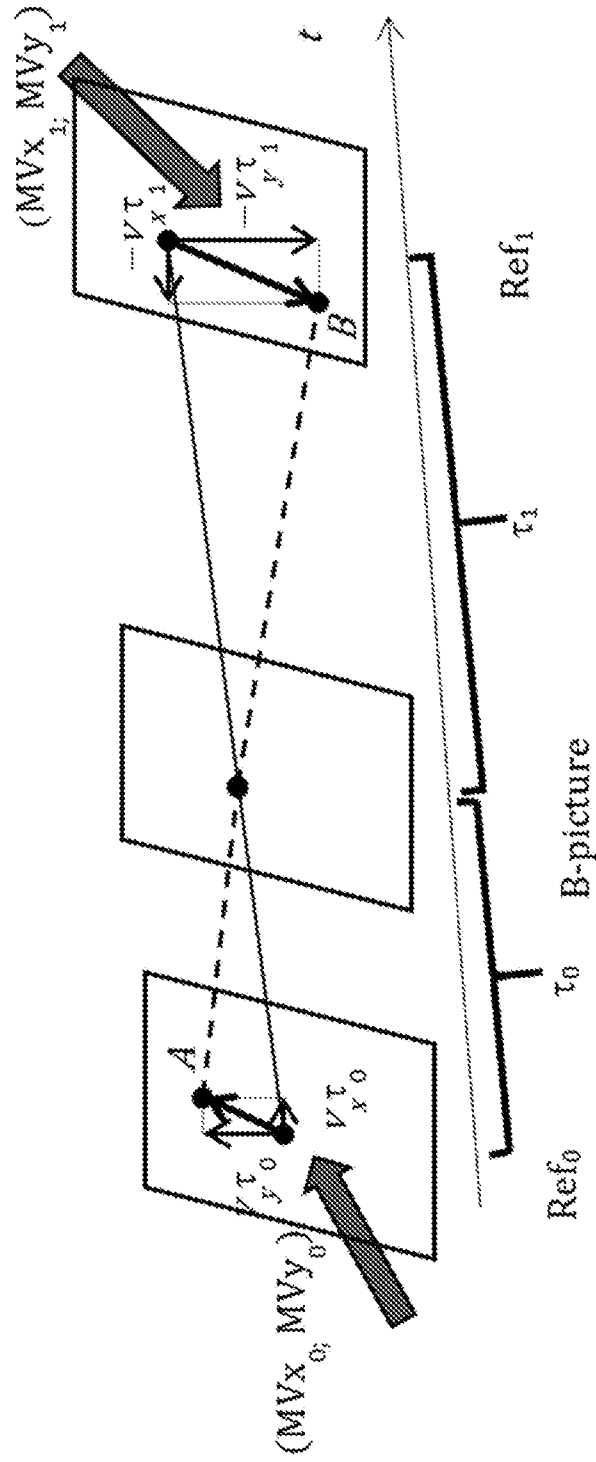


FIG. 24

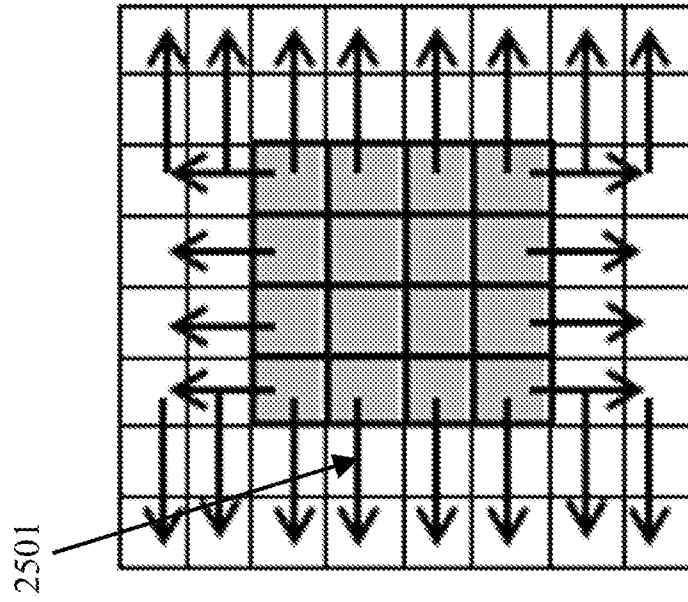


FIG. 25A

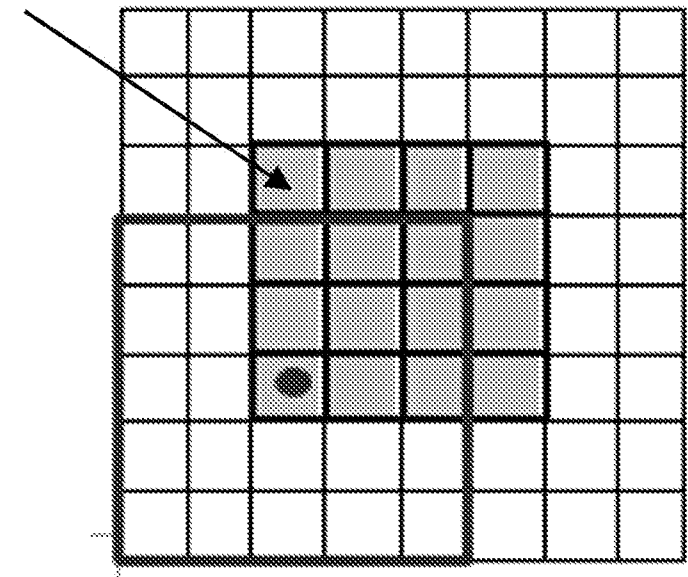


FIG. 25B

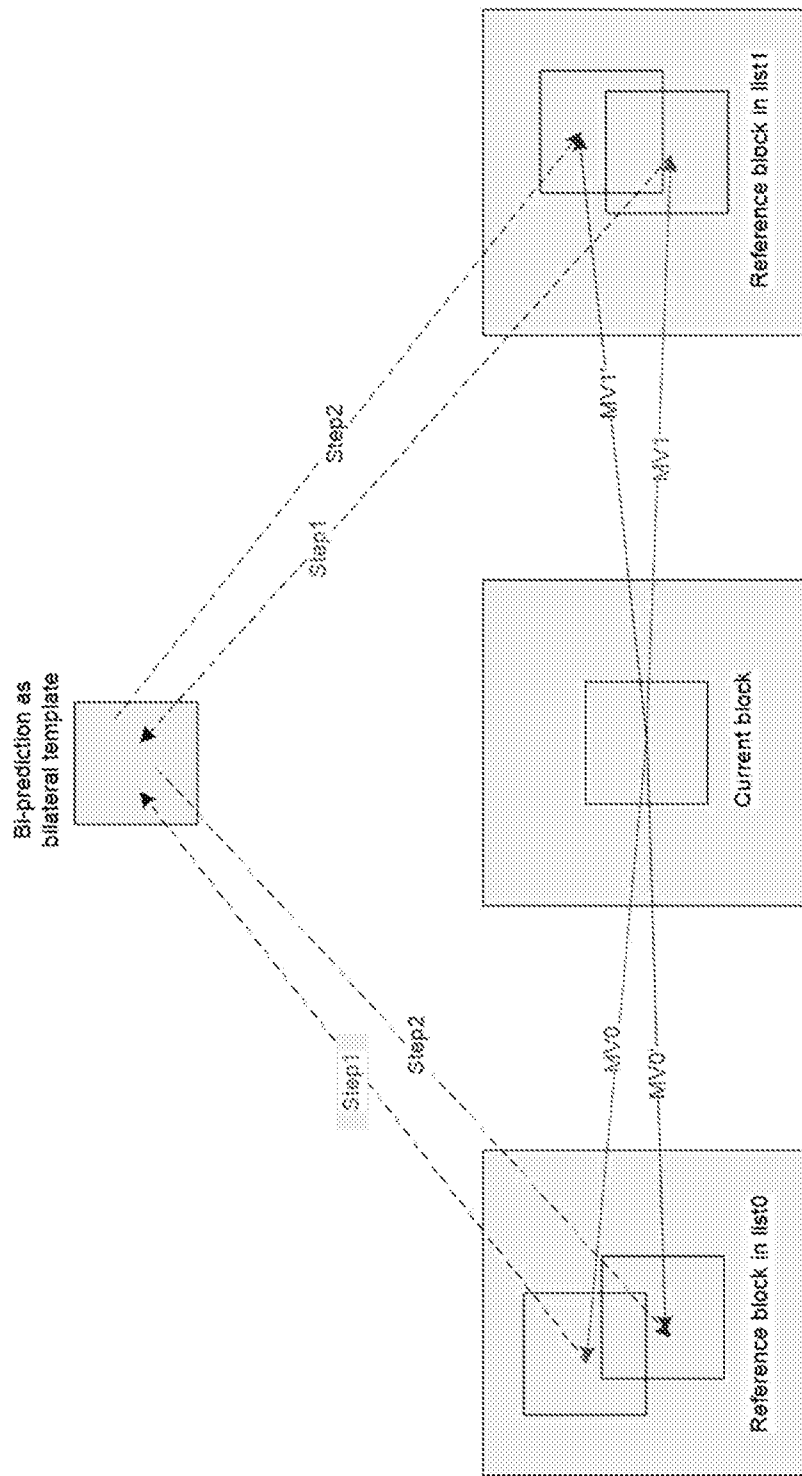


FIG. 26

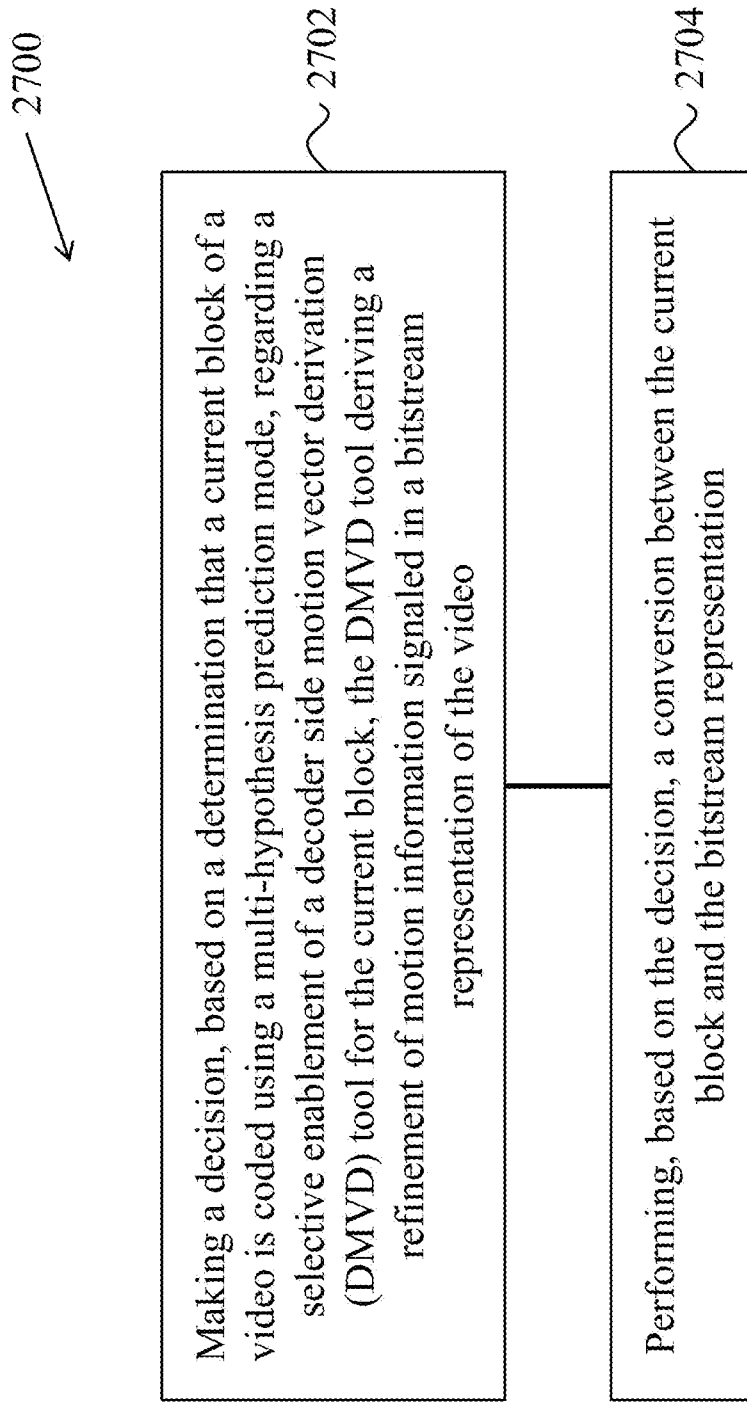


FIG. 27A

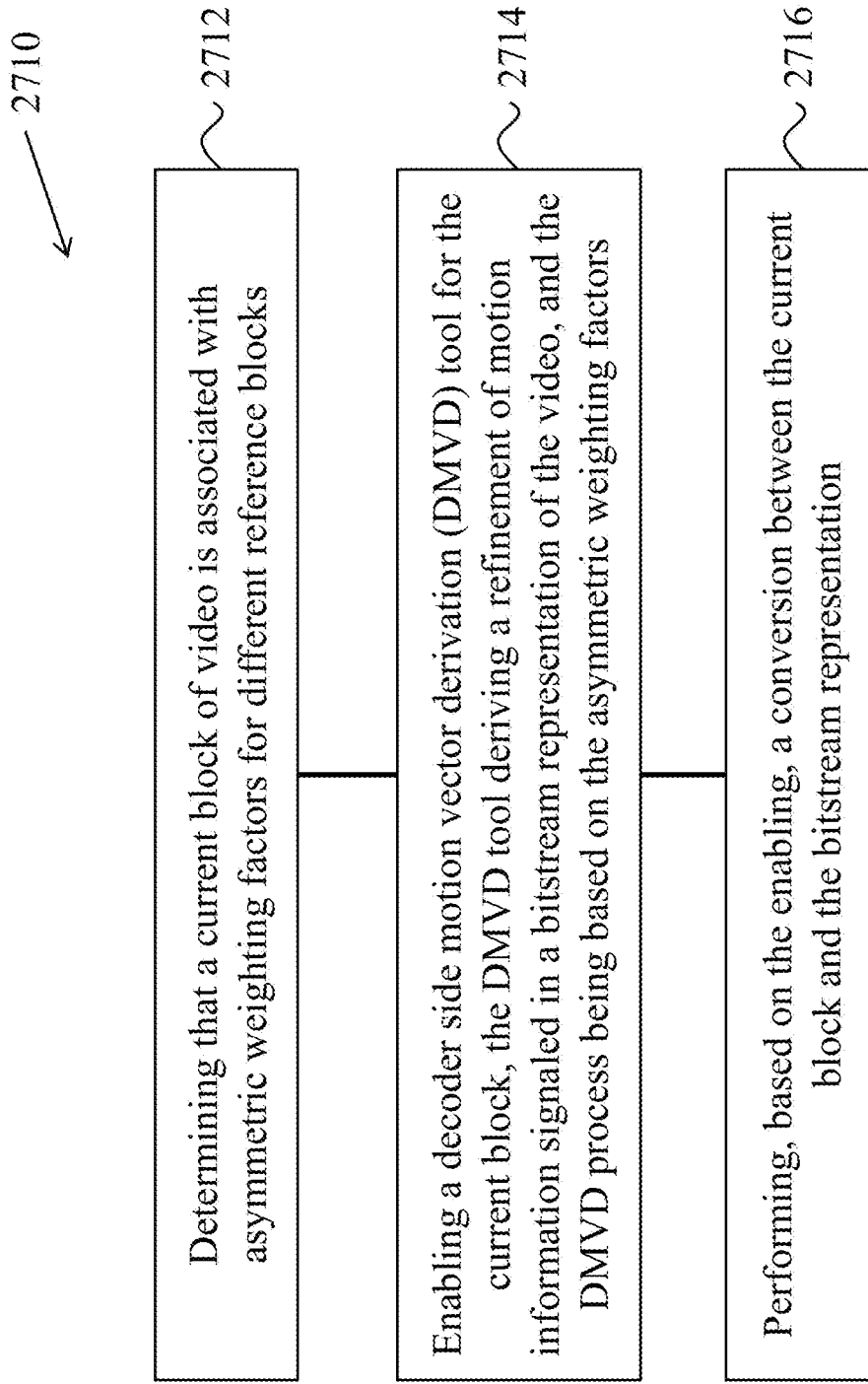


FIG. 27B

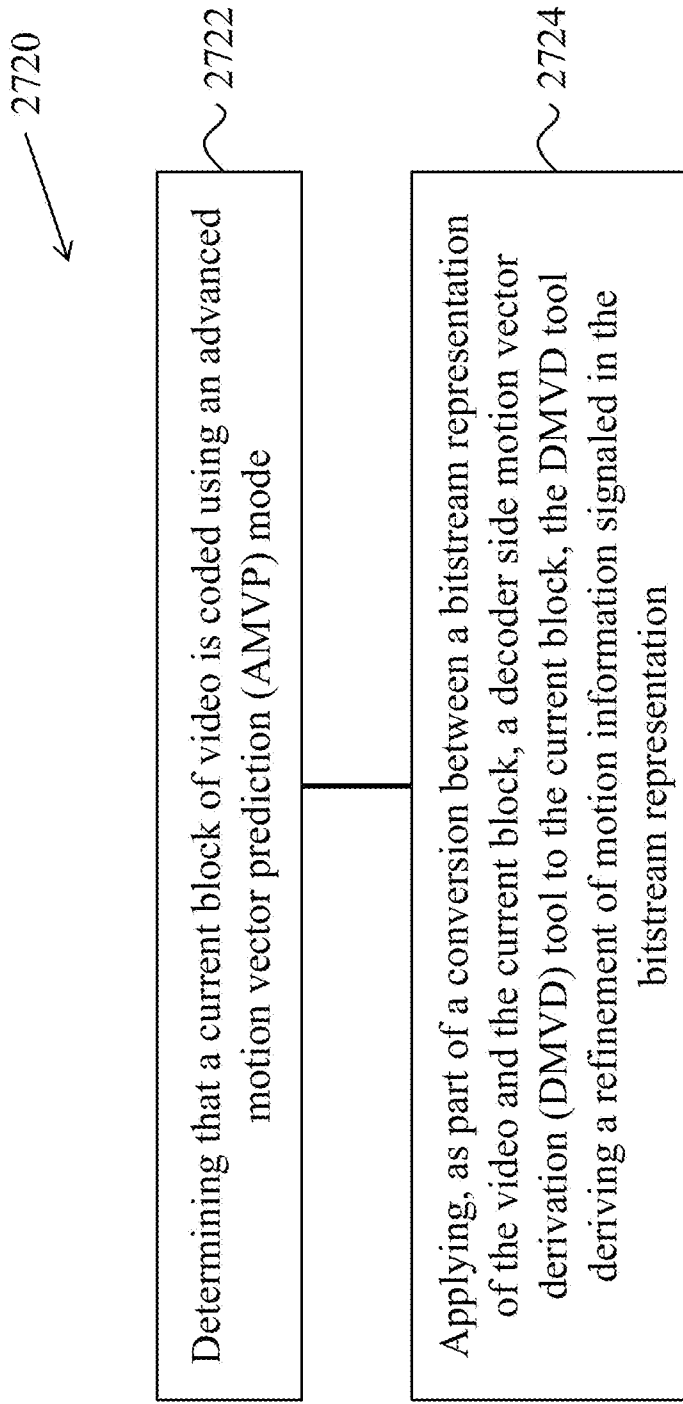


FIG. 27C

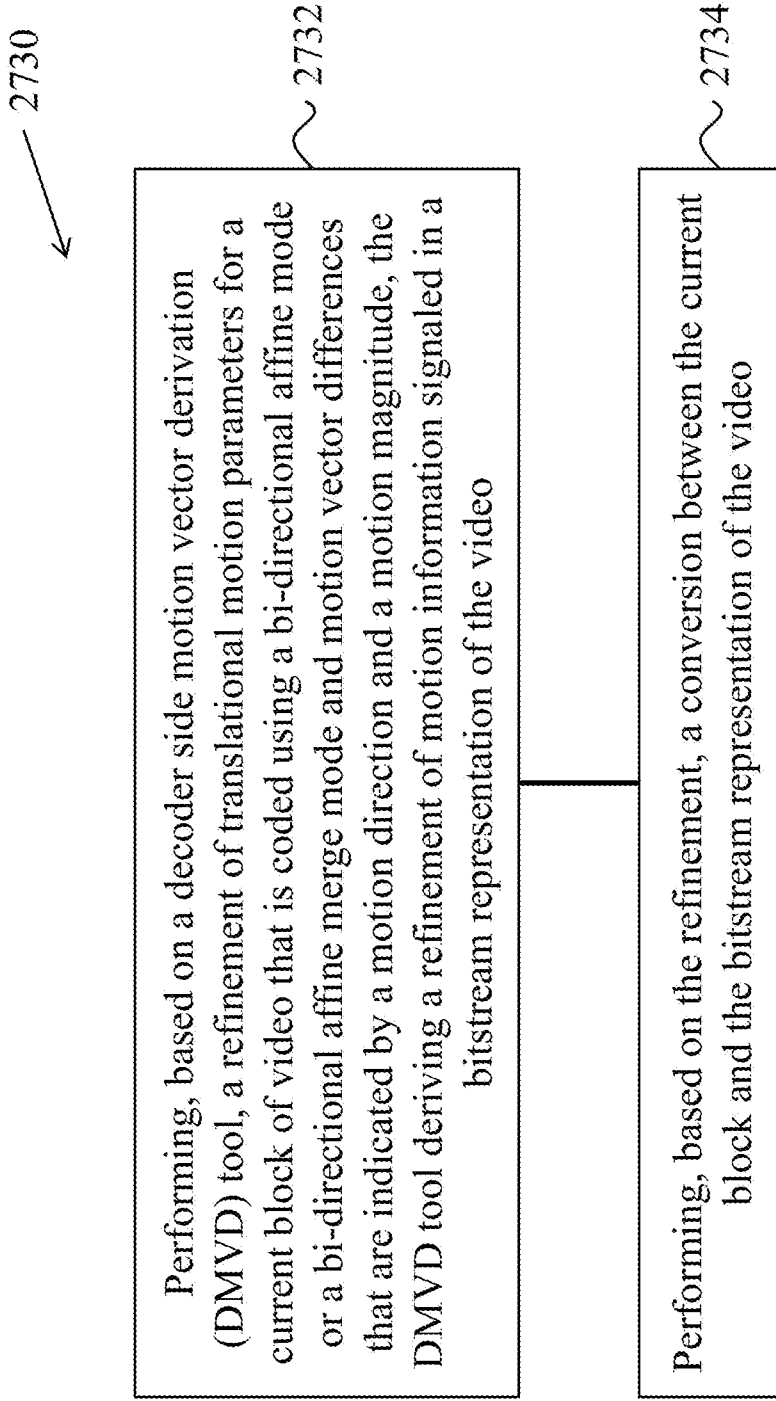


FIG. 27D

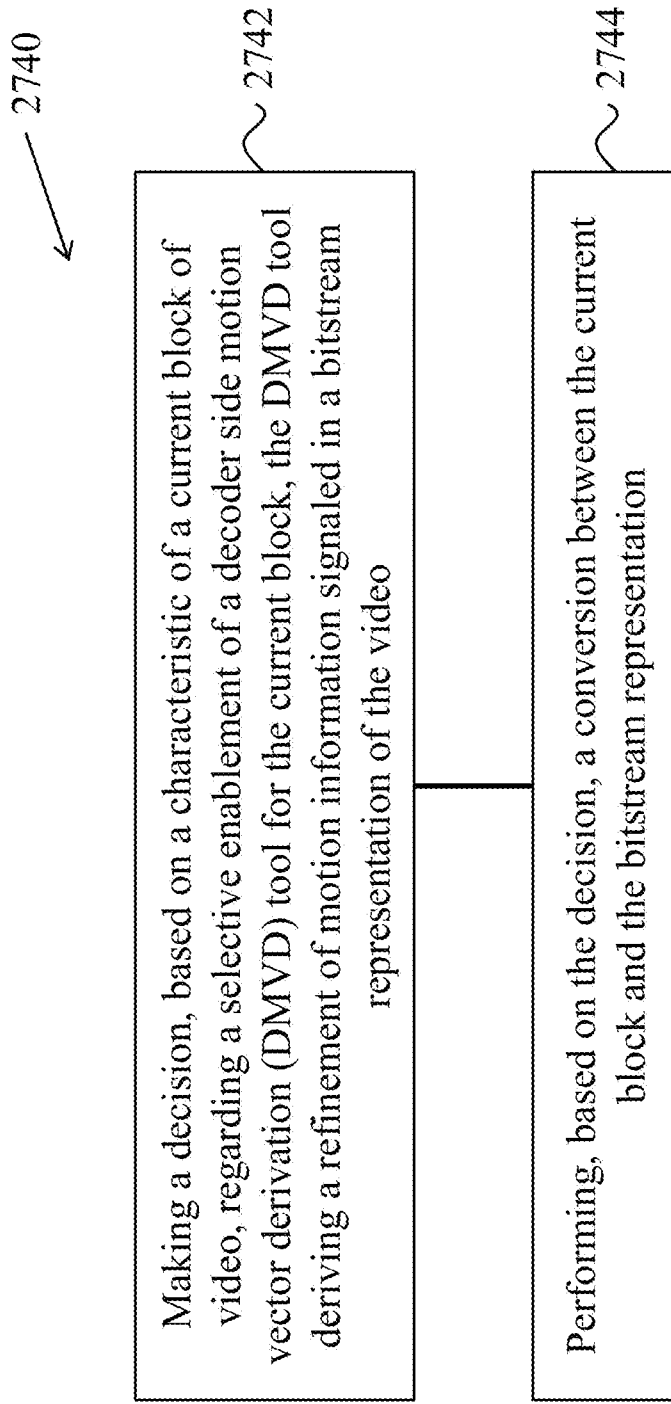


FIG. 27E

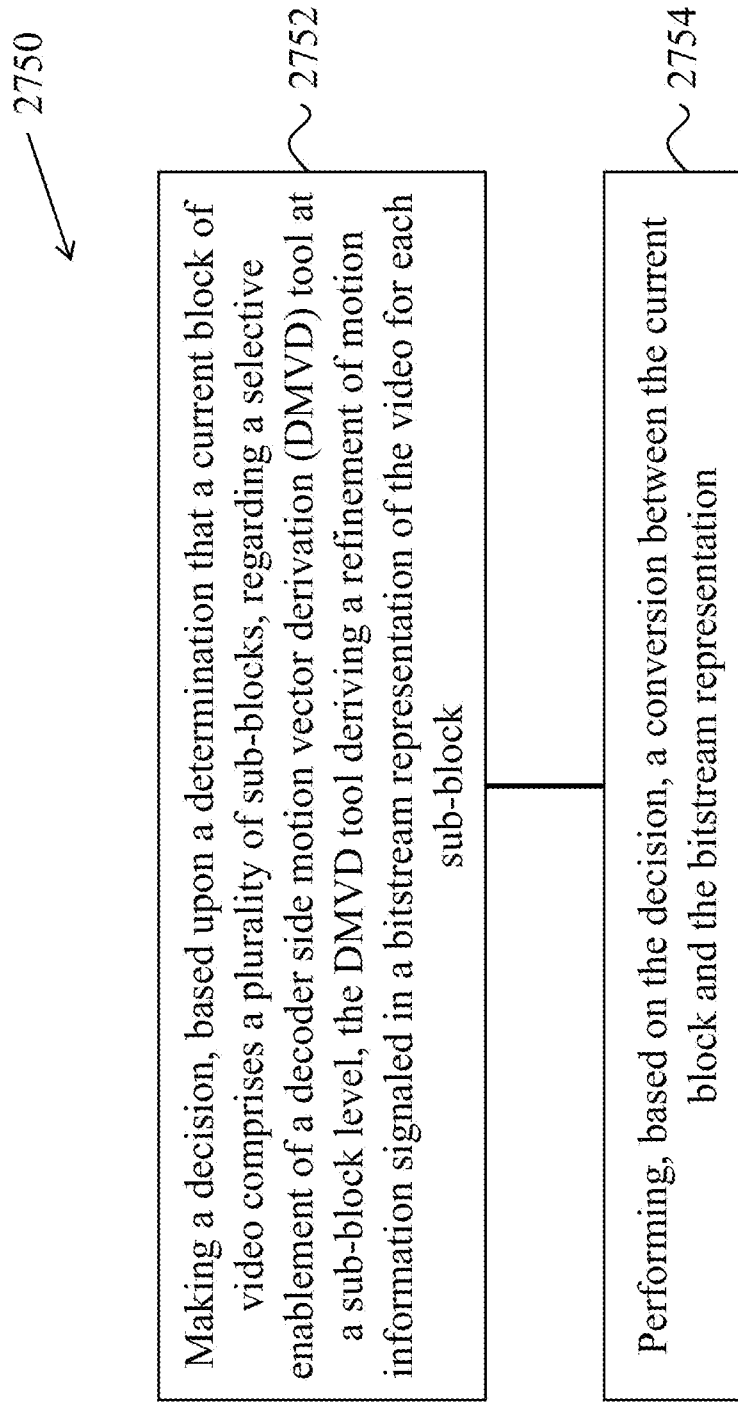


FIG. 27F

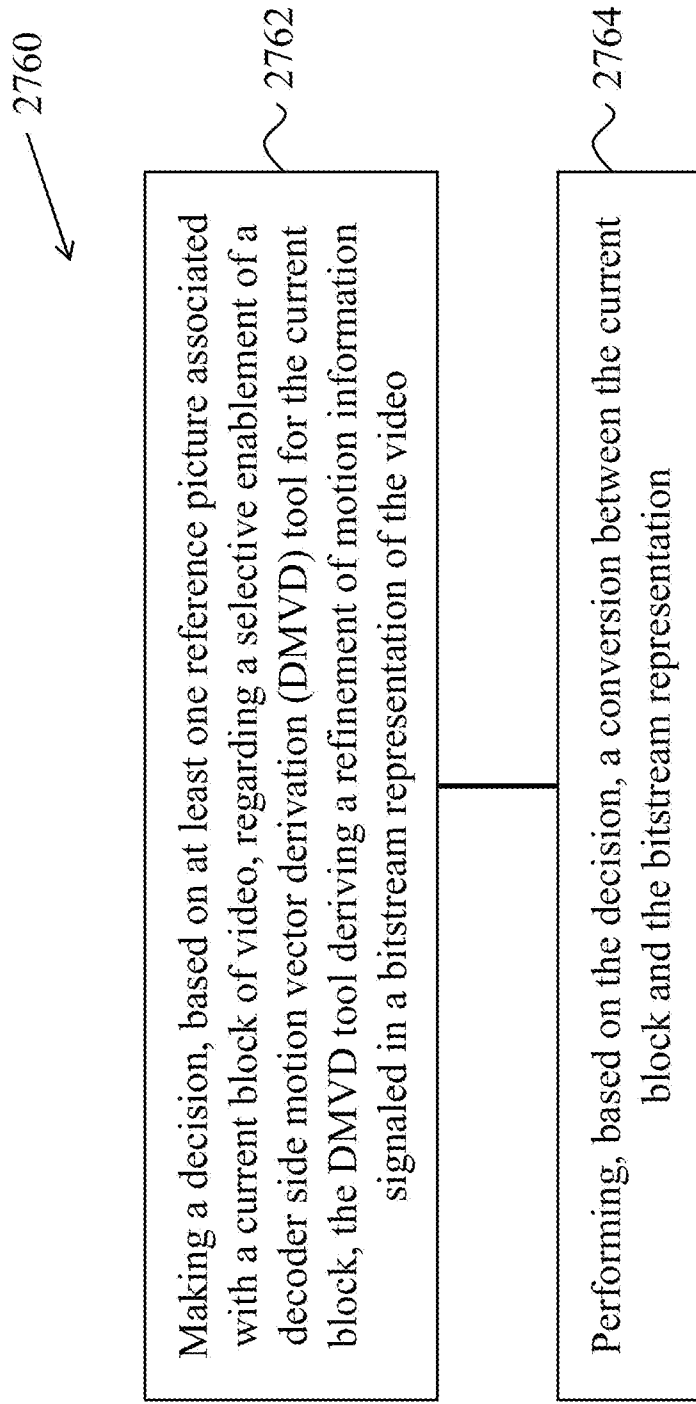


FIG. 27G

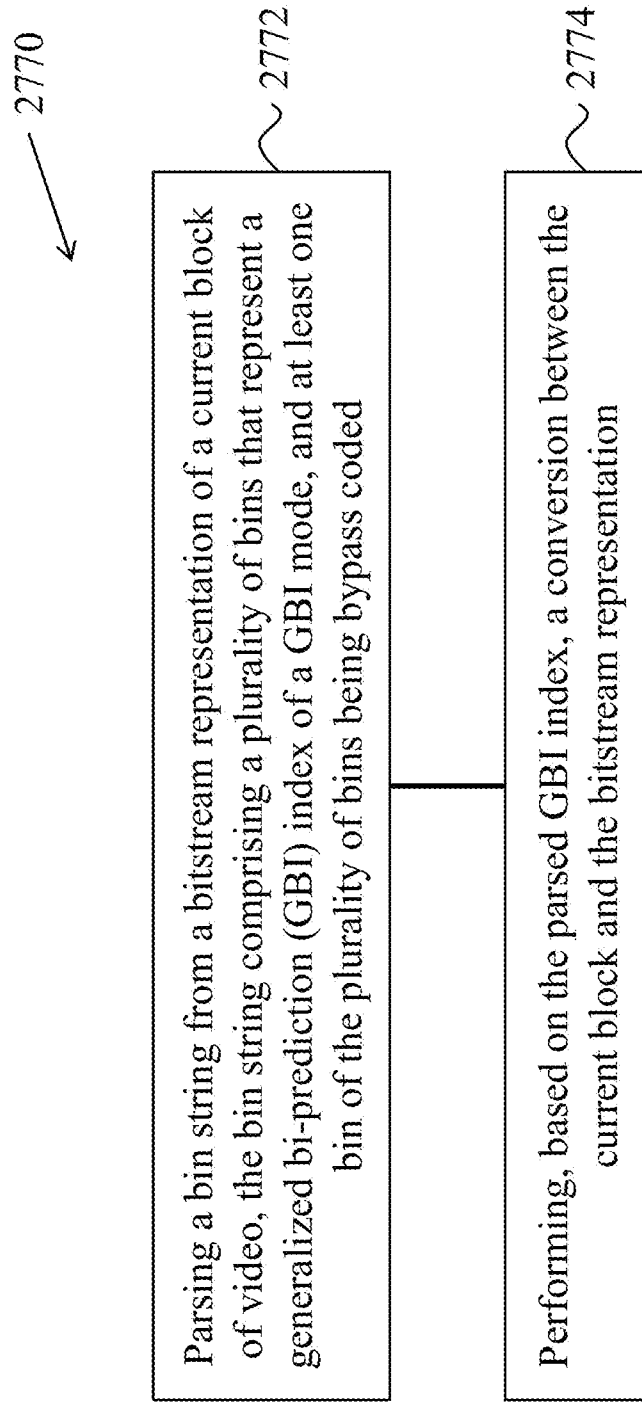


FIG. 27H

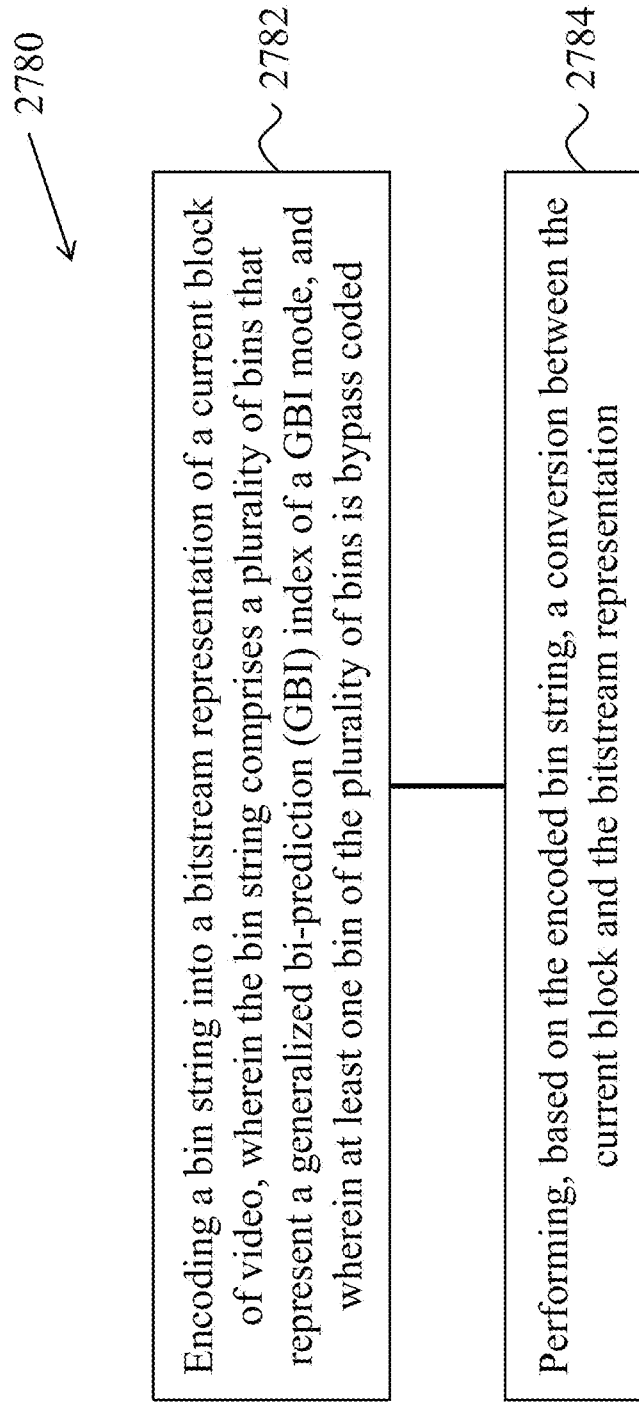


FIG. 27I

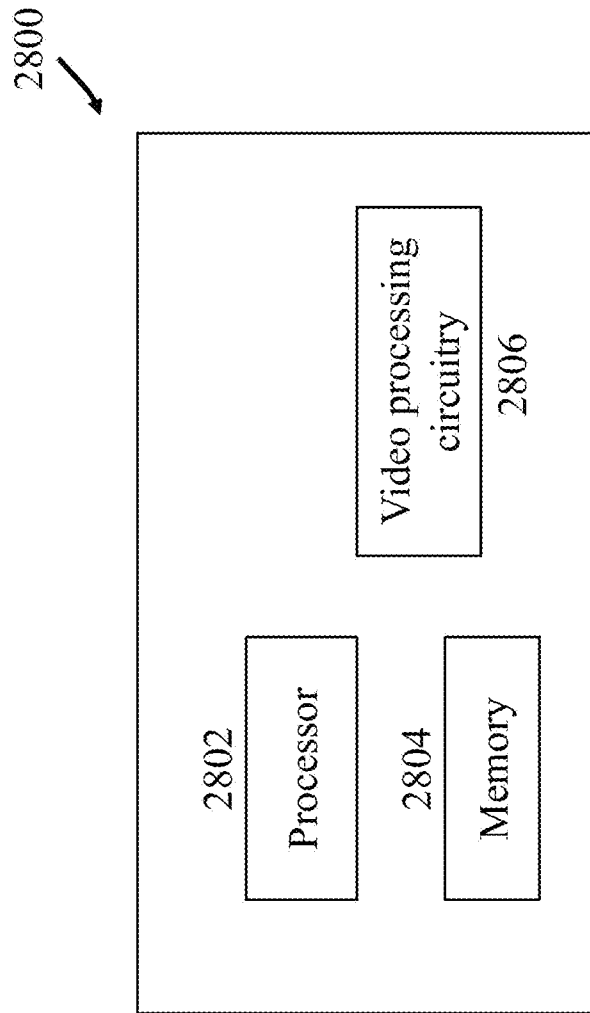


FIG. 28

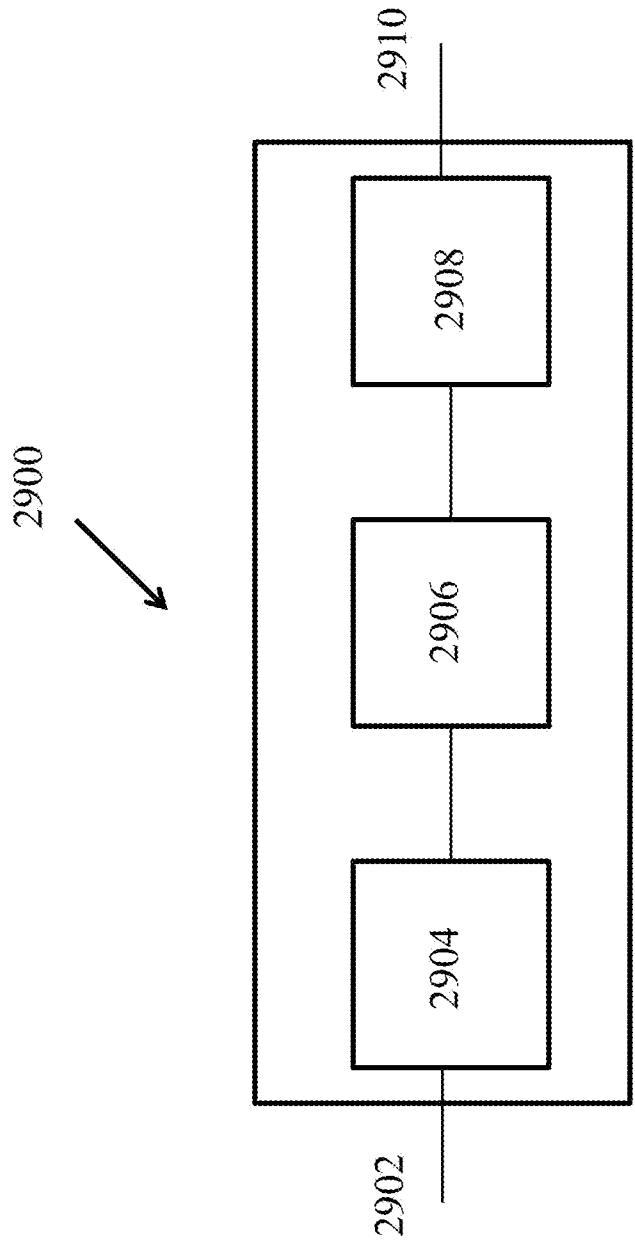


FIG. 29

INTERNATIONAL SEARCH REPORT

International application No
PCT/IB2019/058975

A. CLASSIFICATION OF SUBJECT MATTER INV. H04N19/159 H04N19/52 H04N19/573 ADD.		
According to International Patent Classification (IPC) or to both national classification and IPC		
B. FIELDS SEARCHED Minimum documentation searched (classification system followed by classification symbols) H04N		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EPO-Internal		
C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	Y-W HUANG (MEDIATEK) ET AL: "Video coding technology proposal by Mediatek", 1. JCT-VC MEETING; 15-4-2010 - 23-4-2010; DRESDEN; (JOINTCOLLABORATIVE TEAM ON VIDEO CODING OF ISO/IEC JTC1/SC29/WG11 AND ITU-TSG.16); URL: HTTP://WFTP3.ITU.INT/AV-ARCH/JCTVC-SITE/,, no. JCTVC-A109, 18 April 2010 (2010-04-18) , XP030007543, paragraph [2.8.2] paragraph [2.12]	1-15,31, 32
----- -/--		
<input checked="" type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.		
* Special categories of cited documents :		
"A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family	
Date of the actual completion of the international search	Date of mailing of the international search report	
5 December 2019	25/02/2020	
Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Authorized officer Schoeyer, Marnix	

INTERNATIONAL SEARCH REPORT

International application No

PCT/IB2019/058975

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	<p>WINKEN (FRAUNHOFER) M ET AL: "CE10: Multi-hypothesis inter prediction (Tests 1.2.a - 1.2.c)", 12. JVET MEETING; 20181003 - 20181012; MACAO; (THE JOINT VIDEO EXPLORATION TEAM OF ISO/IEC JTC1/SC29/WG11 AND ITU-T SG.16), , no. JVET-L0148 9 October 2018 (2018-10-09), XP030195291, Retrieved from the Internet: URL:http://phenix.int-evry.fr/jvet/doc_end_user/documents/12_Macao/wg11/JVET-L0148-v3.zip JVET-L0148 v3.docx [retrieved on 2018-10-09] page 1 paragraph [05.5]</p>	1-15,31, 32
X	<p>----- WINKEN (FRAUNHOFER) M ET AL: "CE10: Multi-hypothesis inter prediction (Tests 1.5-1.8)", 11. JVET MEETING; 20180711 - 20180718; LJUBLJANA; (THE JOINT VIDEO EXPLORATION TEAM OF ISO/IEC JTC1/SC29/WG11 AND ITU-T SG.16), , no. JVET-K0269 2 July 2018 (2018-07-02), XP030198744, Retrieved from the Internet: URL:http://phenix.int-evry.fr/jvet/doc_end_user/documents/11_Ljubljana/wg11/JVET-K0269-v1.zip JVET-K0269.docx [retrieved on 2018-07-02] page 1 - page 2</p>	1-15,31, 32
A	<p>----- MARKUS FLIERL ET AL: "Rate-Constrained Multihypothesis Prediction for Motion-Compensated Video Compression", IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS FOR VIDEO TECHNOLOGY, INSTITUTE OF ELECTRICAL AND ELECTRONICS ENGINEERS, US, vol. 12, no. 11, 1 November 2002 (2002-11-01), XP011071889, ISSN: 1051-8215 paragraph [0001] - paragraph [0011]; figure 1 ----- -/--</p>	2-5

INTERNATIONAL SEARCH REPORT

International application No
PCT/IB2019/058975

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	<p>ALSHIN A ET AL: "Non TEB3: Bi-directional optical flow for inter-layer texture prediction", 12. JCT-VC MEETING; 103. MPEG MEETING; 14-1-2013 - 23-1-2013; GENEVA; (JOINT COLLABORATIVE TEAM ON VIDEO CODING OF ISO/IEC JTC1/SC29/WG11 AND ITU-T SG.16); URL: HTTP://WFTP3.ITU.INT/AV-ARCH/JCTVC-SITE/,, no. JCTVC-L0084, 7 January 2013 (2013-01-07), XP030113572, the whole document</p> <p align="center">-----</p>	10

INTERNATIONAL SEARCH REPORT

International application No.
PCT/IB2019/058975

Box No. II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet)

This international search report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons:

1. Claims Nos.:
because they relate to subject matter not required to be searched by this Authority, namely:

2. Claims Nos.:
because they relate to parts of the international application that do not comply with the prescribed requirements to such an extent that no meaningful international search can be carried out, specifically:

3. Claims Nos.:
because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a).

Box No. III Observations where unity of invention is lacking (Continuation of item 3 of first sheet)

This International Searching Authority found multiple inventions in this international application, as follows:

see additional sheet

1. As all required additional search fees were timely paid by the applicant, this international search report covers all searchable claims.

2. As all searchable claims could be searched without effort justifying an additional fees, this Authority did not invite payment of additional fees.

3. As only some of the required additional search fees were timely paid by the applicant, this international search report covers only those claims for which fees were paid, specifically claims Nos.:

4. No required additional search fees were timely paid by the applicant. Consequently, this international search report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.:

1-15(completely); 31, 32(partially)

Remark on Protest

- The additional search fees were accompanied by the applicant's protest and, where applicable, the payment of a protest fee.
- The additional search fees were accompanied by the applicant's protest but the applicable protest fee was not paid within the time limit specified in the invitation.
- No protest accompanied the payment of additional search fees.

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-15(completely); 31, 32(partially)

Claims 1-15 are directed towards a method of video decoding where a multi-hypothesis prediction mode and decoder side motion vector derivation are used.

2. claims: 16-19(completely); 31, 32(partially)

Claims 16-19 are concerned with decoder side motion vector derivation using asymmetric weighting factors.

3. claims: 20-24(completely); 31, 32(partially)

Claims 20-24 are concerned using an advanced motion vector prediction mode and optionally taking into account motion vector differences (claims 21-23).

4. claims: 25-30(completely); 31, 32(partially)

Claims 25-30 are concerned with decoder side motion vector derivation wherein a refinement of translational motion parameters occurs.
