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(12)

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(B1)

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(45)  
(11)  
(24)

2001 11 01  
10 - 0297735  
2001 05 24

(21) 10 - 1999 - 0028208  
(22) 1999 07 13

(65) 2001 - 0009695  
(43) 2001 02 05

(73)

3 416

(72)

1가27 - 64

105 1604

(74)

:

(54)

1 가 2 가 1  
 , , , , , 2  
 가 , 가  
 가 가 2 ,  
 ,  
 3

1

2

3

가

가

가

DDR(Dual Data Rate)

(Rambus)

800MHz

가

가

가

가 1998 11 9

가

( :98 - 47831, ; )

1

1 (101) 1 2 (111,121), 1 2 (Co  
re Interface) (113,123), 1 2 (131,141), (Interface Logic)(151),  
1 2 (161,162), (Delay Locked Loop) (163) (171)

1), (101) 1 (111) 2 (121) 1 (13  
(151), , 1 (161)/ (163)/ 2 (162),  
(171), 2 (141)

(121) 2 (101) , 1 (111) 1 (131) 2  
(141)

1 2 (131,141) (SCLK) (T  
 CLK) (163) , 1 2 (131,14  
 1) (SCLK) (TCLK) 가 가 가 .

1 1 (131) (171) (171)  
 1 (131) (171)  
 1 가 (101)

2 . 2  
 1 .

2 (201) 1 2 (211,221), 1 2 ( (213,223), (231), 1 2 (241,242), (243), (251)  
 (261)

(201) 1 (211) 2 (221) (231),  
 (241)/ 2 (243)/ 2 (242), (251),  
 (261) , 1 (201) , 1 (211)  
 2 (221) (261)  
 1 .

(TCLK) (243) (SCLK)  
 (243) 가 1 가 .  
 (261) (251) 1 (251) (2)

2 (201) , 1 2 (241,242) (261)  
 (L2,L4) 가 (L2,L4) (251)  
 (L2,L4) 가 (L2,L4) 가 (L2,L4) (251)  
 가 가 가 가 가 2

2 (201) (251) (Forward) (Reverse)  
 (201)

가

1 2 ; ;

1 2 ; ;

1 2 , 1  
2

2 1

가

가

가

1 가 2

3

3

1 , (301) , 1 (311) 1 (313)  
(321) , (331), (351), (341) (342)  
(343) 2 (323) 2 (340), (361), 2

3) (301) , 1 (313) (351) (32)

(331) (351) 2 (361)

(340) (351) (361) 2

2 (323)

51), 1 (311), 1 (313), (331), (3 (340), (361), 2 (323), 2

(321)가

2) (340) (343) (341) (34

(351) 2 (301) , 1 (351) (D1)가

3 (301) (D2) (351)

3

1 1 1 (311) 1 (311) (L8,L9)

(313) (331)

(361) 1 (311) (361)

(311) (L8,L9)

2 2 2 (321) 2 (L10,L11)

(323) (331)

(361) 2 (321) (361)

(321) (L10,L11)

1 (313) 2 (323) ( ) 1

(313) 1 (311) , (323) 2 , (321)

, , 가 2 , (323) 2 , (321)

(351) , , , ,

51) (340) (342) (L1) (3

(331)

(340) (343) (351)

(CLK) (TCLK) (361)

(SCLK)

(341)

(340) (341)

(351) (351) (L2, (341))  
 L4), (341) (361) (L2,L4) (351)  
 (341) (361)  
 2 (331) (342) (L1) 1  
 , (340), (361) ( )  
 ).

(361) (SCLK) (TCLK) (L2,L4),  
 (361) (SCLK) (TCLK) (361)  
 (SCLK) (341) (TCLK) (L8,L9) 1 (361) 1  
 (313) (TCLK) (L10,L11) 2 2  
 (323)

(361) (361)  
 (341) (L2,L4) (L8,L9)  
 1 (313) (L10,L11) 2 (323)  
 (361) 1 (313) (L8,L9)  
 2 (323) (L10,L11)  
 (L2,L4) (341)

(251)  
 (341) (340) (261) 가  
 (L2,L4) (361) 가 (L2,L4),  
 2 (L2,L4), 가  
 3 (301) 가 가  
 , 1 (351) (D1)가 (351)  
 2 (D2) , (351) , 3  
 (301)

가 , 가 가 , , 가

(57)

1.

1 2 ;

;

;

, ;

1 2

1 2 , ,

2 1

2.

1 , 2

3.

1 , 1 2

4.

1 ,

5.

1 ,

6.

1 , 1  
2

7.

1 , 1 2

8.

1 2 ;

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, ;

1 2

1 2 , ,

1 2 2 , 1

9.

8 , 1 2

10.

8 , 1 2

11.

8 ,

12.

8 ,

13.

8 , 1

14.

8 , 1 2

1





