

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
18 May 2006 (18.05.2006)

PCT

(10) International Publication Number
WO 2006/050632 A2

(51) International Patent Classification: Not classified

(21) International Application Number:
PCT/CH2005/000669

(22) International Filing Date:
11 November 2005 (11.11.2005)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:
60/627,784 12 November 2004 (12.11.2004) US

(71) Applicant (for all designated States except US): UNAXIS
BALZERS AG [LI/LI]; LI-9496 Balzers (LI).

(72) Inventor; and

(75) Inventor/Applicant (for US only): BELINGER, Andy
[CH/CH]; Gonzenweg 2, CH-9478 Azmoos (CH).

(74) Common Representative: UNAXIS BALZERS AG;
Patentabteilung SRLP, LI-9496 Balzers (LI).

(81) Designated States (unless otherwise indicated, for every
kind of national protection available): AE, AG, AL, AM,

AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN,
CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI,
GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE,
KG, KM, KN, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV,
LY, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NG, NI,
NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG,
SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US,
UZ, VC, VN, YU, ZA, ZM, ZW.

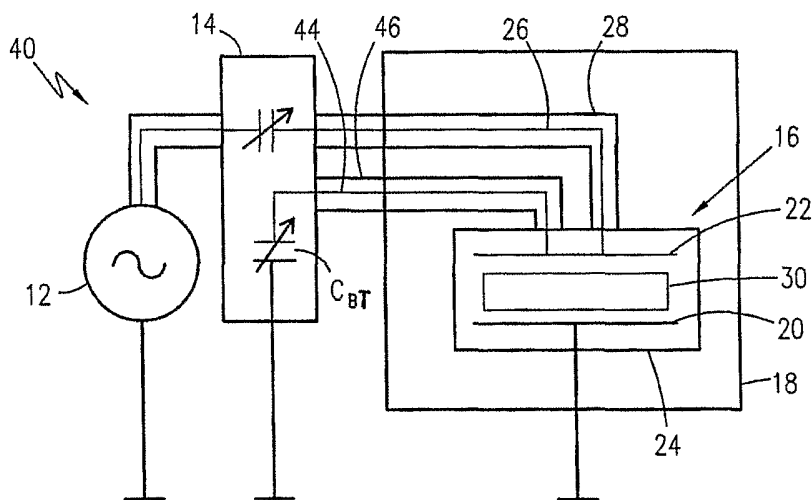
(84) Designated States (unless otherwise indicated, for every
kind of regional protection available): ARIPO (BW, GH,
GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM,
ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM),
European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI,
FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT,
RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA,
GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

— without international search report and to be republished
upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guid-
ance Notes on Codes and Abbreviations" appearing at the begin-
ning of each regular issue of the PCT Gazette.

(54) Title: CAPACITIVELY COUPLED RF-PLASMA REACTOR



(57) Abstract: An RF plasma reactor is provided for depositing semi-conductive layers on to very large glass areas. The RF plasma reactor includes a vacuum chamber, a reactor chamber, RF power supply, a matching network, first and second metallic plates located inside the vacuum chamber and a plasma-discharge region defined between the first and second metallic plates. The RF plasma reactor further includes a feed line and an impedance-transformation circuit both of which are electrically connected to the first metallic plate. The impedance-transformation circuit further includes a blocking-tuneable capacitor that transforms an impedance of the reactor.

WO 2006/050632 A2

CAPACITIVELY COUPLED RF-PLASMA REACTOR

This application claims the benefit of U.S. Provisional Patent Application No. 60/627,784 filed on November 12, 2004.

5 FIELD OF THE INVENTION

The present invention relates in general to RF capacitive coupled plasma reactors for processing a very large area display. More specifically, the present invention relates to improvements in the coupling efficiency of the RF power delivered to plasma typically at a frequency of 13.56MHz or less.

10 BACKGROUND OF THE INVENTION

The present invention is based on problems and requirements that have arisen in depositing semi-conductive layers on very large glass areas for the display and solar manufacturing industries. The resulting solution, however, can be applied to other applications. Thus, even though the present invention will be described relating to plasma reactors for Plasma Enhanced Chemical Vapor Deposition (PECVD) systems for very large area display processing, the present invention can also be applied to other applications relating to plasma reactors. Further, the development of PECVD for very large area display processing is disclosed in U.S. Pat. No. 6,281,469, the contents of which are herein incorporated by reference.

20 FIGURE 1 shows a conventional capacitively-coupled, RF-plasma reactor system 10 for a PECVD system. The reactor system 10 includes an RF power supply 12, a matching network 14, a reactor chamber 16, and a vacuum chamber 18. The reactor chamber 16 includes two metallic plates 20, 22 arranged in parallel enclosed in a metallic-reactor casing 24. The first metallic plate 22 is electrically connected to the RF power supply 12 via a feeding element 26 and the matching network 14 and the first metallic plate 22 is, thus, a live electrode. The second metallic plate 20 is connected to ground and is, thus, a ground electrode. During the deposition process a substrate is placed on the second metallic plate 20

for processing. The feeding element 26 is shielded with a grounding shield 28 and can be any type of electrical feeding element known in the art, such as an RF stripline, RF ribbon, or a triplate stripline. A plasma-discharge region 30 is defined in between the two metallic plates 20, 22. As shown in FIGURE 1, the RF power supply 12 and matching network 14 are located outside the vacuum chamber 18 and the reactor chamber 16 and feeding element 26 are located inside the vacuum chamber 18. Thus, during the deposition process, the RF power supply 12 and the matching network 14 are under atmospheric conditions and the reactor chamber 16 and RF feed line 28 are under vacuum conditions. A typical gas used for forming a plasma in the PECVD process is a silicon nitride SiN gas. Other gases commonly known in the art, however, may be used in this type of application such as organometallics, hydrides and halides.

FIGURE 2 shows a simplified equivalent circuit of the conventional PECVD system during the deposition of SiN and will be used to illustrate the disadvantage of the conventional RF-plasma reactor system 10. The dotted line boxes represent a portion of the conventional RF-plasma reactor system 10 as indicated by the reference numbers. During the deposition process, in the conventional RF-plasma reactor, several kilowatts of RF power at a radio frequency of 13.56 MHz must be delivered to the plasma in order to achieve the necessary deposition rate and maintain a reasonable throughput. A disadvantage to this process in large area parallel plate reactors is that a very large parasitic-reactor capacitance C_R , typically greater than 5000pF, forms between the live electrode 22 and the grounded reactor casing 24. Thus, the feeding element 26 must be capable of handling very large RF currents I_F , typically greater than 300A. The large RF currents require a very wide stripline design, which leads to a second parasitic-feed-line capacitance C_F between the live wire of the feeding element 26 and the grounding shield 28. The feed-line capacitance C_F is typically greater than 3000pF. The reactor C_R and feed-line C_F capacitance transform a plasma impedance Z_P to a feed-through impedance $Re(Z_F)$ having a very small value, typically less than 0.05 ohms. The feed-through impedance $Re(Z_F)$ is the impedance as seen at the entrance of the vacuum chamber 18 where the feeding element 26 enters the vacuum chamber 18. The feed-through impedance $Re(Z_F)$ in turn creates a larger RF current I_F , typically greater than 400A, which now must be accommodated by the matching network 14 and the feeding element 26. As a result, most of the power delivered by the RF power supply is dissipated in the lossy elements

R_M , R_F of the matching network 14 and the feeding element 26 respectively. Thus, the efficiency of the system is low, typically $\eta_s < 0.3$. Therefore, very large and expensive RF power supplies are required in order to achieve the necessary plasma power density and deposition rate. Further, as the size of the glass increases the efficiency of the plasma power coupling efficiency decreases to values less than 20% at an RF frequency of 13.56MHz.

There have been several solutions to address the above mentioned problem but each have additional disadvantages. For example, the parasitic capacitance C_R and C_F could be reduced by increasing the gap between the live parts, i.e. the live electrode 22 and feeding element 26, and the grounded parts, i.e. the reactor casing 24 and the ground shield 28. The disadvantage to this solution, however, is that the plasma between the gaps could ignite. Another solution is to water cool the reactor. This, however, is difficult in a vacuum system and water cooling does not significantly enhance the plasma coupling efficiency.

Another solution is adding an impedance-transformation circuit to the RF-plasma reactor system 10. Power losses through the lossy elements R_M , R_F in the matching network 14 and the feeding element 26 respectively can be reduced by decreasing the RF current I_F . Reducing the RF current I_F while maintaining the plasma power can be accomplished with an impedance-transformation circuit, which increases the feed-through impedance $\text{Re}(Z_F)$. In theory, connecting an inductor between the live electrode and ground will suffice as an impedance-transformation circuit. But an impedance-transformation circuit solely made of one inductor is impractical for several reasons. For example, the inductor needs to be a low-loss inductor, there is nothing to prevent the DC voltage from shorting to ground, and there is no tuning capability.

Thus, what is desired is a practical impedance-transformation circuit for an RF capacitive coupled plasma reactor for processing very large substrates that overcomes the above mentioned disadvantages.

BRIEF SUMMARY OF THE INVENTION

In accordance with one aspect of the present invention, a plasma reactor is provided comprising, a vacuum chamber, a first metallic plate and a second metallic plate located inside the vacuum chamber, an RF power supply, a matching network, a plasma-discharge

region containing plasma defined between the first and second metallic plates, a feeding element electrically connected to the first metallic plate, and an impedance-transformation circuit electrically connected to the first metallic plate.

In accordance with another aspect of the present invention, a plasma reactor is provided comprising, a vacuum chamber, an RF power supply, a matching network, a first metallic plate and a second metallic plate located inside the vacuum chamber, a plasma-discharge region for containing plasma defined between the first and second metallic plates, a feeding element electrically connected to the first metallic plate, an impedance-transformation circuit electrically connected to the first metallic plate, comprising an isolation capacitor, later referred as blocking capacitor.

In accordance with yet another aspect of the present invention a method of depositing semi-conductive layers in a vacuum is provided comprising the steps of, providing a plasma reactor further including an RF power supply, a vacuum chamber, a reactor chamber, having a reactor impedance, located inside the vacuum chamber, a first and second metallic plate located inside the vacuum chamber; a plasma-discharge region for containing plasma defined between the first and second metallic plates, a feeding element electrically connected to the first metallic plate, and an impedance-transformation circuit electrically connected to the first metallic plate, placing a substrate on the second metallic plate, delivering RF power to the plasma, transforming the reactor impedance to an intermediate impedance with the impedance-transformation circuit, and transforming the intermediate impedance to a feed-through impedance with the feeding element, whereby the feed-through impedance is increased.

Additional benefits and advantages of the present invention will become apparent to those skilled in the art to which it pertains upon a reading and understanding of the following detailed specification.

BRIEF DESCRIPTION OF THE DRAWINGS

The invention may take physical form in certain parts and arrangement of parts, a preferred embodiment of which will be described in detail in this specification and illustrated in the accompanying drawings that form a part of the specification.

FIGURE 1 is a schematic of a conventional capacitor-coupled, RF-plasma reactor system.

FIGURE 2 is an equivalent circuit of the reactor system of FIGURE 1.

FIGURE 3 is a schematic of a capacitor-coupled, RF-plasma reactor system with an impedance-transformation circuit in accordance with the present invention.

FIGURE 4 is an equivalent circuit of the reactor system of FIGURE 3.

FIGURE 5 is a graph showing the comparison of the impedance transformation between the conventional circuit of FIGURE 2 and circuit with the impedance-transformation circuit of FIGURE 4.

10 DETAILED DESCRIPTION OF THE INVENTION

Referring now to FIGURES 3 and 4, a more practical impedance-transformation circuit is shown in the schematic in FIGURE 3 and the electrical equivalent circuit in FIGURE 4. All components described in FIGURES 1 and 2 above that are the same in FIGURES 3 and 4 will not be repeated.

15 FIGURE 3 shows a capacitively-coupled, RF-plasma reactor system 40 (hereinafter "transformed RF-plasma reactor system") for a PECVD system having an impedance-transformation circuit 42, shown in FIGURE 4, in accordance with the present invention. The impedance-transformation circuit 42 includes a transformation circuit feeding element 44 with a grounding shield 46 and a tuneable-blocking capacitor C_{BT} .

20 Referring to FIGURE 4, the second feeding element 44 is represented in the equivalent circuit as having parasitic capacitance C_T , a lossy element R_T , and an low-loss inductor L_T . The transformation circuit feeding element 44 is located inside the vacuum chamber 18 and is connected to ground via the tuneable-blocking capacitor C_{BT} . Thus, with the addition of the impedance-transformation circuit 42, the transformed RF-plasma reactor system 40 now
25 includes the feeding element 26 and the transformation circuit feeding element 44 both of which are electrically connected to the first metallic plate 22. The tuneable-blocking capacitor C_{BT} is located outside the vacuum chamber 18 and can be integrated into the matching

network 14, resulting in amended matching network 14' (Fig. 3). The tuneable-blocking capacitor C_{BT} can increase the feed-through impedance $\text{Re}(Z_F')$ thereby decreasing the total RF current I_F' during the deposition process. Further, the tuneable-blocking capacitor C_{BT} can balance the current between the two feeding elements 26, 44 without venting the system.

5 The following equations illustrate the effect of the impedance-transformation circuit 42 relating to power loss. First, in the conventional RF plasma reactor system 10, the current I_F flowing out of the matching network 14 and through the feeding element 26 with no impedance-transformation circuit 42, as shown in FIGURES 1 and 2, is approximately:

$$(1) \quad I_F = \sqrt{P_F / \text{Re}(Z_F)}$$

10 for an RF signal having a wavelength greater than the diameter of the electrode or longer than the feeding line and where P_F is the power at the output of the matching network 14, which is dissipated through the lossy elements R_M and R_P in the feeding element 26 and the plasma. The power lost through the lossy elements R_M and R_F in the matching network 14 and feeding element 26 is defined by the following equation:

$$15 \quad (2) \quad P_{\text{Loss}1} = I_F^2 (R_M + R_F)$$

The efficiency of the matching network 14 is defined by:

$$(3) \quad \eta_{MB} = \text{Re}(Z_F) / [\text{Re}(Z_F) + R_M]$$

Further, the efficiency of an L-type or T-type matching network is given through:

$$(4) \quad \eta = Q_U / (Q_U + Q_L)$$

20 where Q_U is the unloaded quality factor of lumped elements and Q_L is the loaded quality factor of the lumped elements.

With the addition of the impedance-transformation circuit 42, as shown in FIGURES 3 and 4, the current I_F' flowing out of the matching network 14 and through the feeding element 26 is partially compensated by the current I_T flowing through the transformation circuit element 44 and tuneable-blocking capacitor C_{BT} . The optimal balance between I_F' and I_T can be adjusted through the tuneable-blocking capacitor C_{BT} and depends on the balance between

the power losses of the lossy elements ($R_F' + R_M$) of the feeding element 26 and matching network 14 and the lossy element R_T of the transformation circuit feeding element 44. The total losses in the matching network 14 and the feeding element 26 and transformation circuit feeding element 44 is defined as:

$$(5) \quad P_{Loss2} = I_F'^2(R_M + R_F') + I_T^2 R_T$$

for an RF signal having a wavelength greater than the diameter of the electrode or longer than the feeding line. If the tuneable-blocking capacitor C_{BT} is adjusted such that the currents I_F' , I_T are equal then $I_F' = I_T = I_F/2$. Further, if the currents I_F' , I_T are equal and if the lossy elements R_M , R_F' , R_T , and R_F are also equal, thus, $R_M = R_F' = R_T = R_F$. Thus, if I_F' , I_T are equal P_{Loss1} becomes:

$$(6) \quad P_{Loss1} = I_F'^2(R_M + R_F) = 2I_F'^2 R_F$$

and P_{Loss2} becomes:

$$(7) \quad P_{Loss2} = I_F'^2(R_M + R_F') + I_T^2 R_T = (3/4)I_F'^2 R_F$$

Thus, the loss ratio between the conventional RF-plasma reactor system 10 (no impedance-transformation circuit 42) and the transformed RF-plasma reactor system 40 with the impedance-transformation circuit 42 is:

$$(6) \quad P_{Loss1} / P_{Loss2} = (2I_F'^2 R_F) / (3/4)I_F'^2 R_F = 8/3.$$

As these equations illustrate, the power lost through the lossy elements R_M , R_F in the conventional RF-plasma reactor system 10 (no impedance-transformation circuit 42) are more than twice as much as the power lost through the lossy elements R_M , R_F' , R_T in the transformed RF-plasma reactor system 40 with the impedance-transformation circuit 42. Thus, because the power loss decreases in the transformed RF-plasma reactor system 40 with the impedance-transformation circuit 42, the power delivered to the plasma to maintain the same deposition rate as the conventional RF-plasma reactor system 10 (no impedance-transformation circuit 42) can be reduced. Thus, a smaller RF power supply can be used to achieve the same deposition rate. On the other hand, if the same size RF power supply is used the deposition rate will increase thereby increasing throughput.

FIGURE 5 shows a graph that illustrates how the impedance-transformation circuit 42 transforms the feed-through impedance $\text{Re}(Z_F')$ to thereby decrease the power lost through the lossy elements. In the conventional RF-plasma reactor system 10 the plasma impedance Z_P is transformed by a reactor capacitance C_R and a reactor inductance L_R to a reactor impedance Z_R located at the end of the feeding element 26. The feeding element 26 then transforms the reactor impedance Z_R to a feed-through impedance designated as Z_F . In the transformed RF-plasma reactor system 40 the plasma impedance Z_P is transformed to the reactor impedance Z_R just as in the conventional RF-plasma reactor system 10. The impedance-transformation circuit 42, however, transforms the reactor impedance Z_R to an intermediate impedance Z_R' . The feeding element 26 then transforms the intermediate impedance Z_R' to a feed-through impedance designated as Z_F' . As illustrated in FIGURE 5, the feed-through impedance Z_F' has both a higher resistive or real part and a higher inductive reactive or imaginary part than the feed-through impedance Z_F . In other words, $\text{Re}(Z_F') > \text{Re}(Z_F)$ and $\text{Im}(Z_F') > \text{Im}(Z_F)$. More specifically, the real part of the feed-through impedance $\text{Re}(Z_F')$ is approximately 0.1 to 0.2 ohms whereas the real part of the feed-through impedance $\text{Re}(Z_F)$ is approximately 0.0 to 0.1 ohms. The imaginary part of the feed-through impedance $\text{Im}(Z_F')$ is approximately 1 to 5 ohms whereas the imaginary part of the feed-through impedance $\text{Im}(Z_F)$ is approximately -3 to 1 ohms. Further, the impedance-transformation circuit 42 is not intended to compensate for reactive impedance or cancel out phase shifts. The more inductive the feed-through impedance Z_F , however, the less inductance is required in the matching network. As a result, the quality of the matching network can be enhanced even more because the RF power losses are mainly associated with lumped elements such as inductors made from copper.

While specific embodiments of the invention have been described and illustrated, it is to be understood that these embodiments are provided by way of example only and that the invention is not to be construed as being limited thereto but only by proper scope of the following claims.

WHAT IS CLAIMED IS:

1. An RF plasma reactor comprising:
a vacuum chamber (18);
5 an RF power supply (12);
a matching network (14);
a first metallic plate (22) and a second metallic plate (20) located inside the vacuum chamber;
a plasma-discharge region (30) defined between the first and second metallic plates;
10 a feeding element (26) electrically connected to the first metallic plate (22), the matching network (14) and the RF power supply (12); and,
an impedance-transformation circuit (42) electrically connected to the first metallic plate (22).
2. The plasma reactor of claim 1, wherein the impedance-transformation circuit (42)
15 comprises a transformation circuit feeding element (44) electrically connected to the first metallic plate (22) and a blocking-tuneable capacitor electrically connected to ground.
3. The plasma reactor of claims 1-2 further comprising a reactor chamber (24), having a reactor impedance, located inside the vacuum chamber (18),
20 wherein the impedance-transformation circuit (42) comprises a low-loss inductor and transforms the reactor impedance to an intermediate impedance,
wherein the feeding element (26) transforms the intermediate impedance to a feed-through impedance, and,
whereby the feed-through impedance is increased.

4. The plasma reactor according to claims 1-3, wherein the first metallic plate (22) is electrically connected to an RF power supply (12), the second metallic plate (20) is electrically connected to ground, and the impedance-transformation circuit (42) is electrically connected to ground.
5. The plasma reactor according to claims 1-4, wherein the matching network (14) is located outside the vacuum chamber (18) and electrically connected to the feeding element, wherein the blocking-tuneable capacitor is located inside the matching network (14').
6. The plasma reactor according to claims 1-4, wherein the feeding element (26) and the transformation circuit feeding element (44) are located inside the vacuum chamber.
7. The plasma reactor according to claims 1-6, wherein the plasma reactor is an RF-PECVD plasma reactor.
8. An RF plasma reactor comprising:
- a vacuum chamber (18);
 - an RF power supply (12);
 - a matching network (14);
 - a first metallic plate (22) and a second metallic plate (20) located inside the vacuum chamber;
 - a plasma-discharge region (30) for containing plasma defined between the first and second metallic plates;
 - a feeding element (26) electrically connected to the first metallic plate (22), the matching network (14) and the RF power supply (12) and
 - an impedance-transformation circuit (42) electrically connected to the first metallic plate (22), comprising a blocking capacitor.

9. The plasma reactor of claim 8 further comprising a reactor chamber (16), having a reactor impedance, located inside the vacuum chamber;
- wherein the impedance-transformation circuit (42) is a low-loss inductor and transforms the reactor impedance to an intermediate impedance,
- 5 wherein the feeding element (26) transforms the intermediate impedance to a feed-through impedance, and,
- whereby the feed-through impedance is increased.
10. The plasma reactor of claim 8-9, wherein the impedance-transformation circuit (42) comprises a transformation circuit feeding element (44) electrically connected to the first metallic plate (22) and a blocking capacitor electrically connected to ground.
- 10 10. The plasma reactor of claims 8-10, wherein the capacitor is a blocking-tuneable capacitor.
12. The plasma reactor according to claims 8-11, wherein the feeding element (26) is electrically connected to an RF power supply (12) and the transformation circuit feeding element (44) is electrically connected to ground and the second metallic plate (20) is electrically connected to ground.
- 15 13. The plasma reactor according to claims 8-12, wherein the matching network (14') is located outside the vacuum chamber and electrically connected to the feeding element (26), wherein the blocking-tuneable capacitor is located inside the matching network.
- 20 14. The plasma reactor of claim according to claims 8-12, wherein the feeding element (26) and the transformation circuit feeding element (44) are located inside the vacuum chamber (18).
- 25 15. A method of depositing semi-conductive layers in a vacuum comprising the steps of: providing a plasma reactor with an RF power supply (12), a vacuum chamber (18), a matching network (14), a reactor chamber (16), having a reactor impedance, located inside the vacuum chamber, a first (22) and second (20) metallic plate located inside the vacuum chamber; a plasma-discharge region (30) for containing plasma defined

between the first and second metallic plates, a feeding element (26) electrically connected to the first metallic plate (22), and an impedance-transformation circuit (42) electrically connected to the first metallic plate (22);

placing a substrate on the second metallic plate (20);

5 delivering RF power to the plasma;

transforming the reactor impedance to an intermediate impedance with the impedance-transformation circuit (42); and,

transforming the intermediate impedance to a feed-through impedance with the feeding element (26), whereby the feed-through impedance is increased.

10 16. The method of claim 15, further comprising the step of depositing a thin film on to the substrate.

17. The method of claims 15-16, wherein the impedance-transformation circuit (42) comprises a transformation circuit feeding element (44) electrically connected to the first metallic plate (22) and a blocking-tuneable capacitor electrically connected to
15 ground.

18. The method according to claims 15-17, wherein the blocking-tuneable capacitor is located inside the matching network (14').

19. The method according to claims 15-18, wherein the feeding element (26) and the transformation circuit feeding element (44) are located inside the vacuum chamber
20 (18).

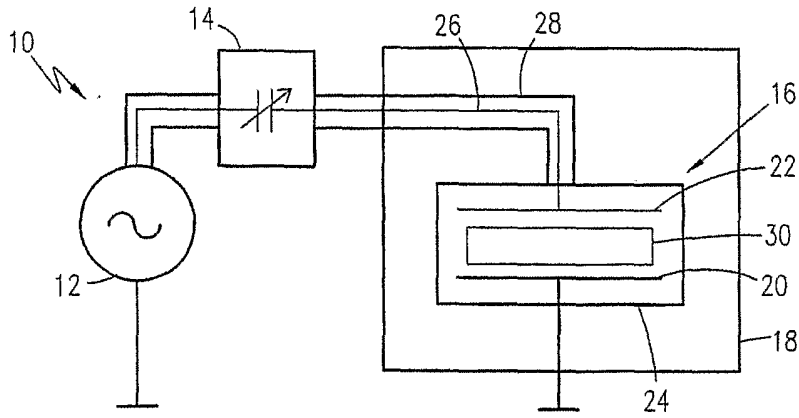


Fig. 1

Prior Art

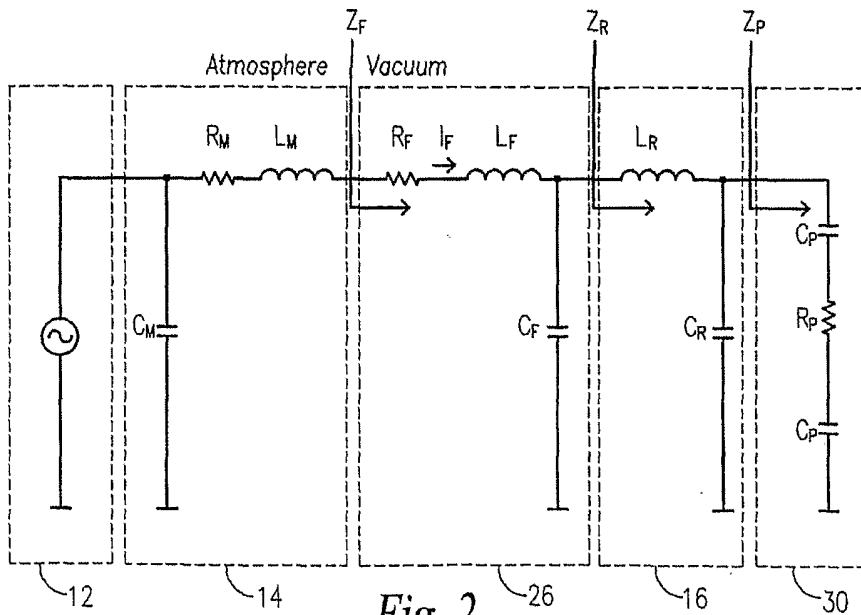


Fig. 2

Prior Art

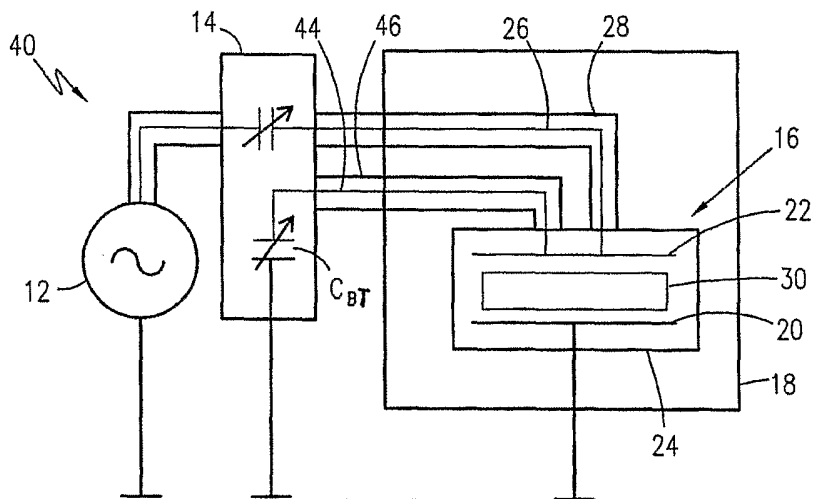


Fig. 3

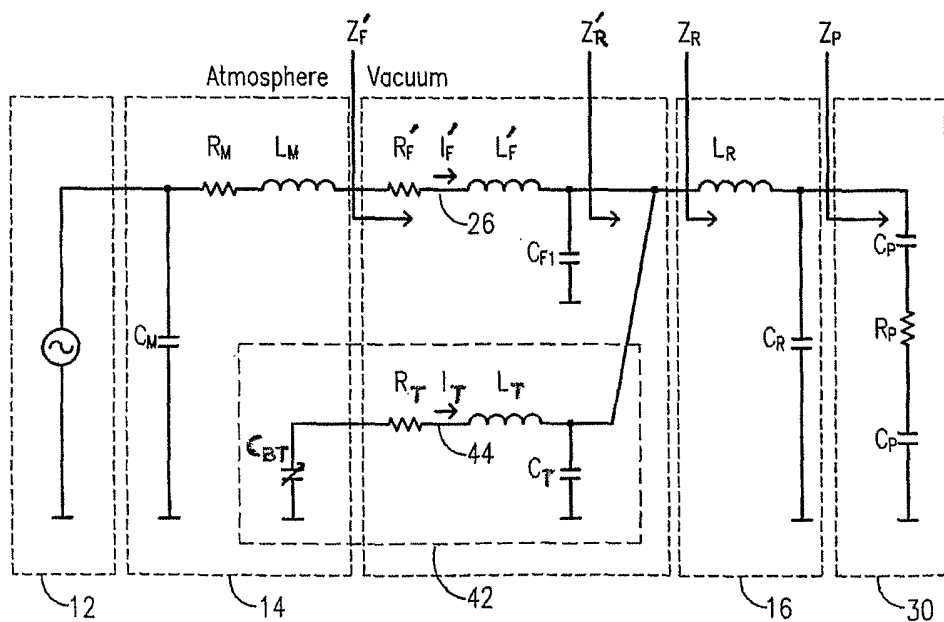


Fig. 4

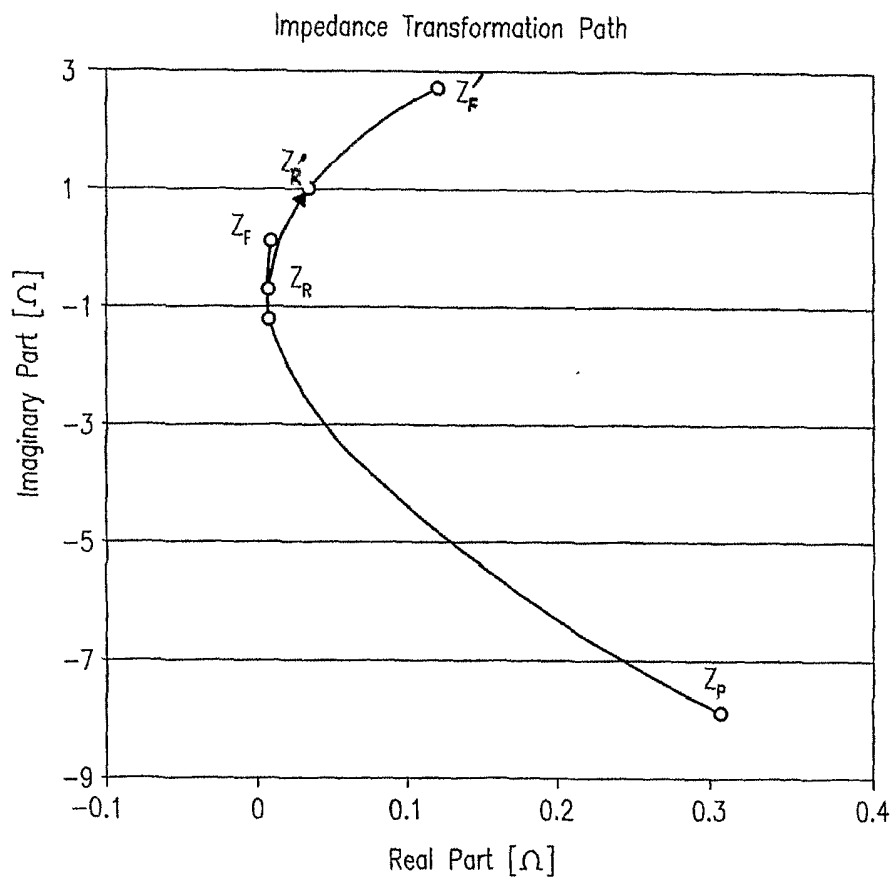


Fig. 5