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(54) Title: METHODS AND APPARATUS FOR RECOGNIZING A SUBROUTINE CALL

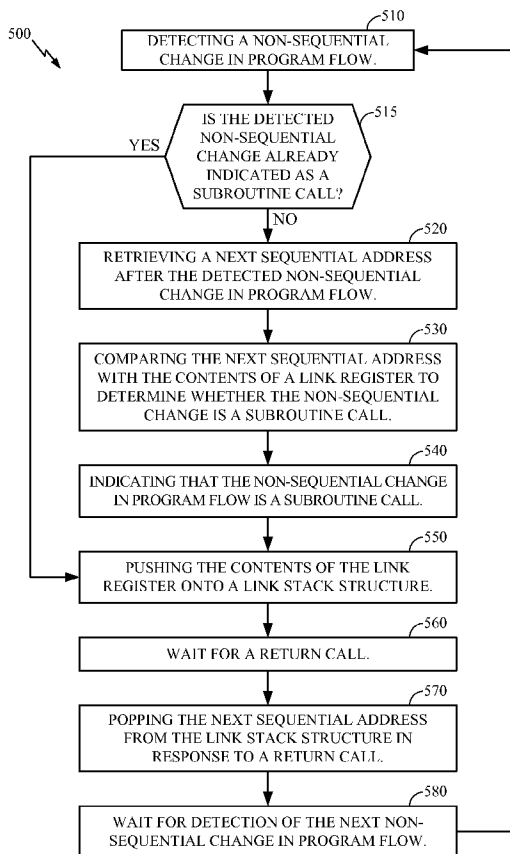


FIG. 5

(57) Abstract: An apparatus for recognizing a subroutine call is disclosed. The apparatus includes a circuit comprising a first input for receiving contents of a register, a second input for receiving a non-sequential change in program flow, and a third input for receiving the next sequential address after the non-sequential change in program flow. The circuit is configured to compare the next sequential address and the contents of the register to determine whether the non-sequential change in program flow is a subroutine call.

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C. DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 6 898 698 B1 (SUNAYAMA RYUICHI [JP] ET AL) 24 May 2005 (2005-05-24) column 2, line 36 - column 3, line 24.	1-20
A	US 6 910 124 B1 (SINHARROY BALARAM [US]) 21 June 2005 (2005-06-21) column 1, line 12 - column 2, line 42	1-20
A	US 6 157 999 A (ROSSBACH PAUL C [US] ET AL) 5 December 2000 (2000-12-05) column 1, line 12 - column 2, line 47	1-20
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