



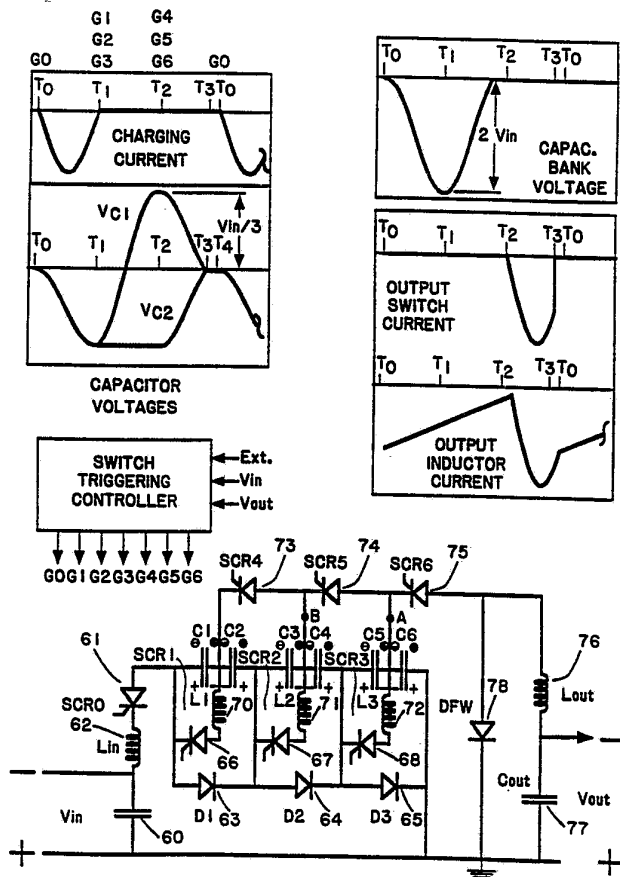
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(54) Title: COMPACT AND EFFICIENT TRANSFORMERLESS POWER CONVERSION SYSTEM

(57) Abstract

A transformerless power conversion system including a plurality of capacitors (C_1-C_6) connected in series; a charging circuit (60-62) connected to the plurality of capacitors, the charging circuit charging the plurality of capacitors from a voltage source (V_{in}) to a predetermined voltage; a circuit (63-75) for inverting the polarity of the charge stored in selected capacitors of the plurality of capacitors, the polarity inverting circuit including a plurality of inductor circuits (66-72) each of which can be switchably coupled to a corresponding different one of the selected capacitors to form a resonant circuit which aids in inverting the polarity of stored charge in that capacitor; and a discharging circuit (73-75) for extracting power from the plurality of capacitors at a transformed voltage.



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COMPACT AND EFFICIENT TRANSFORMERLESS
POWER CONVERSION SYSTEM

Background of the invention

5 Disclosed is a new Transformerless Power
Conversion System (TPCS) that allows a direct voltage
step-up or step-down of DC or AC power without the use of
magnetic core transformers. The operation is
accomplished with solid state switching devices,
10 capacitors, preferentially air core inductors and a
switch control system.

The conversion and step-up from AC to DC and the
inverse process can be implemented with high efficiency
and without the generation of harmonics. The transformer
15 elimination in conjunction with high inversion frequency
operation results in a low weight system without
transformer core losses and third harmonic generation.
For high ratio DC to DC transformation the TPCS
transports the input charge directly to the output
20 without requiring an AC link. The three distinct
operations of charging, transformation, and energy
discharge are typically in sequential order and allows a
complete isolation between the input and output power
grids. The TPCS allows complete power flow control and
25 with it allows an improvement of power grid stability. A
number of integrated TPCS modules can be configured to
perform in a single system as a voltage transformation
stage, a power converter or inverter, an output
regulator, an AC phase angle corrector as well as a fast
30 power interruption device.

The architecture is specifically suited for high
voltage DC power distribution systems with AC to DC
conversion and step-up at the power station for HVDC
transmission; DC to DC step down for DC distribution in
35 place of AC; and final DC to polyphase AC power

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conversion near the bulk power consumers.

Summary of the Invention

This invention relates to transformerless power conversion system that allows voltage transformation of
5 either DC or AC, conversion of AC to DC or inversion of DC to AC. The system allows voltage step-up or step-down of either AC or DC without the use of a transformer. In combination with the appropriate control logic and solid state switching devices, highly regulated output can be
10 obtained at Megawatt power level with an efficiency equivalent to that of unregulated standard AC power transformations.

DC-DC Transformation

In the prior art for DC to DC step-up the DC
15 needed to be first converted to high frequency AC. The voltage transformation was then performed with a transformer and the AC was then converted back to DC. The transformer not only adds to the system weight but is the major contributor or the reduction of efficiency. In
20 addition scaling to high power and high voltages of the order of 100 KV is impractical. With my invention the input charge is directly transported to the output with an efficient intermediate electrical voltage transformation process. No AC to AC transformation is
25 required. Transformers with its demanding volt-second scaling, magnetic core losses, cost, weight, and large volume requirement are completely eliminated.

AC-AC Transformation

The standard AC power distribution system is based
30 on the voltage transformation capability of iron core transformers operating typically at a low frequency of 50 or 60 Hz. These devices are large, costly, and have a

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constant voltage transfer ratio given by the turns ratio. My invention eliminates the transformer and all its constraints on the AC power distribution system by transporting the input power electrostatically to the
5 output with simultaneous regulation capability.

AC System Interconnection

To interconnect two independent and unsynchronized power systems it is presently necessary to convert the AC to DC and back to AC with the correct phase and
10 frequency. My invention allows the direct transfer of power from one polyphase system to a second polyphase system without the use of a DC link. I must emphasize that the DC to DC link interconnection benefits of power grid for stabilization is retained with my TPCS AC to AC
15 transformation interconnection.

The prior art of high power polyphase AC rectification results in substantial production of harmonics on to the AC input. The required harmonic filtering contributes appreciably to the power
20 distribution cost. With my invention the AC to DC transformation and step-up eliminates the transformer and the harmonics filtering. By operating at a high TPCS frequency only small filters are required at the AC input and DC output.

25 DC Power Distribution

To clear a high voltage DC line fault the present art requires the shut-down of the complete DC line on the converter AC input end. As a result DC lines are presently only economically viable for long distance
30 point to point power transport and for independent system interconnection. In contrast my invention allows the implementation of a complete DC distribution with direct DC to DC step-down with the elimination of all the

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problems associated with the AC distribution system. Faults on a DC branch line can be simply cleared by momentarily stopping the inversion process. Disconnect switches can then be opened or closed under no load
5 conditions.

Variable Output Frequency

The prior art of variable output frequency is complex and costly to implement. The cycloconverter is a high power control system that can directly convert AC
10 polyphase power from high frequency to lower frequency. If a 60 Hz source is used, the cycloconverter output frequency is typically variable from zero to 10 Hz with the higher end of the frequency spectrum limited to about 40% of the supply frequency. The resulting low-frequency
15 wave is composed of segments of the original source frequency. The output voltage can be made to approach a sine wave quite closely. However large reactive power and harmonics are generated. In addition the output voltage is limited.

20 In contrast my invention allows the efficient generation of output with variable frequency and variable voltage. The controlled output frequency may be higher or lower than the frequency of the power source. The same statement applies for the output voltage control.
25 Most importantly my TPCS variable frequency/voltage controller does not generate harmonics, input waveform distortion or reactive power. In addition the output frequency, the output phase, and output power can be controlled at an efficiency similar to that of a AC power
30 transformer. The phase rotation can be changed on a subcycle time period with controlled voltage output for optimum start-up, torque control or speed control of induction motors. Furthermore an efficient variable frequency converter can be also run with straight DC

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input with equal efficiency and performance.

Reactive Power Regulator and Harmonic Filters

TPCS modules can be configured as a fast response phase angle correction system and reactive power
5 regulator (VARG) as part of the voltage transformation system or as a separately stand alone unit. With the fast TPCS operating characteristics such a VARG can respond on the time scale of a fraction of an AC cycle. Not only can my invention regulate the reactive power
10 flow continuously from zero to its maximum rating but also can respond to both leading and lagging phase angle correction requirement.

The phase angle correction is performed without generating either harmonics or phase distortion. For
15 balanced polyphase grids my VAR configuration does not require the storage of energy for large duration of a cycle as is normally required for most operating systems. My VARG has similar 'black box' characteristics as a Synchronous Motor, running without a mechanical load.
20 The major difference is that TPCS VARG has no moving part, is much more economical to run, has much higher efficiency, and can response in a faster time-scale.

The TPCS VARG operational characteristics allow it to be programmed as a stand alone or as integrated Active
25 Harmonic Filters (AHF), again with no substantial energy storage requirement. This operation is possible since TPCS can extract the reactive and harmonic power (or charge) from the low voltage part of an AC cycle and can reinject it into another phase that has a high voltage.
30 The TPCS VARG/AHF can be programmed to neutralize several harmonic frequencies simultaneously. My invention allows the elimination of the large capacitor and reactor energy storage as is the state-of-the-art approach as described by Malesani at all (Active Power Filter with Hybrid

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Energy Storage, in IEEE Trans.on Power Electronics. Vol. 6 No. 3, July 1991).

With the present state-of-the-art of either AC to DC rectification or AC load control, passive or active
5 filtering are required to neutralize the generated harmonics. The TPCS on the other hand can perform this function as efficiently without producing harmonic distortions on the AC input grid.

If harmonics and reactive power is generated by
10 non-linear and reactive AC loads the TPCS "transformer" is powering, the TPCS can efficiently neutralize them as long as the harmonics are balanced on the AC output. The TPCS active harmonic filtering and reactive power generation is performed without large energy storage
15 requirement in the form of either capacitor banks or large inductors. Furthermore the architecture of the TPCS harmonic filtering system is such that it can respond to the requirement of harmonic filtering level change in a fraction of a cycle.

20 Brief Description of the Drawings

Other objects and features of my invention and of these embodiments thereof will be better understood from the following specification taken in conjunction with the annexed drawings and tables of which:

25 FIG. 1 is a representation of the basic TPCS architecture and some selected electrical waveforms for an AC to AC converter;

FIG. 2 is a typical three stage TPCS DC to DC voltage step-up transformation circuit;

30 FIG. 3 is a three stage TPCS DC to DC voltage step-down transformation circuit;

FIG. 4 is a four quadrant step-up and step-down TPCS configuration allowing power flow control in both the forward and backward direction;

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FIG. 5 is a single phase three stage TPCS AC to AC transformation circuit with bi-directional control capability and with phase inversion;

FIG. 6 is a typical single phase three stage AC to DC converter module with voltage step-down transformation;

FIG. 7 is a typical polyphase TPCS AC to DC conversion system with three phase input;

FIG. 8 is a typical circuit of a three stage DC to single phase AC inversion module with voltage step-down transformation;

FIG. 9 is a typical polyphase TPCS VAR generator and harmonics filtering system for a three phase grid;

FIG. 10 is the voltage, reactive current, and power for a three phase grid;

FIG. 11 is a simplified electrical configuration for one TPCS module in conjunction with an inductor for magnetic energy storage and retrieval;

Table 1 are the parameters of a specific control sequence for a three phase TPCS VAR generator;

Table II is the comparison of the result of a TPCS VAR generator sequence with that of theoretical requirements.

DESCRIPTION OF THE PREFERRED EMBODIMENT

25 Basic Architecture

The following is a description of preferred embodiment of my invention. As a first example the transformation of AC to AC with no change of phase and/or frequency is described as schematically shown in Figure 1. This basic architecture is equally applicable for AC voltage step-up as well as step-down. The details will become apparent with later descriptions.

To perform the described function with maximum power throughput, minimum system weight, minimum system

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volume and minimum electrical filtering requirement the system should be designed with an internal operating frequency significantly higher than the AC line frequency. An internal TPCS cycle typically consists of
5 a charging period, an inversion cycle and a discharge period.

On the input end of the TPCS typically a high frequency filter 11 is used, this is followed by a charging section 12. The charging section controls the
10 charging of a set of small high frequency low-loss capacitors and consists typically of switches and a series inductor. This will charge up the capacitor bank to twice of the instantaneous input voltage 10. The charging period is given by:

$$15 \quad T_c = \pi \sqrt{L_c \cdot C_{eff}}$$

where L_c is the charging inductor and C_{eff} is the effective value of the capacitor bank being charged. The capacitor bank is located in inversion section 14 and is the major component. With modern switching devices the
20 capacitor bank may be fully or only partly charged.

Typically as soon as the charging switches have electrically recovered the internal TPCS capacitor bank becomes electrically isolated from the input and the inversion cycle is initiated. During the inversion cycle
25 the electric polarity is inverted on some of the capacitors. The inversion cycle results in either a voltage step-up or step-down. The inversion cycle process, as described below, requires a similar time period as the charging cycle. The inversion process is
30 completed with the voltage of a plurality (typically half) of the capacitors in the capacitor bank reversed.

The third and final period is the discharge cycle where the energy in the TPCS capacitor bank is discharged through the output section 15. With the use of a second
35 high frequency filter 16 high frequency ripples from the

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TPCS operation are eliminated from the output. Again it is preferential to use an output inductor to control the output energy transfer, the discharge period, and to minimize the energy losses.

5 A critical component is the controller 18. It monitors both the input 10 and output 20 condition such as the input and output voltage, load level, and critical internal TPCS status condition and generates 22, 23, 24 the turn-on or gaiting of the switches.

10 Operating the TPCS at a high frequency a sine wave 20 output can be reconstructed with a minimum of input and output filtering. In addition the output voltage or power can be regulated in three primary ways.

1) By charging the capacitors to a predetermined
15 level and controlling the TPCS cycle frequency the output voltage can be increased or decreased.

2) The second mode of control is to use a constant TPCS cycle frequency and achieve the control by regulating the input charging level.

20 3) Finally the above two modes can be combined. The mode selection is subject to detailed component, system architecture and operational requirement trade-offs. It is understood that a plurality of modules, operating in parallel, may be integrated as one system.
25 Such modules would be operated out of phase with each other to minimize the input and output filter requirements. In addition should a module become defective such an architecture could provide build in system redundancy.

30 Figure 1 shows voltage and current waveforms at several points of the TPCS system. For clarity of illustration only 18 TPCS cycles per AC cycle are used. The sine input voltage V_{in} produces a charging current I_c having a sine envelope. The inversion section output
35 current I_{out} has a similar shape but is shifted by one

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cycle in time. The output section produces a current I_f . Its internal operation is selected to cause some filtering. The fully filtered output reflects the sine wave input modified in amplitude by the controlled step-up or step-down ratio. In addition a phase-shift with a time period corresponding to a TPCS cycle is induced between the input and output. To limit the shift to one degree the TPCS would have to be operated at a frequency of 21.6 KHz for a 60 Hz application. This is well within the state-of-the-art of solid state devices or plasma switches such as the Crossatron.

DC to DC Step-Up Transformation

Since DC to DC transformation has a low number of component counts and conceptually is one of the simplest TPCS applications I will use it to introduce the basic TPCS operation. Figure 2 shows a simplified three stage DC voltage step-up circuit that was tested. The input filter, output filter, and control circuit are not shown for clarity.

Before going into the circuit and operational description I will review the state-of-the-art of DC to DC conversion. The basic principle of converters have not changed since its invention by Francis C. Schwarz. His work is described in U.S. Patent No. 3,663,940 "Controllable Load Insensitive Power Converters"; Schwarz May 16, 1972. Most other inventions in this area are refinements and improvements on this classical work. The only feature our two inventions have in common is that both devices operate at high switching frequency and have a regulated output capability.

He first uses a switching circuit to produce AC from the DC input. The AC is then connected to the primary winding of a high frequency transformer. The transformer turns-ratio is then used to establish the

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basic voltage step-up or step-down ratio. Finally to produce DC the transformed AC is rectified and filtered to yield DC at the desired level.

The Schwarz converter has found many applications in the lower voltage range. The operation at high frequency reduced the transformer cross-section area, which scaled approximately inversely with the inversion frequency and directly with the output voltage. This transformer scaling, however, yields large transformer weight for application to higher voltage range with simultaneous reduction in the transformation efficiency.

With my invention the transformer and its disadvantages have been eliminated. Scaling to higher voltages can be performed with high efficiency. Using my invention, converters can be designed that operate at power and voltage levels in the range of commercial power grids.

Returning to Figure 2, the six capacitors 20, 21, 22, 23, 24, 25 are charged in parallel with the polarity as shown by triggering, at the time T_0 , SCR₀, 26 SCR₁, 27 SCR₂, 28 and SCR₃ 29 through the diodes D₁ 30 and D₂ 31. (The demonstration device used two SCRs in place of the diodes.) With the use of resonance charging inductor L₀ the capacitors are charged to twice the input voltage Vin 25 33.

In this voltage step-up mode the capacitor string is charged in an alternating sequence such that each adjacent capacitor has the opposite voltage polarity. The voltage of the capacitor string, when fully charged, is then zero. Two series connected capacitors can be considered a stage and forms a basic building block with a voltage transformation ratio of a factor of two. To increase the voltage transformation ratio additional stages can be added.

The charging period is defined by the L₀ 32 input

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charging inductor and the parallel capacitor value (six times C_1). The half sine wave charging period is given in terms of the component value shown by the following equation:

$$5 \quad T_c = \pi \sqrt{6 L_0 \cdot C_1}$$

and is the time between T_0 and T_1 . The peak current is given by;

$$I_{\max} = V_{in} \sqrt{6 C_1 / L_0}$$

10 With the SCRs 0,1,2 and 3 recovered the triggering of SCR₄, 35 SCR₅, 36 and SCR₆ 37 will start the inversion cycle shortly after the time T_1 .

The purpose of the step-up inversion cycle is to change the polarity of half of the capacitor strings such that all of the capacitors have the same polarity. In 15 the above schematic the polarity of capacitors C_2 , C_4 , and C_6 are being reversed in the selection of a positive output voltage. The reversal time period for capacitors C_2 , C_4 , and C_6 occurs between time markers T_1 and T_2 and is given by:

$$20 \quad T_i = \pi \sqrt{L_1 \cdot C_2}$$

The utilization of a unidirectional switch such as SCRs, GTOs or Crossatrons are important for the inversion process. Such switches or switching configuration prevents the current from ring back through the inductor 25 and allows the extraction of the inverted energy in an efficiently and controlled fashion.

The temporal voltage history for both the even and odd capacitors are shown in the lower left hand corner of the figure. The current shape is again a half of a sine 30 wave where the peak current is given by;

$$I_{imax} = 2 V_{in} \sqrt{C_2 / L_1}$$

The operating condition for the capacitors C_4 and C_6 are identical to that of C_2 . During the inversion cycle the voltage across the capacitor bank has the form given by:

$$35 \quad V_{bank} = 4 \cdot N \cdot V_{in} (1 - \cos(\pi \cdot t / T_i))$$

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where N is the number of stages (three for given example). It can be seen that for the circuit given a step-up, neglecting losses, of a factor of 12 is obtained. The positive voltage across C_1 to C_6 capacitor stack can then be switched to the output.

From an operational point of view the maximum stack voltage can be several times the filtered output voltage. One may wait until the inversion process is completed. However the given circuit allows us to start the discharge cycle, in the interest of shorting the complete TPCS cycle, as soon as the inversion cycle is 50% completed. An earlier start of the discharge will leave residual voltage in the capacitors and will make the charge cycle and control function more complex.

The inverted energy switch-out completes the TPCS cycle. This utilizes for most applications a switch SCR₇ 40 and an output inductor Lout. In addition an important Free Wheeling Diode (FWD) D₃ 42 is shown in the circuit. This configuration allows a complete and efficient energy output transfer by selecting a high output voltage and the appropriate output inductor and filter component.

With the output impedance of the filter lower than the TPCS output section a significant amount of the energy will be transferred into the output inductor. Part of the energy would ring back in to the TPCS capacitors and will recharge them with opposite polarity. However, the function of the Free Wheeling Diode will prevent the recharging and will turn on as the capacitor voltage becomes zero. At this point the next TPCS recharge cycle can start, while the remaining energy from the output inductor is injected into the output filter as shown by the asymmetric current trace. The FWD not only allows a complete energy output transfer but also performs part of the output filtering as can be seen from the output inductor current trace in the lower left hand

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corner.

Two traces of capacitor voltage waveforms are shown. The odd numbered capacitors are simply charged to positive polarity during the T_0 and T_1 period, remain at this level between T_1 and T_2 and are discharged during the third part of the T_2 and T_3 TPCS cycle. As shown in the lower left hand corner, the even capacitors such as C_2 are charged negatively during the T_0 and T_1 period, changed from negative to positive polarity during the T_0 and T_1 period, and are discharged together with the odd capacitors during the T_2 and T_3 period. The voltage across the capacitor string remains zero during the initial capacitor charging cycle and becomes maximum during the inversion cycle as shown in the upper left corner.

DC to DC Step-Down Transformation

The TPCS has the capability of step-up as well as voltage step-down. Figure 3 shows the electrical schematics and some selected voltage and current waveform for a three stage DC to DC transformation. In addition I have selected a negative to negative polarity transformation. Reversing all of the shown solid state devices would simply change the device to a positive to positive step-down voltage inverter.

The step-down transformation cycle utilizes the same three subcycles of charging, inversion, and discharging of the TPCS capacitors. The major differences is that the internal TPCS process is executed in the reversed order, as will become apparent from the following discussions. Instead of charging the capacitor in parallel and discharging them in series, as is the case for the step-up, in the step-down the capacitors are charged in series and discharged in parallel.

The charging is started with the triggering of the

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input switch SCR₀ 61. With the utilization of the input inductor Lin 62 the capacitor string, C₁, C₂, C₃, C₄, C₅, and C₆ will be charged to an electrical potential of about twice that of the input voltage Vin. Since the
5 charging occurred in series the capacitor polarity is plus-minus-plus-minus, as is indicated above the capacitors on the diagram. We recognize that this is the same electric state as for the step-up configuration shortly before the TPCS discharge.

10 The second subcycle is initiated by triggering the switches SCR₁, 66 SCR₂, 67 and SCR₃, 68 at the time T₁. With the proper selection of the inductors L₁, 70 L₂, 71 and L₃ 72 the inversion is completed at T₂. During this cycle the voltage polarity of the odd capacitors are
15 being changed in the same fashion and with a time period control as has been described for the step-up transformation above. The utilization of unidirectional switches such as SCRs current is prevented from flowing back. The voltage polarity of the capacitor after the
20 inversion cycle is shown below the capacitors. At this point the capacitor bank, measured from end to end, is zero as is illustrated with the waveform in the upper right hand corner. We recognize this to be the original state of the voltage step-up configuration.

25 With the inversion completed the energy switch-out cycle is initiated by triggering the switches SCR₄, 73 SCR₅, and 74 SCR₆, 75 at the time T₂. The diodes D₁, 63 D₂, 64 and D₃ 65 complete the discharge circuit. The discharge period is again selected with the value of the
30 output inductor Lout 76 defining the discharge period to the time interval of T₂ to T₃. The Free Wheeling diode D_{FW} 78 prevent the recharging of the capacitor bank, allows the complete discharge of the TPCS capacitors, and aids in the output filtering process.

35 As soon as the Free Wheeling diode comes in to

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play the output SCRs will start to recover and the recharge cycle can be started again. As can be seen this type of step-down inverter is through-put limited and allows both current and power limitation. As soon as a
5 load fault is detected the output can be simply turned off by stopping all triggering outputs from the gate controller. At this point the TPCS DC to DC step-down inverter will simply function as a DC shut-off switch.

The voltage waveforms are shown in the upper left
10 hand corner and with it are also given the trigger timing for all of the SCRs gates. The output switch and output inductor currents are also shown and assume that all of the output SCRs are triggered simultaneously. If the output load is low one may reduce the output filtering
15 requirement by staggering the output by discharging two capacitors at a time. By inserting two additional diodes in the circuit at positions labeled "A" and "B" the capacitors C_5 and C_6 can be discharged first by only triggering SCR_6 . The discharge of capacitors C_3 and C_4 are
20 next by triggering SCR_5 . The final discharge of capacitors C_1 and C_2 is initiated with the triggering of SCR_4 . The diodes at position "A" and "B" prevent the recharging of the discharged capacitors.

Four Quadrant Step-Up and Step-Down Operation

25 In the above description I have shown DC to DC converter for positive to positive step-up and negative to negative step-down. Inspection of both circuits shows that by changing a few components an output with an opposite of that of the input can be obtained. In fact a
30 single TPCS device can combine all possible or a selected combined input and output polarity options.

The schematics of Figure 4 shows a single stage TPCS that can step-up either a positive or negative polarity to either the same or opposite polarity. As

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shown power flowing from left to right will function as a step-up inverter. With the input coming from the right a step-down transformation takes place. The transformation in both directions have, with all components shown, full
5 four quadrant operational options. One may implement all or only part of the options. The table in Figure 4 lists the necessary components required for a specific option with "X" designating the step-up and "Y" the step-down components. The components required for a specific or
10 multiple choice operation must include all of the components under the designated columns. Of interest is that the configuration with polarity inversion allows power flow in both directions without the increase of a single additional component.

15 For example, the configuration for an AC to DC converter to either step down or step-up can be derived by the combination of two parallel TPCS inverters. One inverter would be used for the positive to positive conversion during the positive half of the input AC cycle
20 and a second inverter for the negative half of cycle with a negative to positive transformation. Since the high voltage switching section are the more costly components, it is prudent to maximize its utilization and combine in one device both functions. For the above example of an
25 AC to DC step-up transformer/rectifier configuration both functions may be combined into one unit. This requires the sum of the components indicated by "X" under column one (+in to +out) and three (-in to +out). For this example the high voltage SCR₅ and the associated Free
30 Wheeling diode (not shown) are shared for both halves of the cycle.

In summary the TPCS has a high degree of input and output polarity flexibility and all or a selected option may be combined into one TPCS module.

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AC to AC Transformation

This section gives an illustration of a single phase AC to AC transformation device. It is configured to use two parallel modules with each module operating in an inverting mode. The device, as shown in Figure 5, allows controlled power flow in both directions. From left to right the AC is inverted and transformed up. The power flow can also be reversed with the flow from right to left. This will result in a controlled voltage step-down.

Starting my description with the positive input cycle and with the power flow from the left. The turn-on of SCR₁, SCR₂, and SCR₃ will initiate the charging of the upper capacitor string C₁ to C₆ through the diodes D₁, D₂ and D₃. The lower module is not active during the positive input cycle and its components are allowed to cool-off. The positive charging period is controlled by the selection of the L₁ charging inductor value and will yield a capacitor voltage nearly twice that of the instantaneous input voltage, provided that the TPCS cycle is selected to be significantly shorter than an AC cycle. Under this condition the device operation is nearly identical to that of the above DC transformation configuration with the exception of a "slowly" varying input voltage.

The capacitor polarity is shown next to the capacitors after the charging cycle is complete. Triggering SCR₄, SCR₅, and SCR₆ will start the inversion cycle changing the voltage polarity of capacitors C₁, C₃, and C₅. This will produce a negative voltage of six times that of a single capacitor across the upper capacitor string. Triggering SCR₇ will start the negative voltage output discharge cycle through the inductor L₅. As soon as the voltage of the capacitor string changes from negative to positive polarity the SCR₈ is being

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triggered that will prevent part of the energy in the inductor L_5 to recharge the capacitor string to the opposite polarity. The SCR_8 acts as the previously described Free Wheeling diode, however a switched device
5 is required for AC output operation. With the L_5 inductor current transferred to SCR_8 , the SCR_7 will recover and the next charging cycle, with a slightly changed input voltage, can begin. The L_5 inductor in conjunction with the output filter will deliver a partly filtered AC
10 output.

The upper TPCS module operation, with the positive input cycle and producing a negative output cycle, is continued many times per cycle until the AC input voltage swings negative. At this point the upper module becomes
15 inactive and the lower TPCS module is activated.

The operation of the lower module with the negative input and the positive output cycle is identical with the exception of the inverted polarity. The TPCS charging is initiated with the triggering of SCR_{10} ,
20 SCR_{11} , and SCR_{12} charging the capacitors C_7 through C_{12} , with the polarity as shown on the schematics. For the inversion SCR_{13} , SCR_{14} , and SCR_{15} are used with the output switching function performed by SCR_{16} . The SCR_{17} is the positive output cycle Free Wheeling switch. Two output
25 inductors are shown, one for each polarity. One inductor could be eliminated.

The power throughput and output voltage regulation is performed with the controlled turn-on of the above switches (SCRs) using a controller and triggering module,
30 not shown to simplify the schematics. The controller would again sense the input and output condition for the control function and perform the regulation with the option as has been described for the above DC transformation. In addition internal TPCS diagnostic
35 points are also monitored for fault protection.

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Figure 5 shows for a complete AC cycle the TPCS capacitor input voltage and the inverted output voltage. For constant TPCS frequency the output follows directly the input waveform shape. Correction to input voltage distortion can be made by altering within an AC cycle the TPCS frequency.

The above single phase TPCS AC to AC transformation allows regulated AC voltage transformation. With the appropriated control it can, in addition, perform other functions such as turning the power on and off, perform as a circuit breaker, perform as controller for a device start , and harmonic filter or anti-harmonics generator.

The configuration of Figure 5 allows also regulated power flow from right to left. This direction of power flow will yield a voltage step-down with a step-down ratio defined by the number of stages installed. Additional regulation is obtained with the inverter frequency selection. With the exception of the Free Wheeling switches, all of the other components are used. The Free Wheeling switch is the only element that is only operational at the "transformer" output end. In the step-down mode, the upper TPCS module transforms the negative input voltage from the right to a positive and reduced output voltage to the left, while the lower module generates the negative output cycle on the left with a positive input from the right. In this mode of operation the capacitor strings are charged in series with a high voltage, with capacitor terminals connected with alternating polarity. The inversion cycle changes the polarity of half of the capacitor to the configuration as shown on the schematics. This allows the energy to be extracted to the left. The additional SCR₉ and SCR₁₉, not utilized in the step-up transformation, perform the Free Wheeling switch function

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for the positive and negative cycle respectively. The filtering sections on both the input ends, not shown, assure a smooth input and output power line.

The above single phase AC to AC TPCS transformation module can be used as a building block for a bi-directional polyphase regulated voltage transformation system with an effective 180 degree shift in phase angle.

AC to DC Transformation/Inversion

10 State-of-the-art generation of high voltage DC from an AC input requires an AC transformation followed by a rectification step. Power distribution systems typically use an AC transformer at grid frequency to perform the voltage step-up and a rectification step at
15 high voltage. This is an efficient process, however harmonic filters are required to eliminate harmonics generated by the rectification process. In addition power factor corrections are required. DC power levels in excess of one megavolt are generated this way for long
20 distance power transmission.

For lower weight and compacted application the Schwarz converter may be used by first rectifying the AC, converting the DC to high frequency AC, performing the AC transformation with a significantly reduced transformer,
25 and finally rectifying the high frequency stepped-up AC power to produce DC. The transformation process at high frequency significantly reduces the transformer weight, however, at the expense of reduced efficiency. In addition this process further penalizes the efficiency
30 further as the output voltage is increased much over a 50 KV output.

In contrast my TPCS approach uses the AC input directly and requires no transformer at all. In addition no harmonics are generated with the TPCS conversion

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process and the AC input power factor is for all practical purposes unity. By eliminating the transformer and harmonic filters the TPCS AC to DC conversion not only can compete with the commercial efficiency at the same voltage and power level, but also is more economic and much smaller in size.

The DC output can be obtained using either two TPCS modules as described above. One module would transform the positive and the second one the negative AC input cycle to high voltage DC. A second option is shown in Figure 6 that combines both functions in one module. It performs the positive to positive and negative to positive functions and has the sum of the components listed in the first and third columns of the table in Figure 4. Combining the low voltage input functions and sharing the high voltage output functions makes in many applications technical and economical sense since it shares the two high voltage output switching section components of SCR₁₉ and Free Wheeling Diode D_{fw}.

For the positive AC cycle SCR₁, SCR₂, SCR₃, SCR₄, SCR₅, and SCR₆ are turned on to initiate the TPCS charging cycle. This charges the capacitors with the polarity as shown below the capacitors. For the positive input cycle triggering SCR₇, SCR₈, and SCR₉ starts the inversion. This will result in a capacitor polarity as is shown on top of each capacitor. The inversion will yield an output voltage of 2nVin, where the "2" comes from the resonant charging process and "n" are the number of capacitors in the capacitor string. Triggering the SCR₁₉ will initiate the output cycle through the output inductor L₅. The capacitors C_{in} and C_{out} are part of the input and output filtering section respectively. The Free Wheeling Diode D_{fw}, prevents the capacitor bank voltage from swinging negative and allows efficient output energy transfer. The positive TPCS operation is

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repeated as long as the input cycle is positive.

As the AC input voltage swings negative the controller and triggering module (not shown) switches over to the SCR₁₀, SCR₁₁, SCR₁₂, SCR₁₃, SCR₁₄, and SCR₁₅ for the charging cycle leaving the capacitors with the polarity as shown in the circles. To produce a positive output voltage the SCR₁₆, SCR₁₇, and SCR₁₈ are used to invert the other set of capacitors. The discharge cycle is identical to that of the positive input cycle operation and again uses SCR₁₉ and Free Wheeling diode D_{fw}.

The input and output current is as shown (not to scale) in Figure 6 and consists for an AC input cycle of many TPCS cycles. Using a high frequency output filter will produce a stepped up voltage with a shape that is nearly identical to that of a full wave rectifier. To reduce the input line ripple for a TPCS single phase AC to DC transformer/rectifier standard filtering has to be added to the output. As with a regular filtered full wave rectifying configuration phase distortion takes place with harmonics generation, since during the low voltage part of the input the rectified voltage is lower than the filtered output voltage and no energy can be transferred. This applies also for the TPCS rectification process but to a lesser degree, since the TPCS transformation ratio can be selected to be arbitrarily high without major impact on the system efficiency. Such harmonics can be completely eliminated with the single as well as with a multiple phase TPCS AC to DC inverter.

One approach to eliminate input harmonics is to add to the Figure 6 circuit inductor L₆, SCR₂₁ and SCR₂₁. The function of these components is to invert the capacitor bank if the bank voltage is too low. By inverting the complete capacitor bank the capacitors will

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start out with a nonzero voltage of opposite polarity than the input voltage. This condition will allow the capacitor bank to be charged repeatedly until the voltage is sufficiently high to allow a full discharge into the
5 DC output grid. The charging process is performed in such a rate to extract the correct energy and charge from the AC input cycle to prevent harmonics generation. The process for the positive input cycle is as follows. If after the charging the capacitor voltage is too low, the
10 following switches are being triggered; SCR₂₀, SCR₁₁, SCR₁₂, SCR₁₃, SCR₁₄, and SCR₁₅. The current will be flowing through the inductor L₆, until the capacitor changed their respective polarity. The inductor value can be selected to optimize this operation. After the bank inversion is
15 complete a new positive input charging cycle is initiated as described above. Since an initial voltage of negative polarity is on the capacitors, the bank will be charged to a higher voltage. This process can be repeated several times until the voltage is sufficiently high.
20 For the negative cycle the same procedure can be used by selecting the correct switches. The Table in the figure lists all of the switches used for the various operations. The described bank inversion process falls under "INPUT INVERSION" column.

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To calculate quantitatively the multiple charging process let us assume that the input voltage is V_i and the initial capacitor voltage is given by V_{ci} . The final capacitor voltage will be;

$$V_{cf} = 2 V_i - V_{ci}$$

this equation applies as long as the initial capacitor voltage is less than the input voltage. This yields a charging voltage of $V_{c1} = 2 V_i$ for the first charge cycle, since the initial capacitor voltage is zero. After the first charge cycle the energy in the capacitors is;

$$E_1 = 12 C V_i^2$$

where C is the capacitance of an individual capacitor. By inverting all of the capacitors the initial V_{ci} for the second charge cycle is $-2 V_i$ and will yield a charge voltage of $V_{c2} = 4 V_i$ and energy of $E_2 = 4 E_1$. This can be repeated to yield after "n" cycles a capacitor voltage of $V_{cn} = 2 n V_i$ and energy of $E_n = n^2 E_1$. The use of this cycle inversion in conjunction with the appropriated intervals between charging cycle allows the loading of the AC input such that no harmonics are generated and the effective load for the AC grid appears to be resistive.

A modified charge approach can be used with a polyphase AC grid that does not require the full bank inversion between charge cycle. By charging sequentially from different phases with opposite polarity the bank inversion and some switches can be eliminated with an increase of the logic complexity. This will be further covered in a later section.

Polyphase AC to DC Transformation/Inversion

The single phase AC to DC conversion configuration described in the previous section can be used as a building block. However most high power converters use a polyphase input such as the output of a generating plant. The single phase TPCS step-up transformer/rectifier

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module input section described, can be modified to allow inputs from more than one phase. This modification in conduction with the appropriate switch control and trigger logic can obtain harmonic free input and output
5 power without the use of harmonic filters.

A balanced polyphase generating system delivers constant power if operated into a linear resistive load. To simulate such a resistive load the TPCS must extract from a single phase a power level that is proportional to
10 the square of the instantaneous line voltage. For the simple charge mode described for the AC to DC transformation process the inverted TPCS can not inject the energy into the load during the low voltage part of the AC cycle. A repeated charge and inversion process
15 with one phase may be used to solve this problem. This section will describe a second method that utilizes the voltage input from a plurality of phase. This architecture and mode of operation can be also adopted for reactive power generators, harmonic filters and can
20 correct the power in an unbalanced line.

Figure 7 shows the schematic diagram of a three phase AC to DC converter. The converter module contains the components as shown in Figure 6 with two additional input sections connected to the electrical point "A" as
25 labeled in Figure 6. With the additional two input sections the device can accept the input from all three grid phases and for both the positive and negative AC cycle. The output will be a constant and stepped-up voltage DC with constant power. The other important
30 feature is that controlled energy can be extracted from all three phases at any voltage specifically at the low voltage part of the cycle thereby eliminating the formation of harmonics on the AC input lines and ripple on DC line.

35 The charging cycle of phase 1 is controlled by

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SCR₁, and SCR₁₀ and is assumed to have the voltage waveform of $V_1(t) = A \cos(\omega t)$. Phases 2 and 3 are controlled by SCR₂₀, SCR₂₁, and SCR₂₂, SCR₂₃ respectively. The waveforms are plotted in Figure 7. Specifying a regulated and filtered output of DC of $V_{DC} = 3 \cdot A$ the inverted output voltage must be a minimum of $6 \cdot A$ and therefore, requires that the capacitors are charged to a voltage of a minimum of A . Using the resonant charging mode it follows that the minimum line voltage must have an absolute value of $A/2$. The harmonics are generated on phase 3 by not extracting energy for an electrical phase angle between 0-60 and 180-240 degrees. This problem can be simply rectified with a consecutive bipolar charging sequence as will be described for the first 30 degrees with the aid of the voltage waveforms in figure 7.

During the first 30 degree electrical phase angle period phase 2 can charge the capacitors to the required voltage A and a single charge operation is used. The problem is that phase 3 has an absolute line voltage of less than $A/2$. By triggering SCR₂₃ the capacitors will be charged negative to a value $2 V_3$, and will extract an energy of $E_3 = 2 V_3^2$ (assuming unit capacitance for simplicity). With the capacitor bank being negative we next trigger the SCR₁ for a positive charge cycle of phase 1. This will charge the capacitor bank to $V_{31} = 2 (V_1 - V_3)$. Since V_3 is negative, the charge voltage is larger than "A". At this point the TPCS performs an inversion followed by an extraction cycle.

The total energy extracted transferred is $E_t = 2 (V_1 - V_3)^2$. The energy extracted from phase 1 is $E_{31,1} = 2 V_1 \cdot (V_1 - 2 V_3)$. The two step charging process increases the energy extraction.

The two step charging process can be reversed by first charging with phase 1 followed by phase 3. It can be shown that the total energy transfer is the same,

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however the energy extracted out of phase 1 is $E_{13,1} = 2 V_1^2$ and from phase 3 $E_{13,3} = 2 V_3 (V_3 - 2 V_1)$.

The graph in the upper right hand corner of Figure 7 shows the energy transfer over the first 30 degree range. The upper graph shows the total energy transfer from both phases. The two remaining curves show the energy extracted from phase 3 using for the lower curve the charging sequence of phase 3 followed by phase 1, while the center trace is for charging sequence of phase 1 first followed by phase 3. The energy extracted from phase 1 is the difference between the total energy and the energy given with the curves shown for phase 3. The energy extraction result from a given phase is drastically different depending on the order of charging and can be fully utilized in the optimum control of the TPCS transformation process.

At the 30 degree point phase 3 becomes positive and the two step charging scheme is being used between phase 3 and the negative phase 2. This process continues until phase 3 reaches a value of $A/2$ at the electrical 60 degree point. At this point the rolls between phase 3 and 1 are exchanged and the process continues.

The controller monitors the input and output voltage and controls the switch triggering sequence and the time periods between charging. Several TPCS modules may be used to increase the power throughput and reduce the high frequency filtering requirements. Some of the inductors, input and output filters may be shared.

In summary the TPCS polyphase AC to DC converter can be configured to allow constant power throughput thus eliminating all harmonic filtering requirements on the AC as well as major ripple filtering on the DC side. In addition a unity power factor is maintained, since the current and voltage is maintained to be in phase.

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DC to AC Conversion

The reconversion from high voltage DC to a polyphase AC can be performed at the end of a long distance DC transmission line or at the end of a DC power distribution system . The DC power can be converted to a balanced three (or multiple) phase AC output with constant loading of the DC input. The output can be precisely matched to the phase, frequency, and voltage of the existing grid or to a specific output requirement.

10 In the TPCS conversion process the DC line will be loaded again uniformly and the converter will provide a harmonic free voltage or power regulated AC output. The DC power is stepped down to the required AC voltage level without the requirement of costly transformers, harmonic filters

15 on the AC end and input filters on the DC side.

The TPCS inverter is a power converter in which the normal direction of power flow is from a high voltage DC source to an AC load. Its typical operation control modes can be classified as a Variable output Frequency Modulated (VFM) inverter. The TPCS and its control

20 system can reconstruct an unlimited family of output waveforms. One of the waveforms can be the desired AC output with the desired voltage, frequency and phase.

For an AC waveform reconstruction the TPCS system

25 can be fully charged from the DC line but with continuously varying charging intervals during the AC output cycle. The interval spacing will be inversely proportional to the square of the instantaneous output voltage requirement. As the AC power requirement changes

30 the charging intervals change inversely with the power demand. The TPCS inverter could also be operated as a constant frequency but with Variable Amplitude (VAC). This requires a variable output amplitude control with the energy output per pulse being proportional to the

35 square of the instantaneous output voltage requirements.

- 30 -

Each approach has its advantages and disadvantages. The mode of operation, described below, uses a combined mode and is of Variable Frequency with Variable Amplitude Control (VFM/VAC).

5 Figure 8 shows a module to reconstruct one phase of a polyphase AC line from a DC input. The control circuit has been neglected for clarity. A high DC filter allows the series charging of the capacitor bank consisting of a three stage TPCS module. The capacitor
10 bank is resonantly charging through the inductor L1 by triggering SCR₁. For the reconstruction of a positive output cycle the inversion switches SCR₂, SCR₃ and SCR₄ are used. The capacitors voltage polarity before and after the positive cycle inversion are shown by the upper
15 and lower set of signs respectively. The middle set of signs next to the capacitors shows the inverted capacitors voltage polarity for the negative AC cycle. The positive output cycle discharge is initiated by triggering SCR₈, SCR₉, SCR₁₀ and SCR₁₁, SCR₁₂, SCR₁₃. As
20 before the SCR₂₀ is used as a Free Wheeling Switch to prevent the recharging of the capacitor bank. This allows the maximum power throughput at the top of the cycle by discharging all of the capacitors simultaneously. During the lower part of the AC cycle
25 the capacitors can be discharged at a slower rate by using a sequential discharge. The electrical circuit shown allows such a mode by first triggering SCR₁₀ and SCR₁₃ to discharge C₁ and C₂. Triggering SCR₉ and SCR₁₂ next, while SCR₁₀ and SCR₁₃ are still conducting, will
30 allow the discharge of capacitors C₃ and C₄. The diode D3 is added to prevent the capacitors C₁ and C₂ to be recharged. Finally the set of C₅ and C₆ C₁ are discharged, with D₁ being used as a blocking diode to prevent the recharging of C₃ and C₄. This mode of
35 charging and discharging allows a smoother output for the

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lower voltage part of the AC cycle and with it a reduced high frequency filtering requirement. The disadvantage is the additional cost of the diodes and its associated voltage drop and power losses.

5 The negative cycle is reconstructed in a similar manner. The TPCS charging cycle is identical. The difference starts with the negative inversion cycle by triggering SCR₅, SCR₆ and SCR₇. For the negative output cycle the switches SCR₁₄, SCR₁₅, SCR₁₆, SCR₁₇, SCR₁₈, and
10 DCR₁₉ are used. The diodes D₂ and D₄ allow a slower output discharge. In the upper part of Figure 8 the charging and discharging current sequence are shown schematically. Only a limited number of cycles are shown for clarity.

15 To reconstruct a full polyphase output several of such modules are required with each one being appropriately controlled. If balanced harmonics are generated by the load the TPCS master controller can control the output to neutralize these harmonics. The
20 output becomes simply the superposition of several frequencies with each frequency being controlled in amplitude and phase.

Variable Frequency Converter

We have seen that the TPCS can extract constant
25 power from a polyphase AC grid or a DC line. This power can be used to reconstruct an AC polyphase AC grid with regulated output as has been covered above. The reconstruction of the AC grid is not restricted to the input frequency. This allows the use of the TPCS AC to
30 AC transformation system as a direct link between two independent grids, operating with a different frequency or a different phase.

Furthermore the AC to AC TPCS system can be
controlled to produce a variable frequency output for the
35 regulation of high power linear induction motors. Since

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each output phases of the TPCS can be controlled separately the functions of two phases can be switched on a subcycle time basis producing a change in the phase sequence for an instantaneous change in the rotation of
5 induction motors.

The use of a TPCS AC to AC step up converter could be used between a generator and the power grid. This would significantly improve the power grid electromechanical stability since the rotation of the
10 generators are not required to be in perfect synchronism with the grid. In fact most gear boxes of generators can be eliminated, since the TPCS AC to AC converter can as efficiently convert and transform the balanced grid generator output of any frequency to match the frequency
15 of the power grid.

In summary the TPCS AC to AC converter can be controlled to produce a variable frequency with simultaneous amplitude control with an efficiency similar to that of a standard AC transformer. No upper frequency
20 output restriction does exist such as for the cyclone converter. In addition no harmonic or phase distortions are introduced on to the input grid.

Regulated VAR Generator

TPCS modules can be configured as a fast response
25 phase angle correction system for both leading and lagging VAR. As described in the AC to DC power transformation a TPCS module can extract power or charges from a low voltage line and injects it into another line having either polarity and also higher instantaneous
30 voltage. With the fast TPCS operating characteristics such a VAR generator can respond on the time scale of a fraction of an AC cycle.

If properly controlled the phase angle correction is performed without generating either harmonics or phase

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distortion. For balance polyphase grids my VAR generator configuration does not require the storage of energy for part of a cycle as is normally required for most operating systems. However; for single phase VAR
5 correction existing capacitor banks may be used in conjunction with TPCS modules to control the desired flow of reactive energy. By either extracting from the line or injecting back into the line the correct charge over the complete cycle, harmonics generation can be
10 completely eliminated. This is performed, as for all of the described TPCS operation, without forced commutation of the switching elements. The natural or self commutating configuration of the TPCS modules add significantly to the overall system reliability, reduced
15 complexity, and equipment cost.

For a polyphase grid the TPCS VAR control can be performed with independent single phase VAR controllers. Reduction of the energy storage requirement can be accomplished if the energy storage capacitor banks are
20 shared between the TPCS single phase VAR generators. Since for a balanced grid the sum of the inflow and outflow of reactive energy and reactive current into the energy storage bank is zero at all instances of the AC cycle it becomes obvious that such a shared bank can be
25 significantly reduced. In fact the capacitor bank and its charging and discharging steps can be completely eliminated with the reactive current extracted from one line and with the direct reinjected into another line. The simplified schematic in Figure 9 illustrates such as
30 system.

The illustration shows a controlled TPCS VAR generator for a three phase grid with a neutral. This architecture is equally applicable for grids having larger numbers of phases. Furthermore, it is not
35 necessary to have a neutral since TPCS VAR generator

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modules can be directly connected between phases.

Figure 9 shows a TPCS module with both the low voltage and high voltage sections connected to all three phases of the power grid. This system architecture is a generic module and can operate as a regulated VAR generator and harmonics filter. Both ends can be used for charge extraction or injection. For VAR generation mainly the low voltage end of the TPCS will be used. For harmonics filtering functions the front end is mainly used for energy extraction from the grid and the high voltage end for the energy reinjection.

The VAR generator controller can either regulate the reactive current or reactive power flow. Controlling either one will control automatically the other parameter. This will become apparent from the example and can be readily proven mathematically.

Assuming that a VAR generator is connected as shown in Figure 9 to a three phase grid with a reactive load. The voltage and reactive currents are given by the following equations.

$$\begin{aligned} V_1 &= V_m \cos(\omega t) & I_{r1} &= I_{ro} \cos(\omega t - \pi/2) \\ V_2 &= V_m \cos(\omega t + 2\pi/3) & I_{r2} &= I_{ro} \cos(\omega t + \pi/6) \\ V_3 &= V_m \cos(\omega t + 4\pi/3) & I_{r3} &= I_{ro} \cos(\omega t + 5\pi/6) \end{aligned}$$

Figure 10 shows the power voltage, reactive current, and reactive power flow of all three phases. The operation will be demonstrated for a phase angle of $\omega t = \pi/12$ or 15 degrees.

From the traces it can be seen that energy needs to be extracted from the grid when the shown reactive power is negative. We start out by triggering SCR4 to charge the capacitor from phase 2 through the inductor L2. The charging period is again controlled by the value of the inductor. Table I shows the result of the

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operation. Column 1 designates the operation and lists the phase involved, column 2 lists the control switches, column 3 is the initial TPCS capacitor bank voltage normalized to the maximum phase voltage amplitude V_m , column 4 gives the final capacitor voltage, column 5 lists the normalized charged transfer to the phase, and finally column 6 is the energy transferred to the phase normalized to the energy for the bank charged to a voltage V_m .

10 From the first operation we see that energy was extracted from phase 2 and that the bank was charged to twice the voltage of the phase. The positive number of the charge transfer column indicates that a positive current is being induced. The final voltage of the first
15 operation becomes the initial capacitor voltage of the second operation. The second operation is initiated by triggering SCR5 in order to inject a negative charge and energy into phase 3. The final capacitor voltage and charge transfer is given by;

$$20 \quad V_f = 2 V_{ph} - V_{in} \qquad \Delta Q = C (V_{in} - V_f)$$

The energy injected into phase 3 is:

$$\Delta E = C (V_{in}^2 - V_f^2) / 2$$

and is positive as expected. Operation 3 is an other recharged cycle from phase 3. We note an increase in
25 energy transfer from that of the first operation as a result of the initial capacitor voltage.

Operation 4 is a second charge transfer to phase 3. This will leave the bank at a final positive voltage of significant amplitude to inject charge and energy into
30 phase 1. This will leave the capacitor bank ready for another recharge from phase 2.

The above sequence of cycles extracts energy from phase 2 and transfers it to both phase 1 and 3. Table II shows the result of the 5 operations described. Column
35 2 shows the total charge transfers and can be

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quantitatively compared with the theoretical requirements by multiplying the reactive current by the appropriate time interval. The time interval was selected to match the charge injection requirement of phase 2. The
5 operation is selected to balance correctly the charge injection between phase 1 and 3. Column 3 shows the reactive energy extraction or injection of the three phases and can be compared to the theoretical value in column 5. No attempt is made to track the energy
10 transfer since by transferring the current directly the energy comes out automatically. Table 2 shows also the net charge and energy transfer for all three phases which should be zero. The computation balances for the exception of the residual energy in the bank. This
15 energy will be used in the following operation.

The operation to this point involved simple charge and discharge operation. Table 1, operation 6, 7, and 8, shows an important sequence of operation when the TPCS capacitor bank voltage has the wrong voltage polarity for
20 charge injection into phase 1. This requires either a complete voltage reversal internally of the TPCS or can be performed externally with the addition of the inversion section consisting of L7 and switches SCR13 and SCR14. After the number 6 recharge cycle the capacitor
25 bank is left with a negative voltage. By triggering SCR14 the bank polarity was inverted before the charge injection operation involving phase 1 could be performed.

The control operation appears at first complicated, however the logic can be relatively easily
30 formulated and can be readily implemented using modern control technology. A controller would detect the reactive current and compare it with the preprogrammed requirement. The frequency of operation would be selected to meet the magnitude of the VAR generating
35 requirement. Computations would keep track of the

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reactive current flow distribution on the three phases and select from a series of operational sequences the best one to meet the required reactive current distribution and stay within TPCS operational
5 limitations.

The described VAR regulation can be modified to operate between phases if a neutral is not available. This operation requires additional sets of switches on the lower terminal of the Module. In addition the
10 complexity and component count is increased, however some system advantages may be realized. Finally, it is understood that several of such modules may be operated in parallel with some modules specifically designed for dedicated operation and operating between dedicated
15 phases.

The operation described in this VAR generator section did only use the TPCS front end as a capacitor. In fact for simple VAR correction the TPCS voltage step-up operation is not required and a simple capacitor can
20 be used in its place. This front end mode of operation can be used not only for the VAR generation but can be extended to the other operation described in this specification. Without the TPCS step-up or step-down use I can still synthesize single or polyphase AC output, DC
25 output or output of other waveforms. The input may be from an AC grid or from a DC source. The limitation is in the output voltage range only. Without the TPCS an output of the same or reduced voltage level is most practical.

30 **Inductive Energy Storage**

The basic TPCS allows a complete electrical disconnection between the input and the output. This reliable high voltage "Opening Switch" function is the missing ingredient for practical inductive energy

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storage.

As many studies have shown, inductive energy storage requires much less weight, volume, and is significantly lower in capital cost. The implementation
5 of the TPCS as an opening or disconnect switch allows the practical implementation of such an inductive energy storage system.

Figure 11 is a simplified schematic for a TPCS magnetic energy storage assembly 60 that allows the
10 controlled build-up of a large current and inductive energy in an inductor 61 from a power source. With two or more TPCS modules the charging can be effectively continuous. In addition a free wheeling switch (SCR_{fw}) 62 can be used to circulate the current during the short
15 TPCS recharge period. The free wheeling SCR is turned off every time the TPCS module is injecting energy into the inductor.

With energy build up to the desired value the TPCS charging system is stopped and the output switch (SCR_{out})
20 63 is turned on to extract the magnetically stored energy. At this point the TPCS, connected through the SCR_{in} 64 to the inductor represents a small and discharged capacitor bank. In addition this bank will become completely isolated from the inductor as soon as the SCR_{in}
25 is reverse biased. If the output interface circuit is properly designed, the output can be stopped with a new charge injection from the TPCS charging module into the inductor as long as the TPCS instantaneously injected current is larger than the output current. This will
30 back-bias the output SCR_{out} assembly and turn it off. At this point the new inductor charging cycle is started. A filtering network 65 may be added for component protection.

The system is controlled by gate trigger
35 controller. It receives the external operational

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requirements, in and output conditions, and generates the switch trigger signals.

Reliability Impact of the TPCS

A solid state stack failure in a standard inverter
5 or converter will either cause a direct AC phase to phase
or direct DC line short. A similar component failure in
a TPCS inverter or converter will not cause the same
catastrophic failures and requires much less protection
and fault clearing equipment. In the basic TPCS
10 operation either a relative small capacitor is being
charged from a line or discharged into a line. A short
of a solid state stack will not cause a major inrush
current and can be cleared much more readily. With
several TPCS modules operating in parallel the defective
15 module can be isolated and the remaining units can remain
operational with only a reduction of the maximum power
throughput. Repairs can then be scheduled for the
appropriated time.

The operation of the basic TPCS are in three basic
20 steps, i.e. charging the capacitors, inverting the TPCS
capacitor chain, and discharging the inverted chain. A
step is typically completed before the next is being
initiated. If a failure is detected and the sequence is
interrupted no short current will flow from the input to
25 the output. At that point the system can be isolated
without the use of high power air blast switchgear. This
is especially beneficial on the DC side, since DC faults
are much more difficult to clear. In fact TPCS AC to AC
transformation unit, AC to DC converter, and DC to AC
30 inverter can be each used as an effective high power and
high voltage interrupter. In addition the basic operation
of any described TPCS device has a maximum power
throughput and can not be overloaded. The TPCS can be
used as an effective power or current limiting device.

35 Although the invention is described with respect

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to a preferred embodiment, modifications thereto will be apparent to those skilled in the art. Therefore, the scope of the invention is to be determined by reference to the claims which follow.

5 I Claim as my invention:

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Claims

1. A transformerless power conversion system comprising:
 - a plurality of capacitors connected in series;
 - 5 a charging circuit connected to said plurality of capacitors, said charging circuit charging the plurality of capacitors from a voltage source to a predetermined voltage;
 - a circuit for inverting the polarity of the charge
10 stored in selected capacitors of said plurality of capacitors, said polarity inverting circuit including a plurality of inductor circuits, each of which can be switchably coupled to a corresponding different one of the selected capacitors to form a resonant circuit which
15 aids in inverting the polarity of a stored charge in that capacitor; and
 - a discharging circuit for extracting power from the plurality of capacitors at a transformed voltage.

2. The transformerless power conversion system of
20 claim 1 further comprising a control circuit controlling the operation of the charging circuit, the inverting circuit and the discharging circuit, said control circuit establishing a charging phase in which the charging circuit charges the plurality of capacitors, an inversion
25 phase in which said inverting circuit to invert the polarity of said selected capacitors, and a discharge phase in which the discharge circuit extracts power from the plurality of capacitors.

3. The transformerless power conversion system of
30 claim 2 wherein the control circuit causes the inversion phase and the discharge phase to overlap in time.

4. The transformerless power conversion system of

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claim 2 wherein a cycle of operation includes a charge phase, an inversion phase and an extraction phase, and wherein said control circuit causes said power conversion system to go through many cycles of operation per second.

5 5. The transformerless power conversion system of claim 1 wherein said inverting circuit comprises a plurality of unidirectional switching devices for electrically coupling each of said inductor circuits to its corresponding one of said plurality of capacitors

10 6. The transformerless power conversion system of claim 5 wherein each of at least some of said plurality of unidirectional switching devices is a thyristor.

 7. The transformerless power conversion system of claim 5 wherein each of at least some of said plurality
15 of unidirectional switching devices is an SCR.

 8. The transformerless power conversion system of claim 5 wherein each of at least some of said plurality of unidirectional switching devices is a Crossatron.

 9. The transformerless power conversion system of
20 claim 5 wherein each of at least some of said plurality of unidirectional switching devices is a gate turn-off device (GTO).

 10. The transformerless power conversion system of claim 1 wherein each of at least some of said
25 plurality of inductor circuits includes an inductor.

 11. The transformerless power conversion system of claim 10 wherein the inductor in each of at least some of said inductor circuits is an air core inductor.

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12. The transformerless power conversion system of claim 1 further comprising a high frequency output filter through which the extracted power passes.

13. The transformerless power conversion system of claim 1 wherein each inductor circuit is connected across its corresponding capacitor and comprises an inductor in series with a unidirectional switch.

14. The transformerless power conversion system of claim 1 further comprising a high frequency input filter between the voltage source and the charging circuit.

15. The transformerless power conversion system of claim 1 wherein the charging circuit charges the plurality of capacitors during a charging phase to a voltage that is about twice the value of the instantaneous voltage of the voltage source.

16. The transformerless power conversion system of claim 1 wherein the charging circuit comprises an inductor which is connected in series with the voltage source.

17. The transformerless power conversion system of claim 1 wherein the charging circuit comprises an inductor and a unidirectional switch which are connected in series with the voltage source.

18. The transformerless power conversion system of claim 17 wherein the unidirectional switch of the charging circuit electrically couples the inductor of the charging circuit to said plurality of capacitors to form a resonance charging circuit.

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19. The transformerless power conversion system of claim 17 wherein the control circuit causes the unidirectional switch of said charging circuit to electrically couple the charging circuit inductor to said plurality of capacitors and then subsequently decouple said charging circuit inductor from said plurality of capacitors when a preselected period of time has elapsed, wherein the electrical coupling of said charging circuit inductor with said plurality of capacitors forms a resonant circuit having a resonance period of T seconds, and wherein the preselected period of time is about $T/2$ seconds.

20. The transformerless power conversion system of claim 19 wherein the control circuit causes the charging circuit to couple and then decouple said charging inductor to said plurality of capacitors many times per second.

21. The transformerless power conversion system of claim 1 wherein the charging circuit charges said plurality of capacitors so that the polarities of the charged series connected capacitors alternate.

22. The transformerless power conversion system of claim 21 wherein the inverting circuit inverts the polarity of every other one of said plurality of capacitors so that the polarities of the charged series connected capacitors are the same.

23. The transformerless power conversion system of claim 1 wherein the magnitude of the transformed voltage is larger than the voltage of the voltage source.

24. The transformerless power conversion system

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of claim 23 wherein the number of said plurality of capacitors is N and the magnitude of the transformed voltage is $2N$ times the voltage of the voltage source.

25. The transformerless power conversion system
5 of claim 1 wherein said plurality of capacitors comprises
 N capacitor stages each of which includes two of said plurality of capacitors connected in series and has a first and second terminal and a charging terminal electrically coupled to the point at which the two
10 capacitors in that stage are connected together, and wherein said charging circuit charges said N capacitor stages through said charging terminals.

26. The transformerless power conversion system
of claim 25 wherein said charging circuit includes a
15 switching means for electrically coupling the first and second terminals of each of said capacitor stages to a common point during the charging of said capacitor stages.

27. The transformerless power conversion system
20 of claim 26 wherein each of said plurality of inductor circuits is connected across the first terminal and charging terminal of a corresponding different one of said capacitor stages.

28. The transformerless power conversion system
25 of claim 25 wherein N is greater than one.

29. The transformerless power conversion system
of claim 1 wherein the series connected capacitors have a first terminal at one end and a second terminal at the other end and the discharging circuit electrically
30 couples to the first and second terminals of said series

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connected capacitors to produce the transformed voltage.

30. The transformerless power conversion system of claim 1 wherein the charging circuit charges said plurality of series connected capacitors so that the
5 polarities of the charged series connected capacitors are all the same.

31. The transformerless power conversion system of claim 30 wherein the inverting circuit inverts the polarity of every other one of said plurality of
10 capacitors so that the polarities of the charged series capacitors alternate.

32. The transformerless power conversion system of claim 1 wherein the magnitude of the transformed voltage is less than the voltage of the voltage source.

15 33. The transformerless power conversion system of claim 32 wherein the number of said plurality of capacitors is N and the magnitude of the transformed voltage is $2/N$ times the voltage of the voltage source.

34. The transformerless power conversion system
20 of claim 1 wherein said charging circuit charges said plurality of capacitors in series.

35. The transformerless power conversion system of claim 1 wherein said plurality of capacitors comprises N capacitor stages each of which includes two of said
25 plurality of capacitors connected in series and has a first and second terminal and a discharging terminal electrically coupled to the point at which the two capacitors in that stage are connected together, and wherein said discharging charging circuit extracts power

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from said N capacitor stages through said discharging terminals.

36. The transformerless power conversion system of claim 35 wherein each of said plurality of inductor
5 circuits is connected across the first terminal and the discharging terminal of a corresponding different one of said N capacitor stages so as to invert the polarity of one of the two capacitors in that stage.

37. The transformerless power conversion system
10 of claim 1 wherein the discharge circuit comprises an output inductor and a unidirectional switch which couples power from the plurality of capacitors into the output inductor during the discharge phase.

38. The transformerless power conversion system
15 of claim 37 wherein the discharge circuit further comprises a unidirectional device preventing the output inductor from reversing the polarity of the plurality of capacitors during the discharge phase.

39. The transformerless power conversion system
20 of claim 1 wherein said discharging circuit transfers power to a load and wherein said control circuit monitors said load and controls the operation of the charging circuit, the inverting circuit and the discharging circuit to produce a regulated output to said load.

25 40. The transformerless power conversion system of claim 1 wherein said control circuit operates said charging, inverting and discharging circuits to produce a predetermined output current pulse shape.

41. The transformerless power conversion system

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of claim 4 wherein said control circuit operates said charging, inverting and discharging circuits to produce a sequence of output current pulse shapes of varying amplitude.

5 42. The transformerless power conversion system of claim 4 wherein said voltage source is a DC voltage source and said control circuit operates said charging, inverting and discharging circuits to synthesize an AC voltage output.

10 43. The transformerless power conversion system of claim 1 further comprising a filter circuit that filters the transformed voltage to produce an output voltage, wherein said control circuit includes means for monitoring the output voltage and wherein said control
15 circuit operates said charging, inverting and discharging circuits to regulate the output voltage.

 44. The transformerless power conversion system of claim 42 further comprising a filter circuit that filters the AC voltage output to produce a filtered AC
20 output voltage, wherein said control circuit includes means for monitoring the filtered AC output voltage and wherein said control circuit operates said charging, inverting and discharging circuits to regulate the filtered AC output voltage.

25 45. The transformerless power conversion system of claim 4 wherein said voltage source is an AC signal source of a frequency f_s and wherein said control circuit operates said charging, inverting and discharging
circuits to synthesize an AC output signal having a
30 frequency f_o that is different from the frequency f_s .

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46. The transformerless power conversion system of claim 45 wherein the frequency f_s of the AC signal source varies over time and wherein the frequency f_o of the AC output signal is fixed.

5 47. The transformerless power conversion system of claim 46 wherein f_o is 60 Hz.

48. The transformerless power conversion system of claim 1 wherein said charging circuit comprises a plurality of charge transfer circuits each connected to a
10 different phase of a multiphase AC power line and said discharging circuit comprises a plurality of charge injection circuits each connected to a different phase of the multiphase AC power line and wherein said control
15 circuit operates said plurality of charge transfer circuits and said plurality of charge injection circuits to extract power from one phase of said multiphase power line and inject the extracted power into another phase of said multiphase power line.

49. The transformerless power conversion system
20 of claim 48 wherein said control circuit selects the other phase into which the extracted power is injected so as to alter the phase angle of the AC signal into which the extracted power is injected.

50. The transformerless power conversion system
25 of claim 4 further comprising a separate energy storage device connected to said discharging circuit for temporarily storing the extracted power.

51. An electrical charge transfer apparatus for transferring charge from one phase to another phase of a
30 multiphase power line carrying a AC signal having a

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period of T_{AC} , said apparatus comprising:

a capacitive storage circuit;

a plurality of charging circuits each of which is
for coupling a different phase of said multiphase AC
5 power line to said capacitive storage circuit and to
thereby transfer charge from that phase to the storage
circuit, each of said plurality of charging circuits
including a unidirectional switch and an inductor,
wherein each of said charging circuits when coupled to
10 said capacitive storage circuit forms a corresponding
resonant circuit having a resonant frequency of $1/T_i$,
where i is an index identifying the charging circuit and
wherein the T_i are all less than T_{AC} ;

a plurality of injecting circuits each of which is
15 for coupling said capacitive storage circuit to a
different one of said phases of said multiphase power
line and to thereby inject charge from said capacitive
storage circuit into the phase to which it is coupled,
each of said injection circuits comprising an inductor
20 and a unidirectional switch; and

a control circuit operating the unidirectional
switches of said plurality of charging circuits to
produce a sequence of resonant charging cycles that
transfer charge from one phase to said capacitive storage
25 unit and operating the unidirectional switches of said
plurality of injecting circuits to transfer charge from
the capacitive storage unit to another phase.

52. The electrical charge transfer apparatus of
claim 51 wherein said control circuit operates the
30 unidirectional switches of said plurality of charging
circuits to transfer charge to the capacitive circuit
through a sequence of resonant charging cycles each
having a duration of about $T_i/2$ seconds.

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53. The electrical charge transfer apparatus of claim 51 wherein said plurality of charging circuits and said plurality of injecting circuits share components whereby the inductor of one of said charging circuits is
5 also the inductor of a corresponding one of said injecting circuits.

54. The electrical charge transfer apparatus of claim 51 wherein said control circuit operates the unidirectional switches of said plurality of charging
10 circuits and said plurality of injecting circuits to transfer energy from one phase to another so as to achieve phase angle correction in the phase to which the energy is transferred.

55. The electrical charge transfer apparatus of claim 51 wherein said control circuit operates the unidirectional switches of said plurality of charging
15 circuits and said plurality of injecting circuits to transfer energy from one phase to another so as to achieve harmonic correction in the phase to which the energy is transferred.
20

56. The electrical charge transfer apparatus of claim 51 wherein said control circuit operates the unidirectional switches of said plurality of charging
circuits and said plurality of injecting circuits to
25 transfer energy from one phase to another so as to achieve waveform distortion correction.

57. The electrical charge transfer apparatus of claim 51 wherein the capacitive storage circuit comprises the transformerless power conversion system of claim 1.

30 58. The electrical charge transfer apparatus of

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claim 51 wherein the capacitive storage circuit is a capacitor.

59. The electrical charge transfer apparatus of claim 58 wherein said plurality of charging circuits and
5 said plurality of injecting circuits all share a common inductor whereby that shared inductor serves as the inductor of each said charging circuits and the inductor of each of said injecting circuits.

60. The electrical charge transfer apparatus of
10 claim 58 further comprising a series combination of an input inductor and an input unidirectional switch in parallel with said capacitive storage circuit and wherein said control circuit operates said input unidirectional switch to reverse polarity of charge stored in said
15 capacitive storage circuit.

61. A method of converting power from a voltage source, the method comprising:
charging a plurality of series connected capacitors from the voltage source;
20 inverting the polarity of the charge stored in selected capacitors of said plurality of capacitors;
electrically coupling to said plurality of capacitors to produce a transformed voltage;
extracting power from the plurality of capacitors
25 at the transformed voltage; and
repeating the afore-mentioned sequence of steps many times per second.

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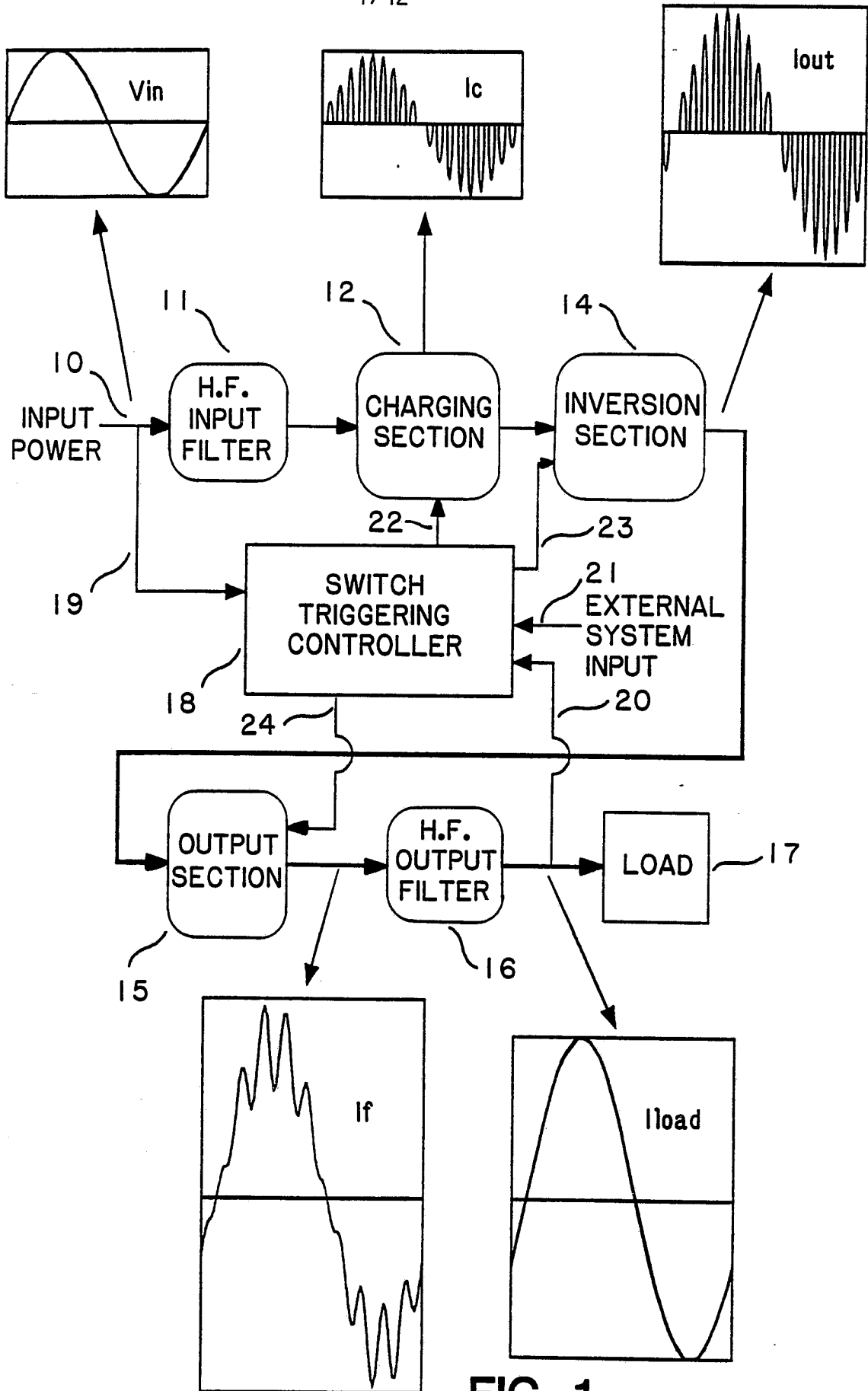


FIG. 1

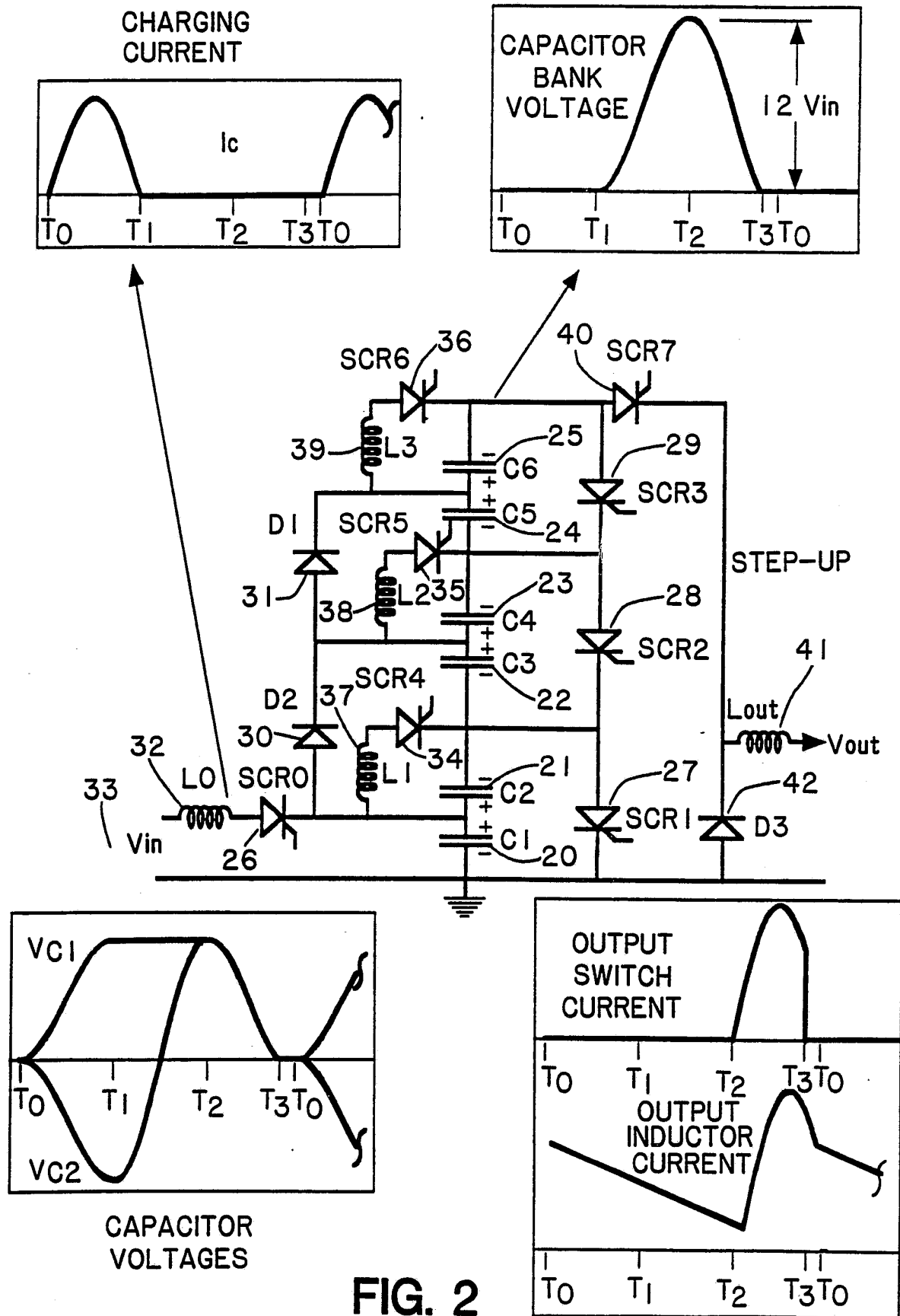


FIG. 2

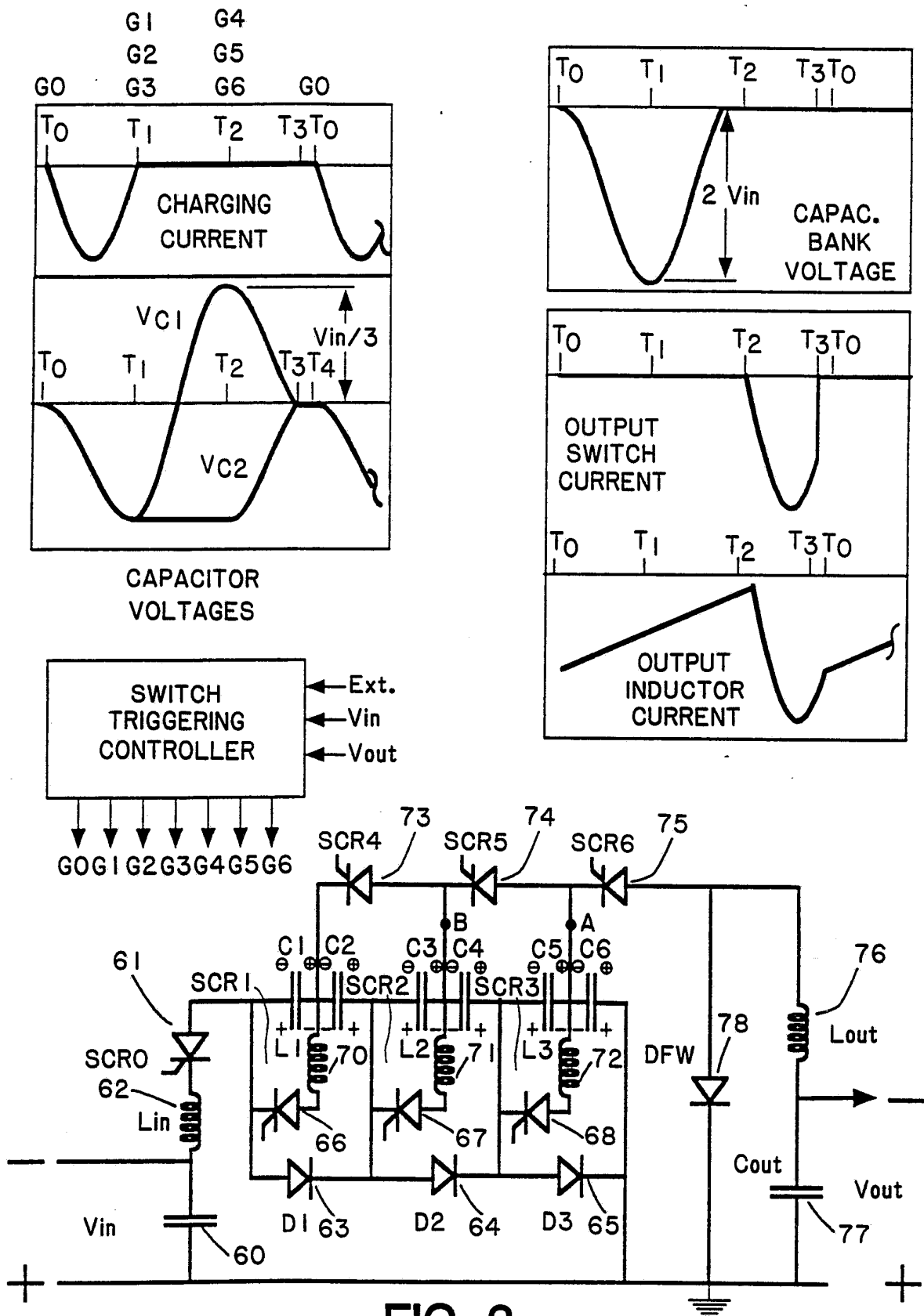
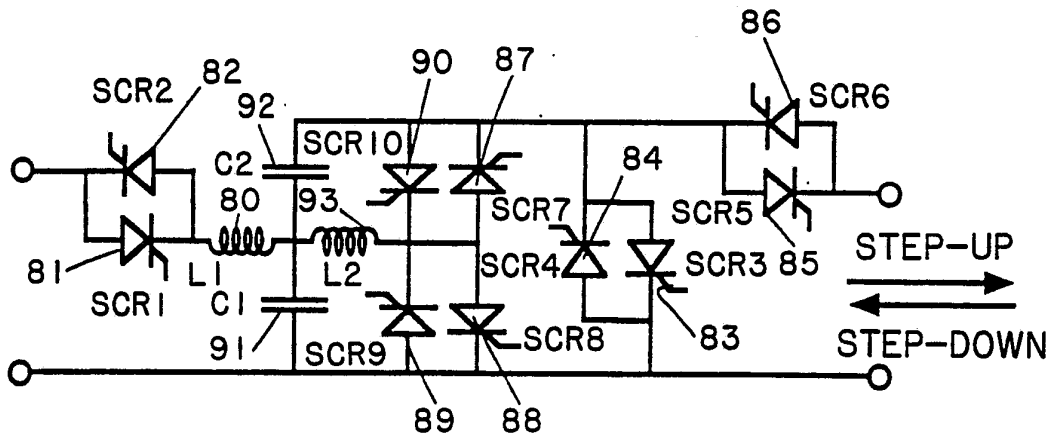


FIG. 3



PART	+ IN + OUT	+ IN - OUT	- IN + OUT	- IN - OUT	NO.
SCR1	X	X,Y		Y	81
SCR2	Y		X,Y	X	82
SCR3	X	X,Y		Y	83
SCR4	Y		X,Y	X	84
SCR5	X		X,Y	Y	85
SCR6	Y	X,Y		X	86
SCR7	X			Y	87
SCR8		X,Y			88
SCR9			X,Y		90
SCR10	Y			X	91

X=STEP-UP Y=STEP-DOWN

FIG. 4

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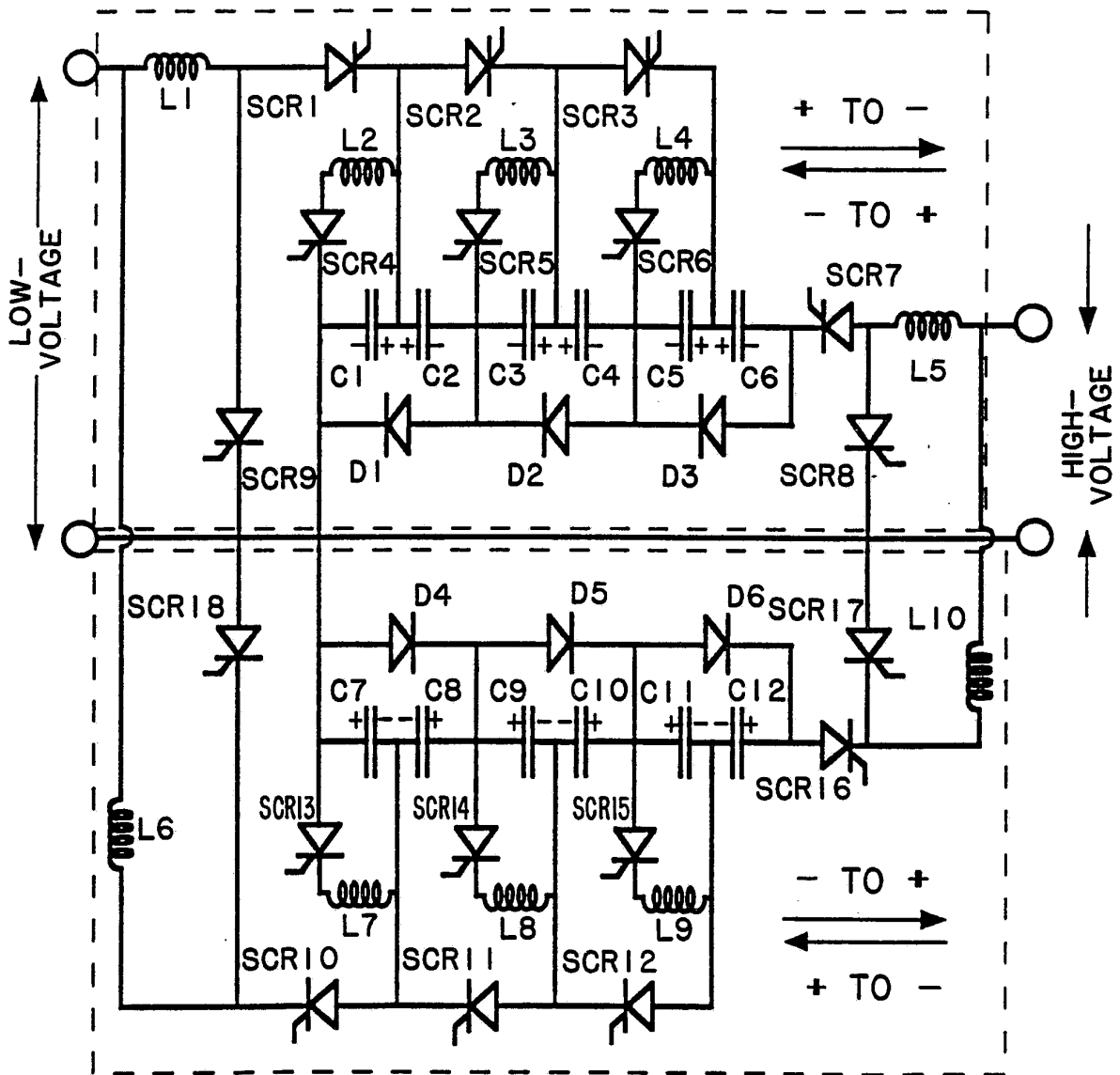
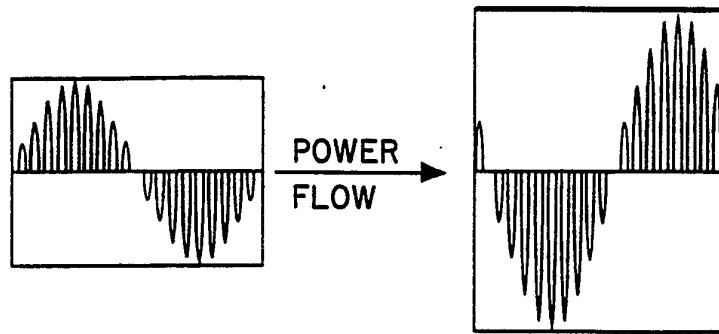
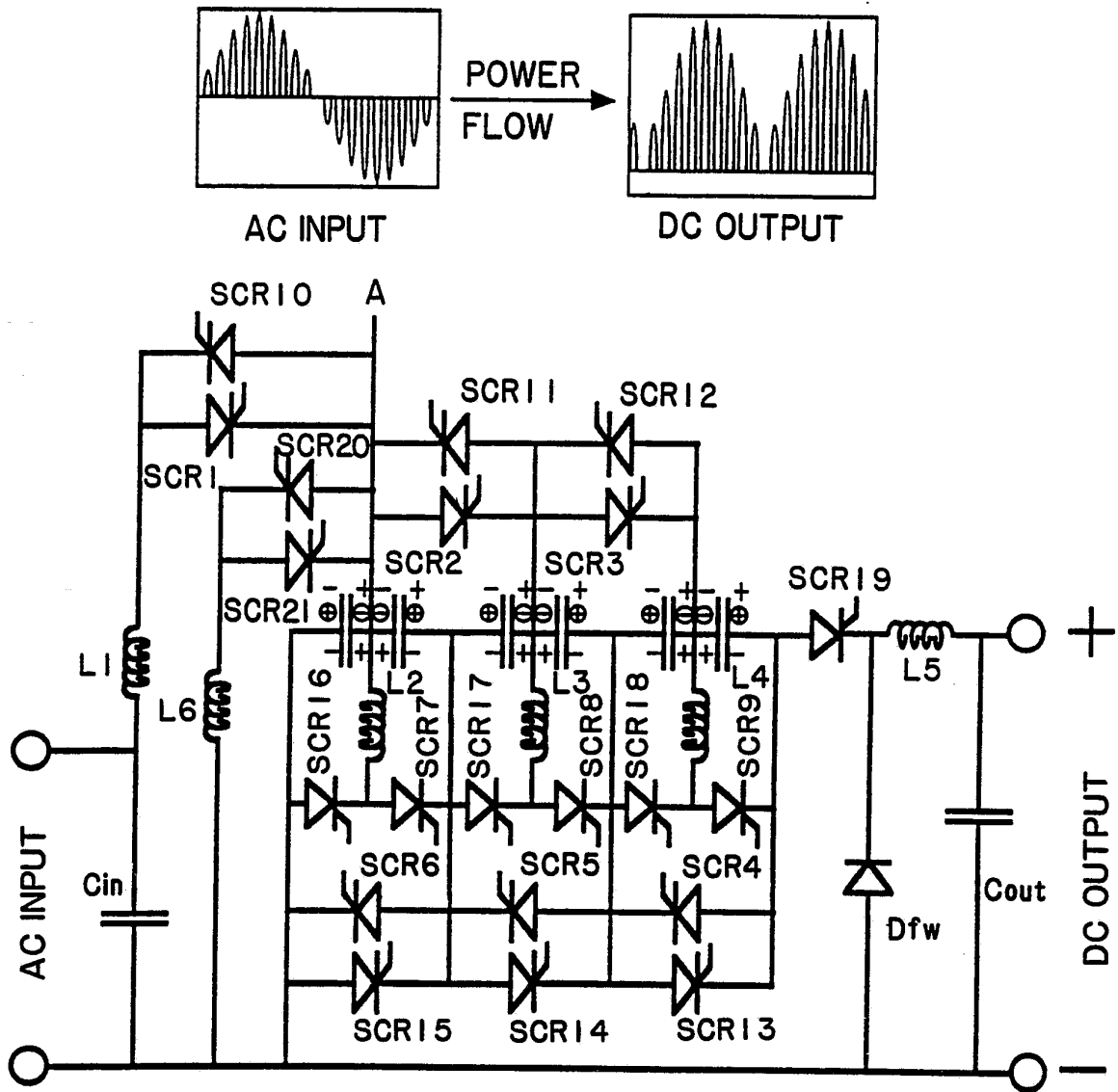


FIG. 5

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SWITCH TRIGGERING TABLE		
FUNCTION	POSITIVE CYCLE	NEGATIVE CYCLE
CHARGING	1,2,3,4,5,6	10,11,12,13,14,15
INVERSION	7,8,9	16,17,18
DISCHARGING	19	19
INPUT INVERSION	20,11,12,13,14,15	21,2,3,4,5,6

FIG. 6

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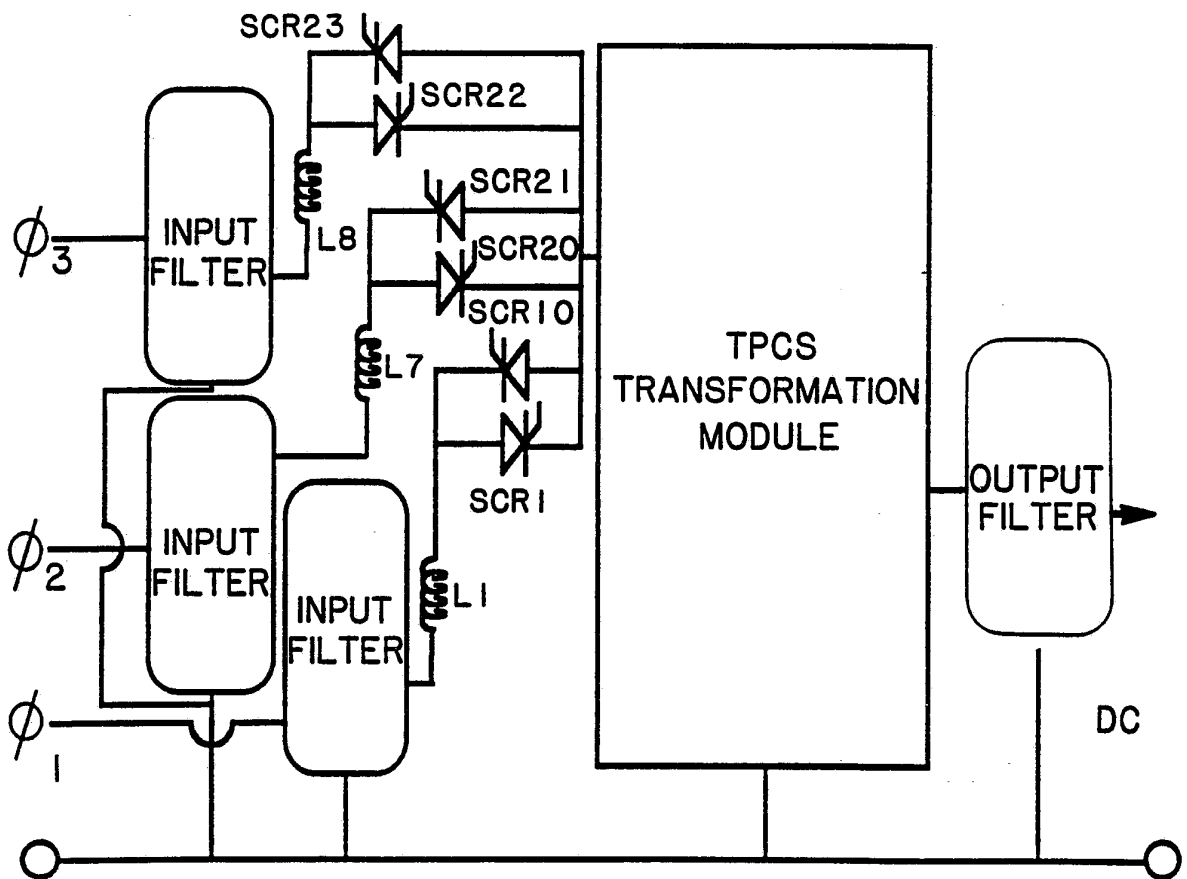
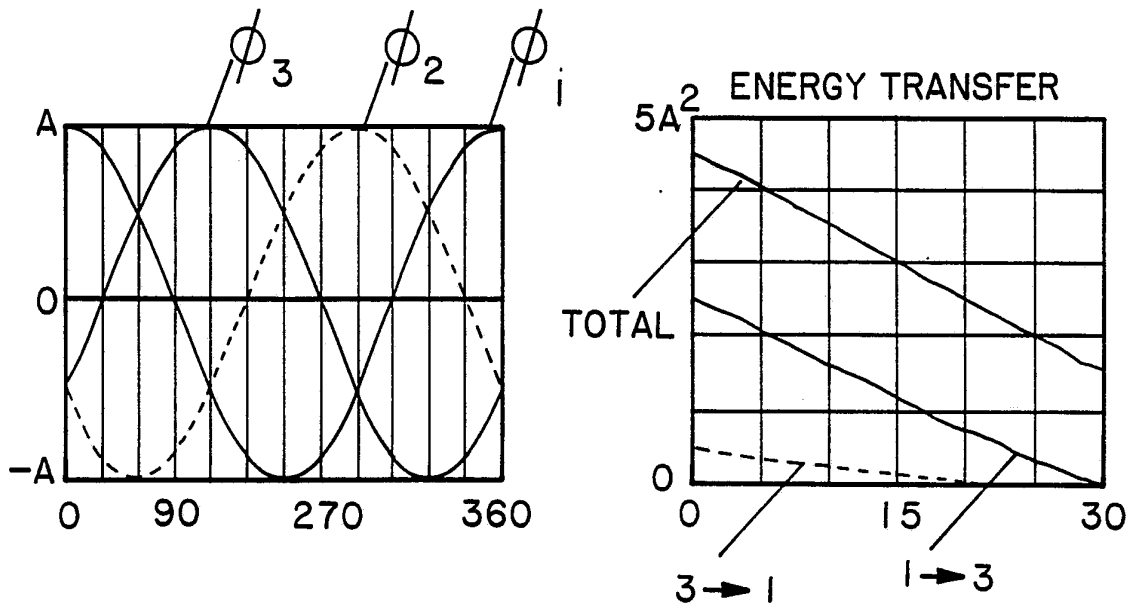


FIG. 7

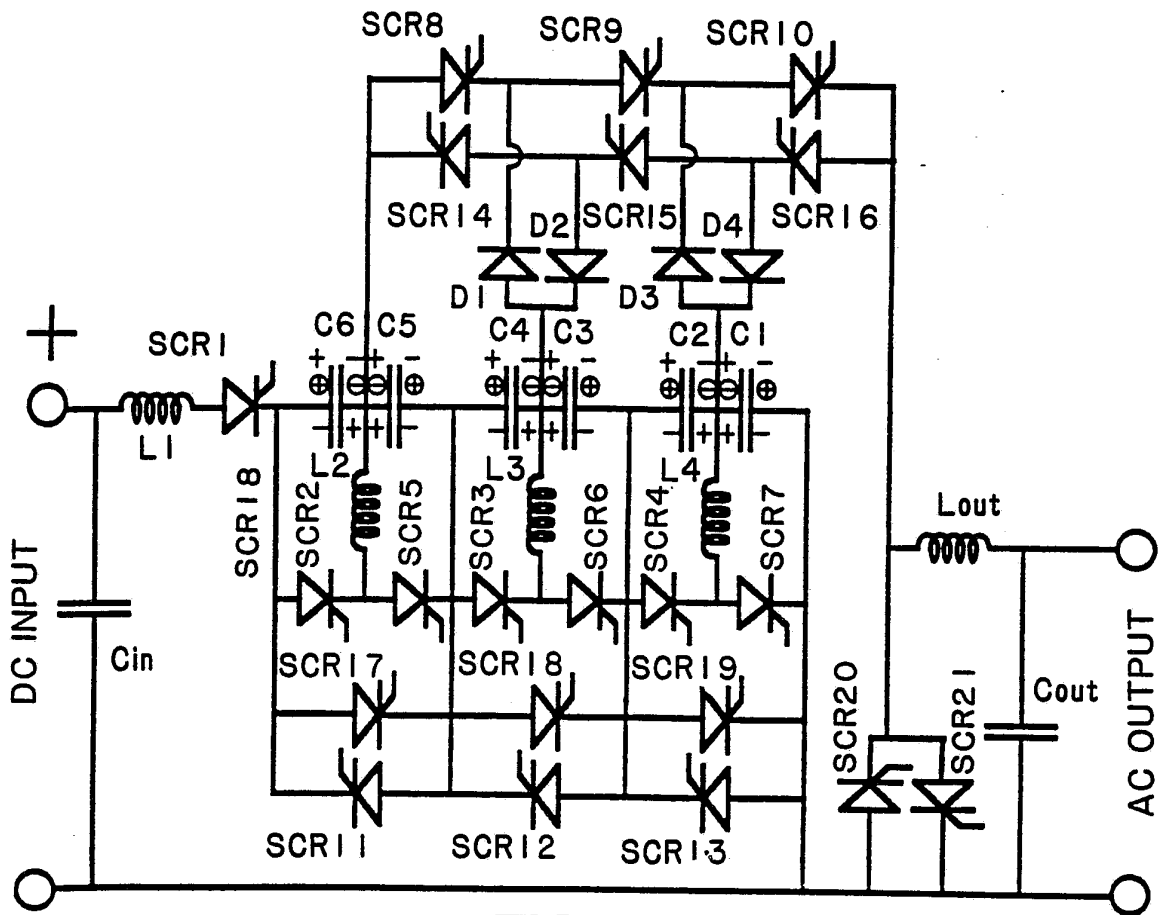
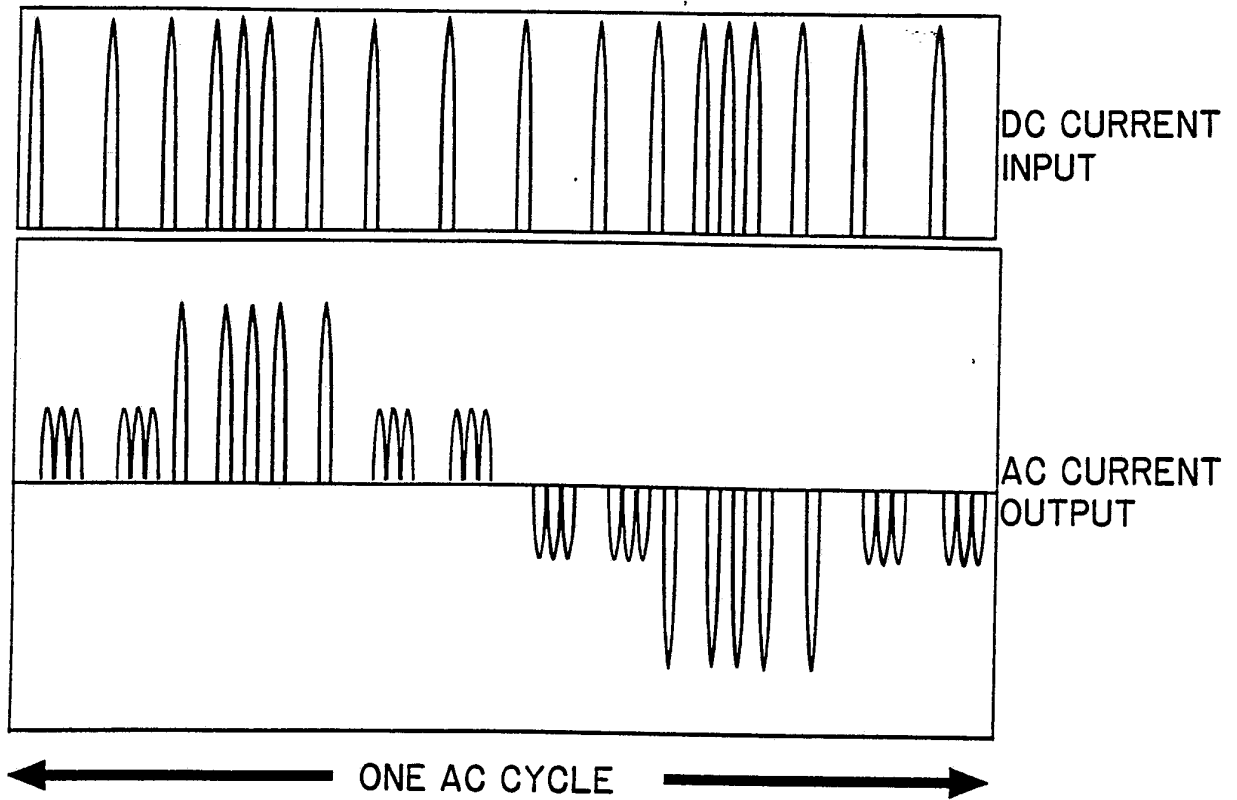
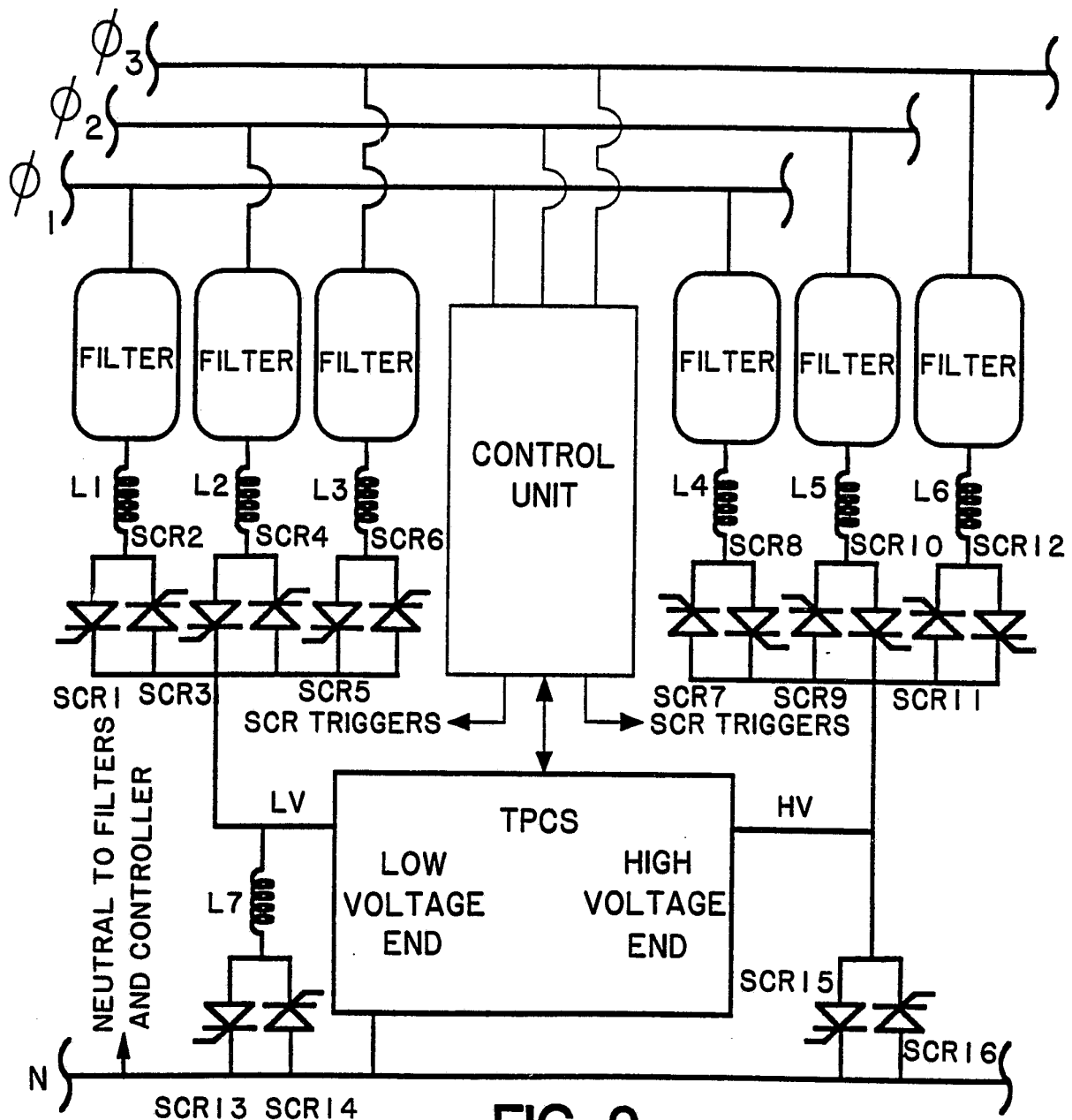


FIG. 8

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V = VOLTAGE
Ir = REACTIVE CURRENT
Pr = REACTIVE POWER

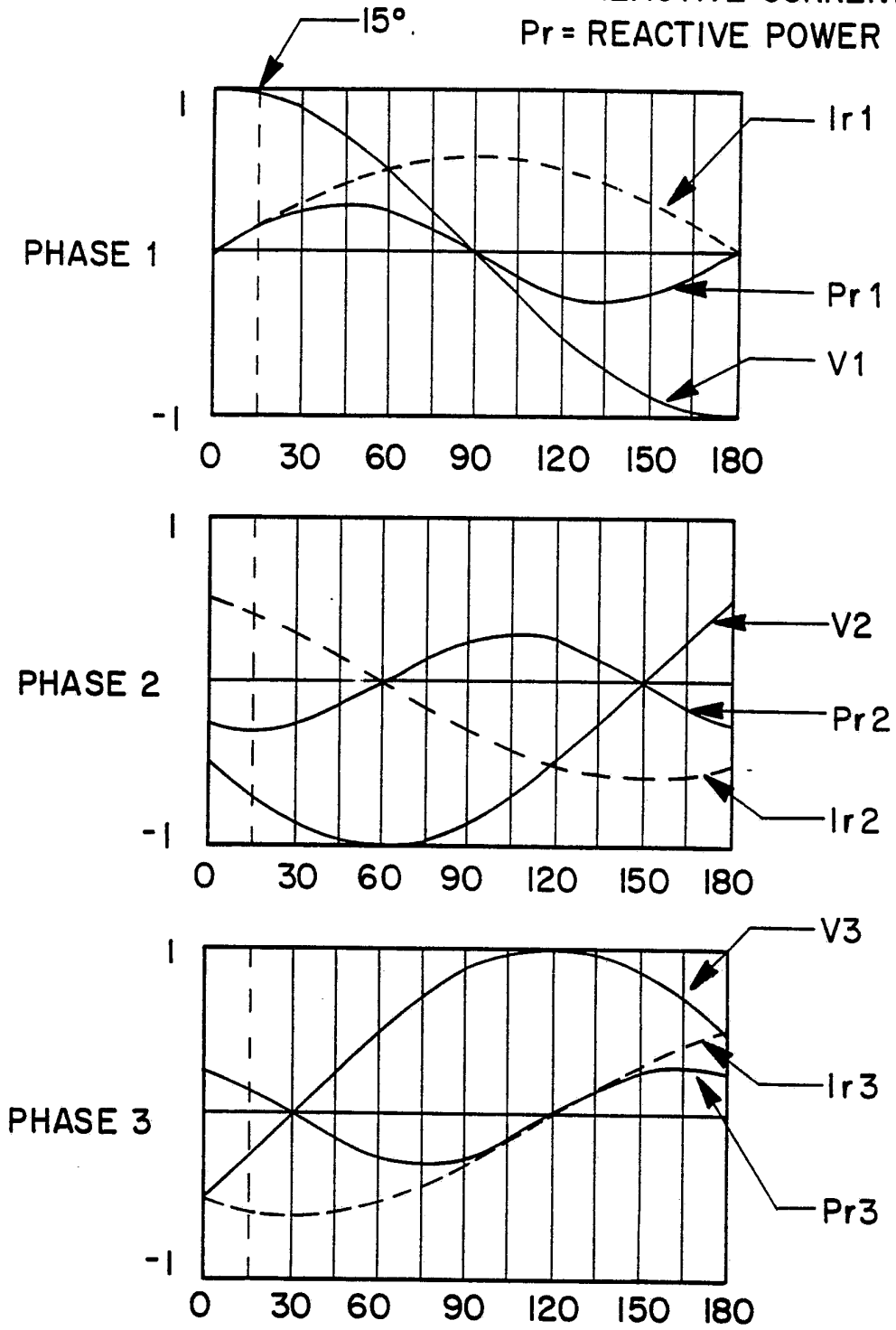


FIG. 10

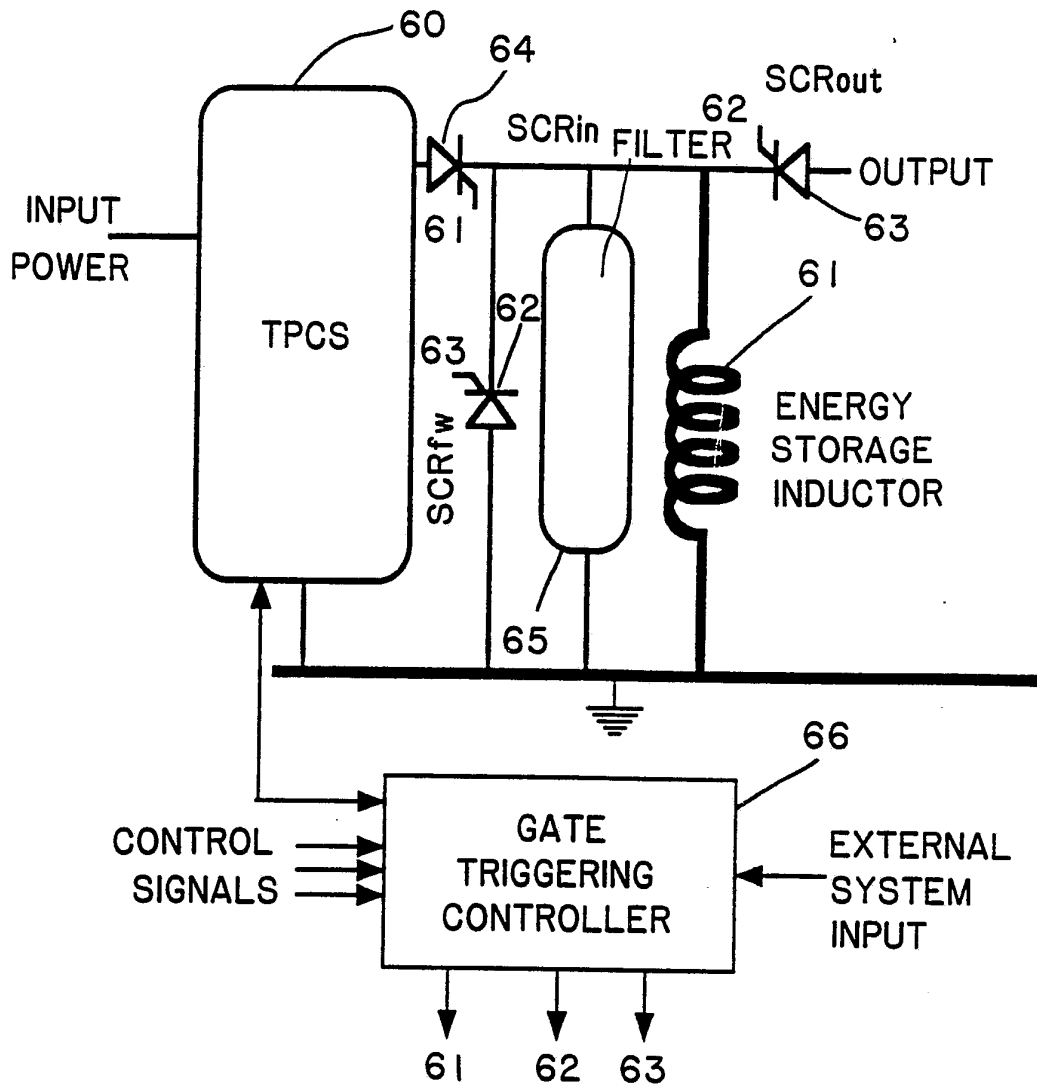


FIG. 11

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TABLE 1 VAR SEQUENCE

OPERATION	SWITCH	V _{in} /V _m	V _{ph} /V _m	V _f /V _m	Q/CV _m	E/E ₀
1. Phase 2 charging	SCR4	0.00	-0.707	-1.414	+1.414	-1.000
2. Phase 3 discharging	SCR5	-1.414	-0.259	+0.897	-2.311	+0.598
3. Phase 2 charging	SCR4	+0.897	-0.707	-2.311	+3.207	-2.268
4. Phase 3 discharging	SCR5	-2.311	-0.259	+1.793	-4.104	+1.062
5. Phase 1 discharging	SCR2	+1.793	+0.966	+0.139	+1.654	+1.598
6. Phase 2 charging	SCR4	+0.139	-0.707	-1.553	+1.692	-1.196
7. capacitor inverting	SCR14	-1.553	0.00	+1.553	0.00	0.00
8. Phase 1 discharging	SCR2	+1.553	+0.966	+0.379	+1.174	+1.134
9. Phase 2 charging	SCR4	+0.379	-0.707	-1.553	+1.692	-1.196

TABLE 2 VAR RESULT

OPERATION	ΔQ _t /CV _m	I _r Δt	ΔE _t /E ₀	P _{rΔt} /E ₀
Phase 1	+1.654	+1.691	+1.598	+1.634
Phase 2	+4.622	+4.620	-3.268	-3.268
Phase 3	-6.415	-6.311	+1.660	+1.634
TOTAL	-0.139	0.00	-0.010	0.00
RESIDUAL	+0.139		0.010	

INTERNATIONAL SEARCH REPORT

International application No.
PCT/US93/03146

A. CLASSIFICATION OF SUBJECT MATTER				
IPC(5) :H02M 3/07, 7/04 US CL :363/140 According to International Patent Classification (IPC) or to both national classification and IPC				
B. FIELDS SEARCHED				
Minimum documentation searched (classification system followed by classification symbols) U.S. :				
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched				
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) APS: (Polarity (10A) (chang? or Revers? or Switch?)(10A) Capacitor) And (307/109-110 pr 320/1 or 363/59-63 or 363/140)/CCLST				
C. DOCUMENTS CONSIDERED TO BE RELEVANT				
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.		
A	US,A, 3,808,512 (Sakka) 30 April 1974 see entire document.	1-61		
A	US,A, 4,636,930 (Bingham et al.) 13 January 1987 see entire document.	1-61		
A	US,A, 4,807,104 (Floyd et al.) 21 February 1989 See entire document.	1-61		
A	US,A, 4,812,961 (Essaff et al.) 14 March 1989 See entire document.	1-61		
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input type="checkbox"/> See patent family annex.				
<table style="width:100%; border:none;"> <tr> <td style="width:50%; border:none;"> * Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed </td> <td style="width:50%; border:none;"> "T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family </td> </tr> </table>			* Special categories of cited documents: "A" document defining the general state of the art which is not considered to be part of particular relevance "E" earlier document published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family
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Date of the actual completion of the international search 14 MAY 1993		Date of mailing of the international search report 19 JUL 1993		
Name and mailing address of the ISA/US Commissioner of Patents and Trademarks Box PCT Washington, D.C. 20231 Facsimile No. NOT APPLICABLE		Authorized officer <i>Ngoc Ho Nguyen</i> Mr JEFFREY L. STERRETT INTERNATIONAL DIVISION Telephone No. (703) 308-1632		