



## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

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<b>(21) International Application Number:</b> PCT/US93/01507 <b>(22) International Filing Date:</b> 22 February 1993 (22.02.93)  <b>(30) Priority data:</b> 07/842,088                                        26 February 1992 (26.02.92)        US  <b>(71) Applicant:</b> MATERIALS RESEARCH CORPORATION [US/US]; Route 303, Orangeburg, NY 10962 (US).  <b>(72) Inventors:</b> SOMMER, Bruce, A. ; 4520 East Baseline Road #2034, Phoenix, AZ 85044 (US). EICHMAN, Eric, C. ; 3619 East Verdin Road, Phoenix, AZ 85044 (US). CHURLEY, Michael, J. ; 6750 South Bonarden, Tempe, AZ 85283 (US).		<b>(74) Agents:</b> JORDAN, Joseph, R. et al.; Wood, Herron & Evans, 2700 Carew Tower, Cincinnati, OH 45202 (US).  <b>(81) Designated States:</b> AT, AU, BB, BG, BR, CA, CH, CZ, DE, DK, ES, FI, GB, HU, JP, KP, KR, LK, LU, MG, MN, MW, NL, NO, NZ, PL, PT, RO, RU, SD, SE, SK, UA, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, SN, TD, TG).  <b>Published</b> <i>Without international search report and to be republished                upon receipt of that report.</i>
<b>(54) Title:</b> AMMONIA PLASMA TREATMENT OF SILICIDE CONTACT SURFACES IN SEMICONDUCTOR DEVICES  <b>(57) Abstract</b>  <p>Methods are disclosed for treating semiconductor devices with an ammonia (NH<sub>3</sub>) plasma subsequent to freon/O<sub>2</sub> etching to remove native oxide layers. Freon/O<sub>2</sub> etching leaves a fluoridated polymer residue which is removed by the ammonia plasma, thereby resulting in improved contact resistanc of the final semiconductor device product.</p>		

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AMMONIA PLASMA TREATMENT OF SILICIDE  
CONTACT SURFACES IN SEMICONDUCTOR DEVICES

Field of the Invention

The present invention relates to a process for treating semiconductor devices which results in improved contact resistance.

Background of the Invention

Semiconductor devices are typically fabricated starting with a substrate, e.g., a silicon wafer, having an insulating dielectric layer such as silicon dioxide on the surface thereof. In many applications, the silicon substrate has contact or junction regions in which the silicon is doped with boron, phosphorous, arsenic, or any other suitable doping compound. The doped contact regions in the substrate are exposed by etching a desired pattern in the oxide layer to form contact or via holes therein. Such etching is performed using etching techniques well known in the art. The contact holes are then filled with a conductor, such as aluminum, to provide electrical contact between the doped regions of the substrate and a conductive film which may be deposited

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over the dielectric insulating layer to serve as a low resistance interconnection within the semiconductor device. Such films may be aluminum, doped polycrystalline silicon, tungsten, or another refractory metal.

The deposition of aluminum as the interconnect layer over an oxide insulating layer having contact holes therein has several drawbacks. One of these which is of particular concern is that aluminum diffuses rapidly or "spikes" into the silicon contact or junction regions exposed in the contact holes. As a means for inhibiting aluminum spiking in doped silicon contacts, it is known to deposit a barrier layer in the contact holes over the silicon contacts. One suitable barrier material is titanium nitride (TiN) and it may be deposited by known chemical vapor deposition or sputtering techniques.

The fabrication of semiconductor devices including, among other things, a barrier layer and an interconnect layer, as described above, typically includes as a first step etching the insulating layer to form contact holes therein. Subsequently, the barrier layer is deposited in the contact holes, and thereafter the desired interconnect layer may be deposited. In such fabrication processes, it is imperative that the surfaces upon which the various layers are deposited be free from impurities such as

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oxide films which form upon exposure to oxygen, i.e., native oxide films. The failure to provide clean contact between the layers of conductive material results in undesirably high contact resistance of the semiconductor device at the location of the contact regions. This high contact resistance greatly limits overall device speed and limits the development of higher density semiconductor devices.

Various techniques are known for removing native oxide films prior to deposition of subsequent layers such as titanium nitride, tungsten or aluminum over silicon contact regions. One known method uses freon/oxygen plasma etch chemistry to remove the native oxide from the exposed contact surfaces. This method has the drawback that a fluoridated carbon polymer residue is formed on the wafer contact surface. The polymer residue inhibits the growth of vapor deposited titanium nitride, thus adversely affecting the contact resistance of the final product.

#### Summary of the Invention

The present invention is directed to a process for pre-treating semiconductor devices with an ammonia plasma which results in improved contact resistance and which overcomes the drawbacks associated with prior art techniques, as discussed above.

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In one aspect of the invention, ammonia plasma is utilized to remove deposition-inhibiting polymer residues from semiconductor contact surfaces which residues result from known freon/oxygen plasma etching used to remove native oxides. The use of ammonia plasma treatment subsequent to freon/oxygen plasma etching, but prior to low pressure chemical vapor deposition (LPCVD) of titanium nitride, or deposition of other materials including tungsten and aluminum, enables the CVD reaction to grow a desired titanium nitride film without the polymer residue contamination and without native oxide, both of which deleteriously affect the contact resistance of the final semiconductor device product.

In a preferred embodiment, a method of the present invention comprises treating a silicon wafer substrate with an ammonia plasma in a CVD reaction chamber prior to LPCVD of titanium nitride (or other material) to thereby remove any undesirable fluorinated polymer residue from an earlier freon/oxygen plasma etch. Alternatively, the ammonia plasma pre-treatment may be performed in a pre-treatment chamber and then the device is transferred to a CVD reaction chamber for subsequent processing.

These and other features and advantages of the present invention will become apparent to persons

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skilled in the art upon reading the following detailed description.

Detailed Description of the Invention

In the fabrication of typical semiconductor devices, a substrate (usually silicon) is provided with a plurality of doped contact or junction regions, which are formed by techniques well known in the art and which do not form a part of the present invention. Typically, a semiconductor device initially has a continuous dielectric insulating layer formed on the substrate. The dielectric insulating layer may be a doped or undoped, deposited or grown silicon dioxide insulating layer. Thereafter, the insulating layer is selectively etched by known techniques to form contact or via holes in which the doped regions are exposed. Exposure of the device to oxygen oxidizes the silicon substrate and a thin layer of native oxide is formed on the exposed surface of the doped regions. This native oxide layer must be removed since it increases the contact resistance of the semiconductor device.

In accordance with one aspect of the present invention, a semiconductor device which has a native oxide layer on the exposed contact surfaces is treated with an ammonia ( $\text{NH}_3$ ) plasma subsequent to a known freon/oxygen etch. The freon/oxygen etching is a well known technique for removing the native oxide layer from semiconductor devices. The use of freon/ $\text{O}_2$

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etching leaves a fluoridated carbon polymer residue on the exposed substrate surfaces. The ammonia plasma treatment of the present invention removes the polymer residue, thus resulting in an overall improvement in the contact resistance of the device.

By way of example but not intended to be limiting in any way, the freon/O<sub>2</sub> etching of a semiconductor device may be accomplished as follows. The semiconductor device is placed in a pre-treatment chamber and exposed to a flow of CF<sub>4</sub>/O<sub>2</sub> on the order of about 30 sccm. The gas composition is preferably about 92% O<sub>2</sub> and about 8% CF<sub>4</sub>. The reactor pressure is about 1.5 torr and is powered at about 150 watts RF. For titanium silicide contacts, the etch treatment is preferably about 35 seconds. It will be appreciated by persons skilled in the art that various other freon/O<sub>2</sub> etching protocols may be used, and such procedures are known in the art.

Once the semiconductor wafer is treated with the freon/oxygen etching, it is then subjected to an ammonia plasma treatment, which is the focus of the present invention. The ammonia plasma treatment may be carried out in a pretreatment chamber or in a CVD chamber. The following examples are illustrative of the ammonia plasma treatment of the present invention.



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## EXAMPLE 1

A semiconductor device, which was previously treated with a freon/O<sub>2</sub> etch, is subjected to ammonia plasma treatment in a pretreat chamber. The ammonia gas flows at a rate of about 30 sccm and the treatment chamber is maintained at a pressure of about 500 mtorr. The wafer temperature is about 80°C and the reactor is powered with about 225 watts RF power. About 60 seconds of ammonia plasma treatment effectively removes the fluoridated polymer residue resulting from the freon/O<sub>2</sub> etch.

## EXAMPLE 2

A semiconductor device, which was previously treated with a freon/O<sub>2</sub> etch, is subjected to ammonia plasma treatment in a CVD reaction chamber. The ammonia gas flows at a rate of about 100 sccm, the treatment chamber is maintained at a pressure of about 150 mtorr, and at a temperature in the range of about 650°-680°C. The reactor is powered with about 200 watts RF power and the plasma treatment is accomplished in about 10 seconds.

Utilizing the ammonia plasma treatment of the present invention, the resulting semiconductor devices are suitable for subsequent deposition of blanket or selective tungsten, titanium nitride, aluminum (or alloys thereof), or virtually any conductive material. The resulting devices have good

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ohmic contact, on the order of about  $10^{-8}$  ohm  $\text{cm}^2$  (specific contact resistance). The ammonia plasma treatment of the present invention may be accomplished in a pretreatment chamber utilizing an ammonia flow rate of at least about 20 sccm, at pressures in the range of about 0.1-10 torr, and for treatment times of at least about 30 seconds. When the process is performed in a CVD reaction chamber, the ammonia gas flow rate is at least about 100 sccm, the pressure is in the range of about 0.1-10 torr and treatment times of at least about 10 seconds are utilized. When the procedure is performed in a pretreatment chamber, the gas temperature is preferably in the range of about 25-400°C. When a CVD reaction chamber is used, the gas temperature is preferably in the range of about 450°-750°C.

It will be appreciated by persons skilled in the art that various modifications can be made to the method of the present invention without departing from the scope of the present invention as defined by the appended claims.

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1. A method of treating a semiconductor device subsequent to a freon/O<sub>2</sub> plasma etch, said method comprising:

treating said semiconductor device with an ammonia plasma subsequent to a freon/O<sub>2</sub> plasma etch, wherein said ammonia plasma treatment results in improved contact resistance in said semiconductor device.

2. A method of claim 1 wherein said ammonia plasma treating is carried out in a pretreatment chamber at a pressure in the range of about 0.1-10 torr and at a gas temperature in the range of about 25°C-400°C.

3. A method of claim 2 wherein said ammonia plasma treating is carried out for at least about 30 seconds at a gas flow rate of at least about 20 sccm.

4. A method of claim 1 wherein said ammonia plasma treating is carried out in a CVD reaction chamber at a pressure in the range of about 0.1-10 torr and at a gas temperature in the range of about 450-750°C.

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5. A method of claim 4 wherein said ammonia plasma treating is carried out for at least about 10 seconds at a gas flow rate of at least about 100 sccm.