



US00D807830S

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(54) **CABLE PORT CHASSIS**(71) Applicant: **Cisco Technology, Inc.**, San Jose, CA (US)(72) Inventors: **George Edward Curtis**, San Jose, CA (US); **Edward John Kliewer**, Sunnyvale, CA (US); **Amrik Singh Bains**, Livermore, CA (US)(73) Assignee: **Cisco Technology, Inc.**, San Jose, CA (US)(**) Term: **15 Years**(21) Appl. No.: **29/554,920**(22) Filed: **Feb. 17, 2016**(51) LOC (11) Cl. **13-03**

(52) U.S. Cl.

USPC **D13/139.2**(58) **Field of Classification Search**

USPC D13/139.1–139.8, 138.1–138.2, D13/137.1–137.4, 184, 182, 155, 152, D13/122

CPC H01R 13/516; H01R 13/6666; H01R 13/447; H01R 24/542; H01R 25/006; G09F 1/1616; G09F 1/1184; G02B 6/4452; H05K 7/1425; H05K 7/1439

See application file for complete search history.

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Primary Examiner — Rhea Shields(74) *Attorney, Agent, or Firm* — Merchant & Gould P.C.(57) **CLAIM**

The ornamental design for a cable port chassis, as shown and described.

DESCRIPTION

FIG. 1 is a top, front and right side perspective view of a cable port chassis according to our design;

FIG. 2 is a front view thereof;

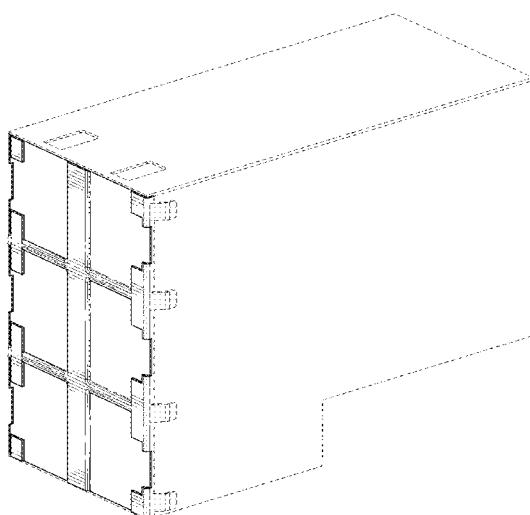
FIG. 3 is a left side view thereof;

FIG. 4 is a right side view thereof;

FIG. 5 is a top view thereof; and,

FIG. 6 is a bottom view thereof, the rear not shown being devoid of ornamentation.

In the drawings, the broken lines are for the purpose of illustrating environment only and form no part of the claimed design. Shade lines having a combination of long and short lines in the Figures show contour and not surface ornamentation.

1 Claim, 6 Drawing Sheets

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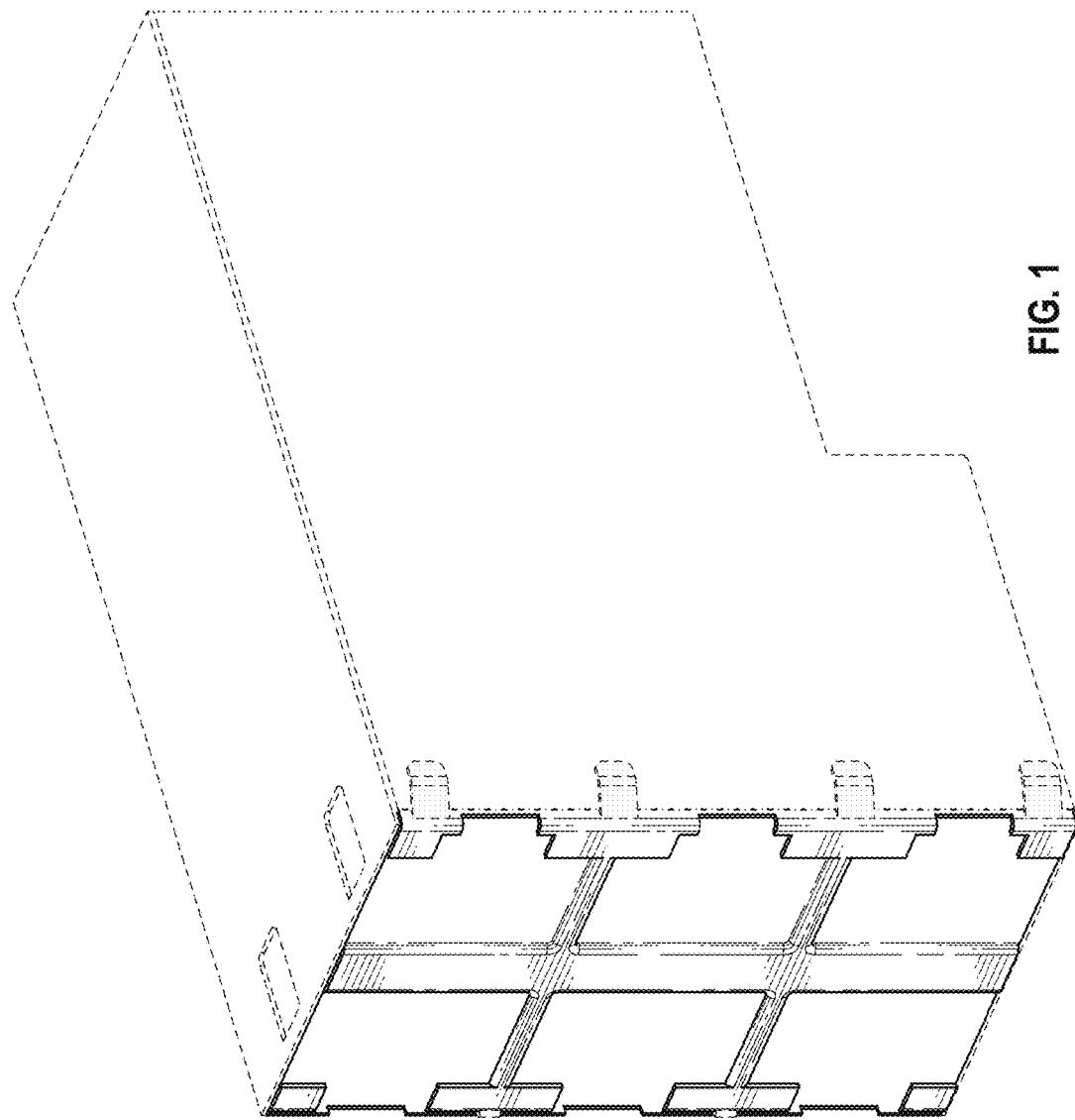
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FIG. 1



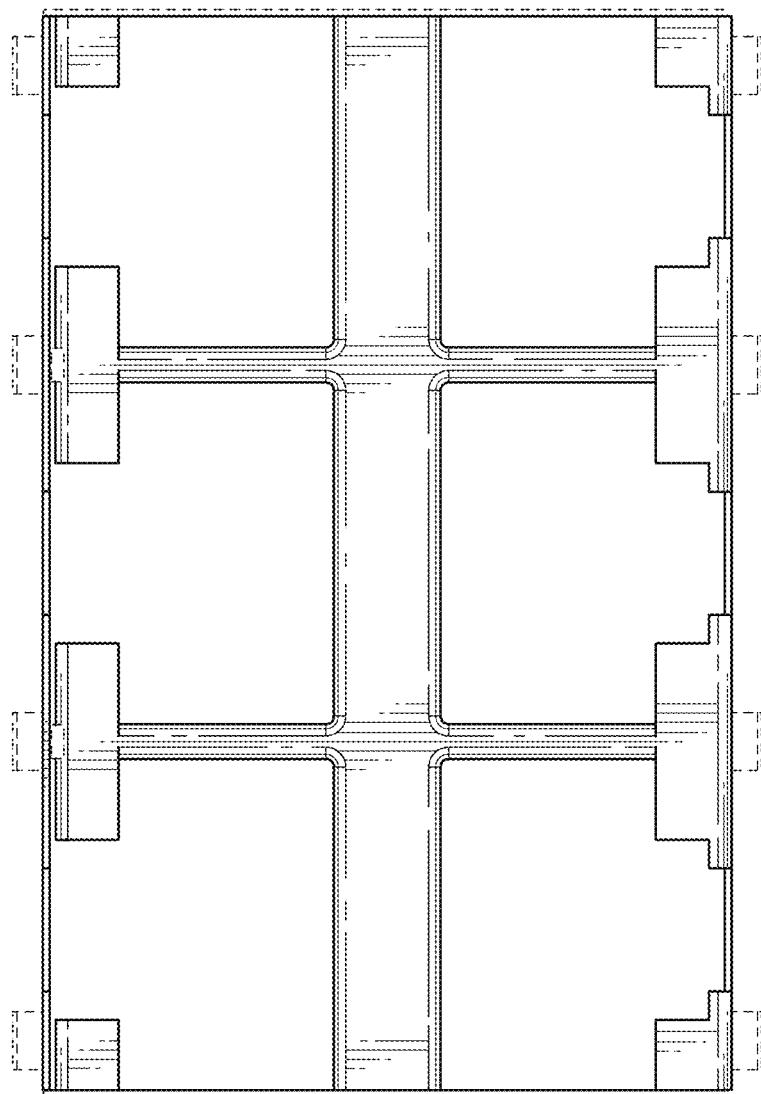


FIG. 2

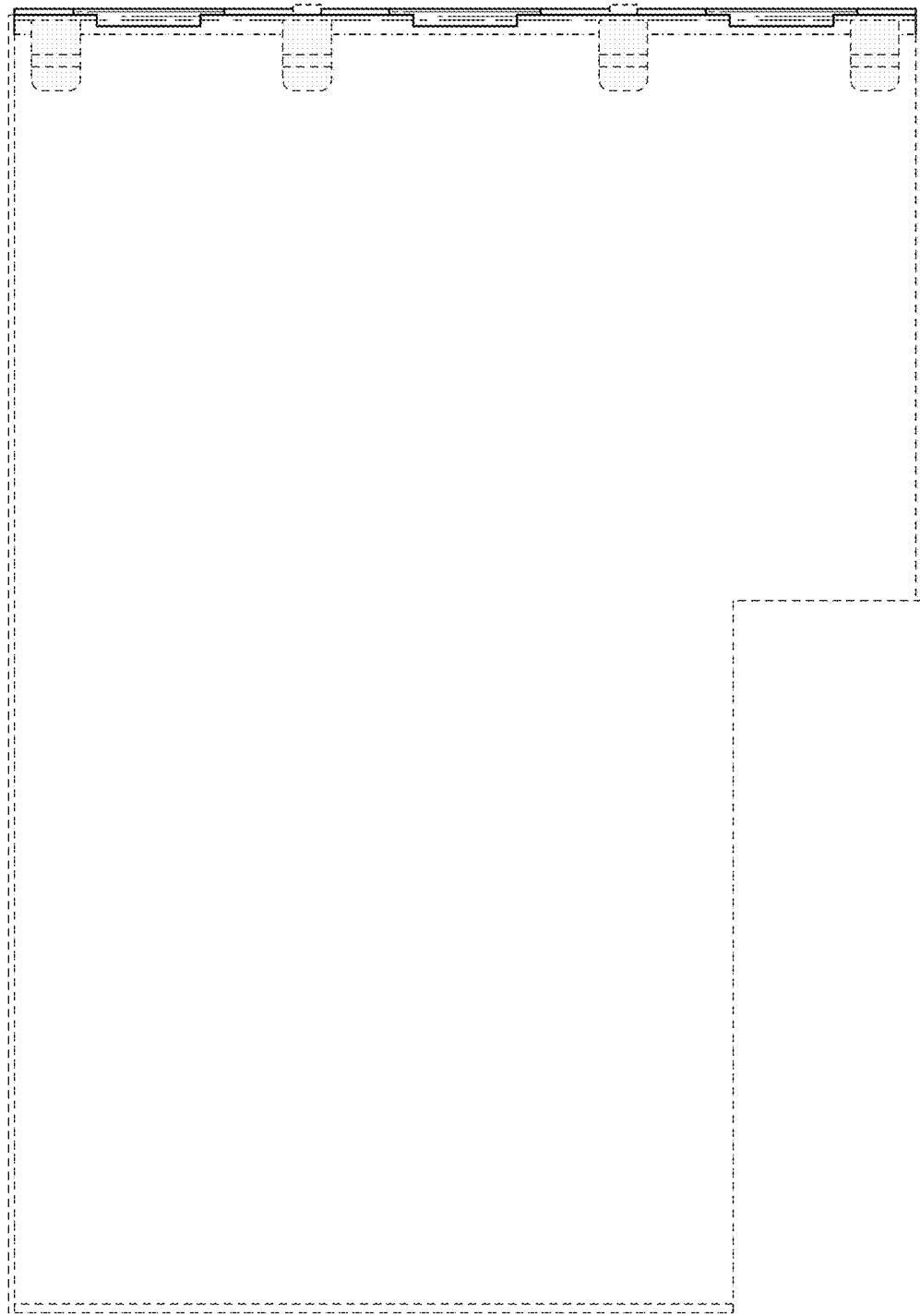


FIG. 3

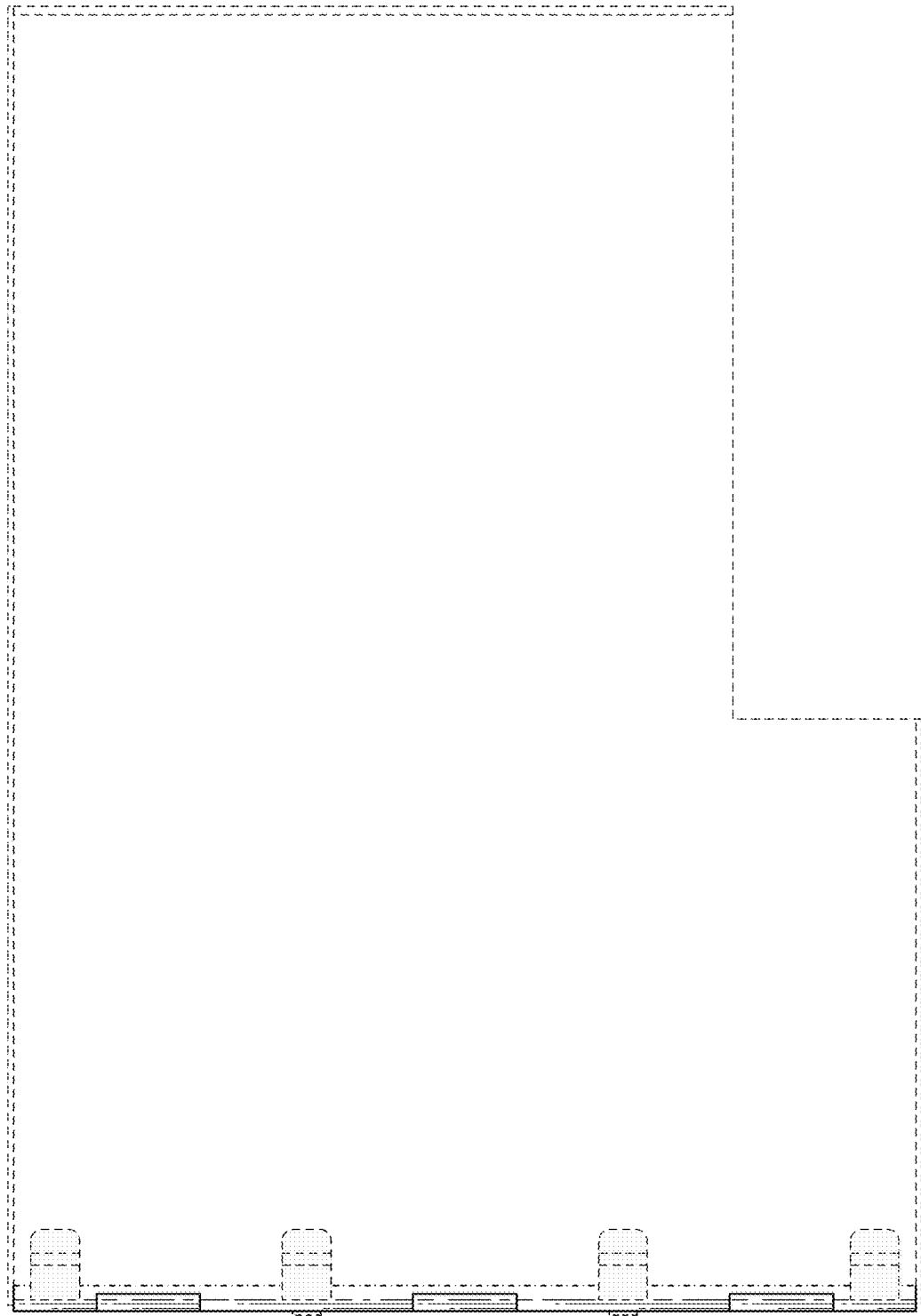


FIG. 4

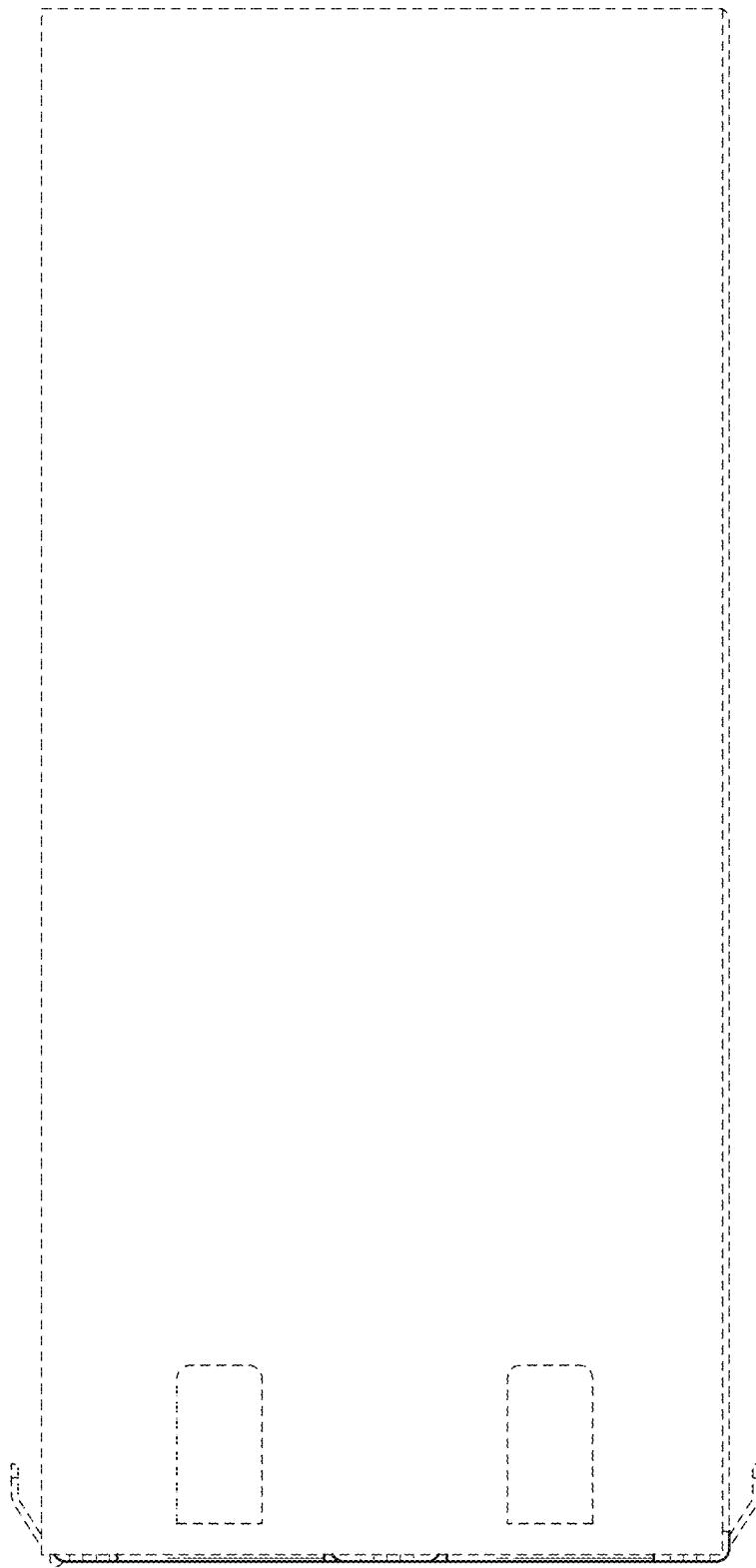


FIG. 5

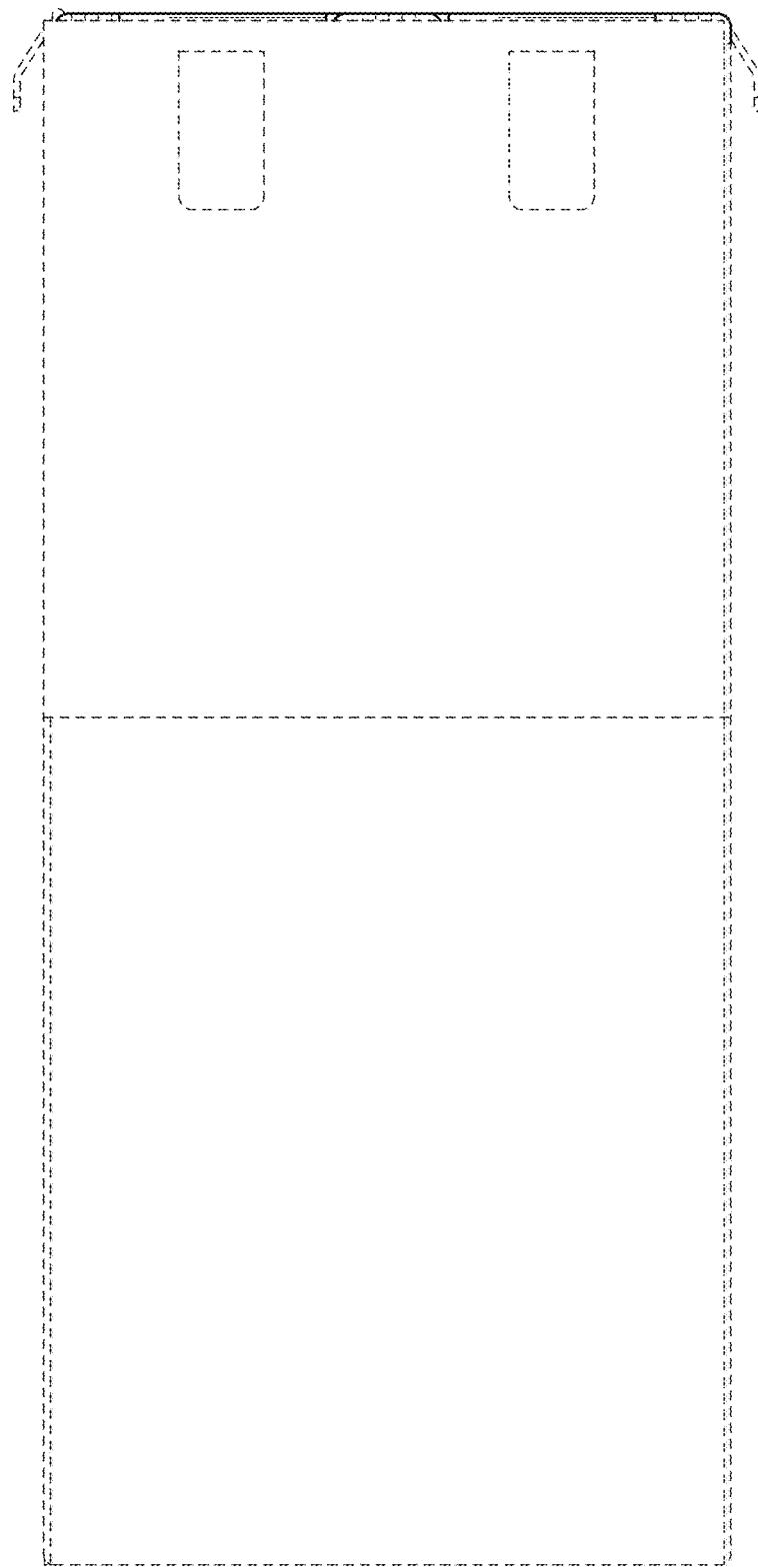


FIG. 6