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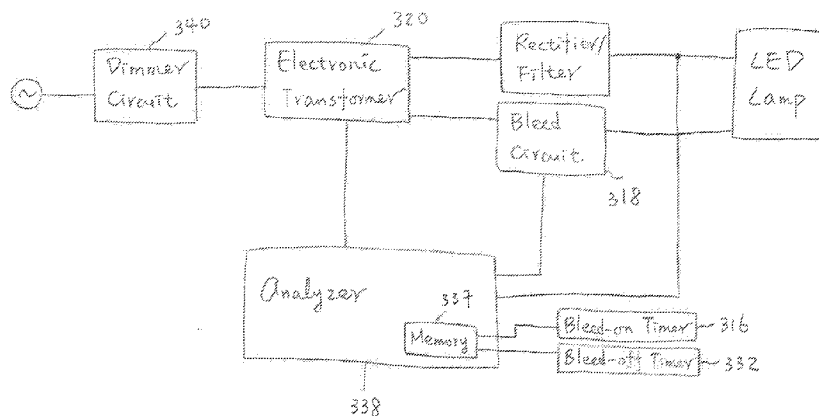


FIG. 3B

(57) **Abstract:** A bleed circuit is applied to a transformer based on a bleed-on time and a bleed-off time determined by monitoring an output voltage waveform of the transformer.



**SYSTEMS AND METHODS OF APPLYING BLEED CIRCUITS IN LED LAMPS**CROSS-REFERENCE TO RELATED APPLICATION

5 [0001] This application claims priority to, and the benefits of, U.S. Provisional Application Serial No. 61/576,482, filed on December 16, 2011, the entire disclosure of which is hereby incorporated by reference.

FIELD OF THE INVENTION

10 [0002] Embodiments of the invention generally relate to light-emitting-diode (LED) lamps and, more particularly, to applications of a bleed circuit in such lamps.

BACKGROUND

[0003] An LED lamp (also known as a bulb or, more generally, an LED lighting product) may  
15 be used to replace an incandescent, halogen, or other bulb; the LED lamp provides the same or similar light while consuming less power and providing a longer operational lifetime. One of the difficulties in designing, in particular, halogen-replacement LED lamps is compatibility with the transformers — for example, electronic transformers — that have traditionally powered halogen  
20 bulbs. An electronic transformer is a complex electrical circuit and produces a high-frequency (i.e., 100 kHz or greater) AC voltage with a low-frequency (60 Hz) envelope output. An LED lamp powered by the electronic transformer requires a special design to ensure a consistent light output, free of any random or periodic flicker. For example, the electronic transformer has a minimum load requirement; below this minimum load, a portion of the transformer's pulse-train output is either intermittent or entirely cut off. If the load is absent or insufficient, the electronic transformer  
25 will enter a low power (or "sleep") mode until the minimum load requirement is satisfied. When the transformer alternately enters and exits the sleep mode, flickering of the LED lamp may result.

[0004] To avoid this problem, some LED lamps use a "bleed" circuit (or a "bleeder"), including, for example, a resistive element, to ensure that the minimum load requirement of the electronic transformer is constantly satisfied. The bleed circuit, however, does not produce light; it  
30 merely wastes power, especially if the bleed circuit is implemented during the entire output voltage waveform of the electronic transformer. The bleed circuit, therefore, may not be compatible with a low-power application. Additionally, the time interval during which the bleed circuit is applied to the electronic transformer (the "application time") may vary if dimming is implemented or different combinations of the electronic transformer and dimmer are used with the LED lamp. A fixed

5 application time may result in unwanted deactivation, or switching on the sleep mode, of the electronic transformer. The application time of the bleed circuit may be determined by continuously monitoring the output voltage waveform of the transformer. However, the necessary monitoring circuitry, if continuously utilized during operation of the LED, will itself undesirably waste power and may generate waste heat that must be dissipated to extend the LED lifetime.

10 Consequently, there is a need for an approach that can determine the appropriate application time of the bleed circuit with minimized power consumption during operation of the LED lamp.

### SUMMARY

[0005] In various embodiments, the present invention relates to systems and methods for  
15 applying a bleed circuit to an electronic transformer using a "bleed-on" timer and a "bleed-off" timer. The "bleed-on" and "bleed-off" timers, for counting down the time that the bleed circuit is on and off, respectively, are set based on the leading and trailing edges of an output voltage waveform of the electronic transformer when an LED lamp is powered on for the first time; the determined bleed-on and bleed-off times are used to activate and de-activate the bleed circuit  
20 during the operation of the LED lamp with no need to continuously monitor the voltage waveform at the output of the transformer. In one embodiment, the bleed-on and bleed-off times are stored in non-volatile memory and can be retrieved and used to control the application of the bleed circuit to the transformer each time the LED lamp is turned on; this avoids the need for monitoring circuitry as well as time delays imposed by the need to analyze the output voltage waveform and then  
25 determine the application time of the bleed circuit. Utilization of bleed-on and bleed-off timers thus avoids initial flickering when the LED lamp is turned on and obviates the need for additional power-consuming circuitry.

[0006] In some embodiments, when a sleep mode (or turnoff) of the electronic transformer is detected (via monitoring the period or frequency of the output voltage waveform, for example) due  
30 to, e.g., the use of a dimming system or different combinations of the electronic transformer and dimmer, the bleed-on and bleed-off times of the bleed circuit are adjusted until the minimum load requirement is satisfied and/or the period of the output voltage waveform is equivalent to that of the transformer. The bleed-on and bleed-off times are then stored in non-volatile memory and can be retrieved upon power-up of the LED lamp. Accordingly, while monitoring circuitry may be used  
35 initially to set the timing of the bleed circuit, it not used thereafter to actually control the circuit. This approach reduces energy consumption while operating the LED lamp in addition to satisfying the minimum load requirement of the electronic transformer to prevent flickering in the LED lamp.

5 [0007] Accordingly, in one aspect, the invention pertains to a method of operating an LED system including an electronic transformer. In representative embodiments, the method includes monitoring an output voltage waveform of the transformer; starting a bleed-on timing cycle upon sensing a first voltage-waveform condition and ending the bleed-on timing cycle after the determined bleed-on time has elapsed; starting a bleed-off timing cycle upon sensing a second  
10 voltage-waveform condition different from the first voltage-waveform condition, and ending the bleed-off timing cycle after the determined bleed-off time has elapsed; and engaging a bleed circuit to the transformer based on a state of the bleed-on timing cycle and disengaging the bleed circuit from the transformer based on a state of the bleed-off timing cycle. In one implementation, the bleed circuit is engaged for a duration adequate to ensure a minimum load on the transformer  
15 sufficient to avoid cut-off throughout the output voltage waveform.

[0008] In various embodiments, the relevant state of the bleed-on timing cycle is the start of the cycle, and the relevant state of the bleed-off timing cycle is the end of the cycle. The bleed-on timing cycle may start when the trailing edge of the output voltage waveform falls below a predetermined threshold and may end when a subsequent trailing edge of the output voltage  
20 waveform falls below a predetermined minimum threshold. The bleed-off timing cycle may start when the leading edge of the output voltage waveform begins to rise and may end when the leading edge of the output voltage waveform exceeds a predetermined maximum threshold.

[0009] In some embodiments, the method includes, for a current bleed-on timing cycle, starting the current bleed-on timing cycle upon sensing that the trailing edge of the output voltage  
25 waveform falls below the predetermined threshold, determining how much time has elapsed since the trailing edge of the output voltage waveform previously fell below the predetermined threshold, and ending the current bleed-on timing cycle after that determined time interval has elapsed. In one embodiment, the method further includes (i) for an initial bleed-off timing cycle, starting the bleed-off timing cycle upon sensing that the leading edge of the output voltage waveform has begun to  
30 rise; ending the bleed-off timing cycle upon sensing that the leading edge of the output voltage waveform exceeds the predetermined maximum threshold; and determining the time interval between the starting and ending of the bleed-off timing cycle; and (ii) for a subsequent bleed-off timing cycle, starting the bleed-off timing cycle upon sensing that the leading edge of the output voltage waveform has begun to rise; and ending the bleed-off timing cycle after the determined  
35 time interval has elapsed. The method may include, for multiple subsequent bleed-on timing cycles, omitting the determining step and ending a subsequent bleed-on timing cycle after the time interval has elapsed. In one implementation, the method includes repeating step (ii) for multiple subsequent bleed-off timing cycles.

5 [0010] In one embodiment, the bleed-on and bleed-off timing cycles are determined based on a period of the output voltage waveform. For example, the bleed-on timing cycle and the bleed-off timing cycle are adjusted until the period of the monitored output voltage waveform is equivalent to a predetermined period of the output voltage waveform generated by the transformer.

[0011] In another aspect, the invention relates to a system for minimizing flicker in a circuit  
10 having an LED lamp and an electronic transformer. In various embodiments, the system includes a bleed circuit, an analyzer for monitoring an output voltage waveform of the transformer and determining a bleed-on timing cycle and a bleed-off timing cycle based at least in part on the monitored output voltage waveform, a memory for storing a duration of the bleed-on timing cycle and a duration of the bleed-off timing cycle, and control circuitry for engaging the bleed circuit to  
15 the transformer during only a portion of a period of the output voltage waveform and based at least in part on the stored durations. The bleed circuit ensures a minimum load on the transformer sufficient to avoid cut-off.

[0012] The bleed circuit may include a resistor and/or a transistor. The analyzer may be configured to start a bleed-on timing cycle when the trailing edge of the output voltage waveform  
20 falls below a predetermined threshold and to end the bleed-on timing cycle when a subsequent trailing edge of the output voltage waveform falls below a predetermined minimum threshold. Additionally, the analyzer may be configured to start a bleed-off timing cycle when the leading edge of the output voltage waveform begins to rise and to end the bleed-off timing cycle when the leading edge of the output voltage waveform exceeds a predetermined maximum threshold. In  
25 some embodiments, the control circuit is configured to engage the bleed circuit at the start of a bleed-on timing cycle and to disengage the bleed circuit at the end of a bleed-off timing cycle.

[0013] As used herein, the term "approximately" means  $\pm 10^\circ$ , and in some embodiments,  $\pm 5^\circ$ . Reference throughout this specification to "one example," "an example," "one embodiment," or "an embodiment" means that a particular feature, structure, or characteristic described in connection  
30 with the example is included in at least one example of the present technology. Thus, the occurrences of the phrases "in one example," "in an example," "one embodiment," or "an embodiment" in various places throughout this specification are not necessarily all referring to the same example. Furthermore, the particular features, structures, routines, steps, or characteristics may be combined in any suitable manner in one or more examples of the technology. The headings  
35 provided herein are for convenience only and are not intended to limit or interpret the scope or meaning of the claimed technology.

BRIEF DESCRIPTION OF THE DRAWINGS

- 5 [0014] In the drawings, like reference characters generally refer to the same parts throughout the different views. Also, the drawings are not necessarily to scale, with an emphasis instead generally being placed upon illustrating the principles of the invention. In the following description, various embodiments of the present invention are described with reference to the following drawings, in which:
- 10 [0015] FIG. 1A schematically illustrates an LED lighting system in accordance with an embodiment of the present invention;
- [0016] FIG. 1B schematically depicts a bleed circuit in accordance with an embodiment of the present invention;
- [0017] FIG. 2A is a voltage waveform at the output of an electronic transformer;
- 15 [0018] FIG. 2B schematically illustrates an LED lighting system including a rectifier/filter and a dimmer circuit in accordance with an embodiment of the present invention;
- [0019] FIG. 2C is a rectified voltage waveform at the output of an electronic transformer;
- [0020] FIG. 2D is a chopped voltage waveform at the output of an electronic transformer using a dimmer circuit;
- 20 [0021] FIG. 3A depicts a setup of a bleed-on timer in accordance with an embodiment of the present invention;
- [0022] FIG. 3B schematically illustrates an LED lighting system including a bleed-on timer and a bleed-off timer in accordance with an embodiment of the present invention;
- [0023] FIG. 3C depicts a setup of a bleed-off timer in accordance with an embodiment of the present invention;
- 25 [0024] FIG. 4A depicts an output waveform of an electronic transformer having a long trailing edge;
- [0025] FIG. 4B depicts an output waveform of an electronic transformer using a dimmer circuit;
- 30 [0026] FIG. 4C depicts an output waveform of an electronic transformer having an improperly loaded bleed circuit; and
- [0027] FIG. 4D depicts an output waveform of an electronic transformer having a properly operated bleed circuit.

DETAILED DESCRIPTION

5 [0028] Described herein are various embodiments of energy-efficient methods and systems for preventing flickering due to violation of a minimum resistive load requirement. FIG. 1A illustrates an LED lamp 110 powered by a conventional electronic transformer 120 and incorporating a bleed circuit 130. Referring to FIG. 1B, in one embodiment, the bleed circuit 130 includes a low-impedance load, for example, a resistor 132 and/or a transistor 134 configured to ensure that the  
10 minimum load requirement for operating the electronic transformer 120 remains satisfied. The output voltage waveform of the electronic transformer 120 is monitored using an analyzer, a comparator, and/or other conventional monitoring circuitry 140 (hereafter referred to simply as an "analyzer"). The analyzer 140 selectively connects the bleed circuit 130 to, and disconnects it from, the electronic transformer 120 based on the monitored waveform. The analyzer 140 may be a  
15 portion of (or implemented using) a processor, e.g., a microprocessor, microcontroller, application-specific integrated circuit (ASIC), field-programmable gate array (FPGA), or any other type of digital-logic or mixed-signal circuit.

[0029] Referring to FIG. 2A, the electronic transformer is a complex electrical circuit that produces a waveform 210 having a high-frequency (i.e., 10 kHz or greater) AC voltage 212 with an  
20 "envelope" 214 approximating a 60 Hz sinusoidal waveform (similar to the frequency produced by a magnetic transformer). Referring to FIG. 2B, in one embodiment, the AC output voltage 210 of the electronic transformer 216 is rectified and typically filtered using a conventional rectifier/filter circuit 218 including, for example, a diode bridge and a filter capacitor. FIG. 2C depicts the rectified and filtered output voltage waveform 220, which is indicated by the dashed envelope. In  
25 one embodiment, the LED lamp system includes a dimmer circuit 222 that operates by phase dimming, in which a portion of a dimmer-input waveform (i.e. the output waveform of the rectified and filtered voltage 220) is cut off or "chopped" to produce a temporally clipped version of the waveform. FIG. 2D illustrates the waveform 220 that results from chopping the output waveform at a trailing-edge point 226. The duration (i.e., duty cycle) of the clipping corresponds to the level  
30 of dimming desired - more clipping produces a dimmer light. In some embodiments, the maximum current passing through the LED lamp 228 is reduced, based on, for example, the phase angle (i.e., dimming level) of the dimmer circuit, to achieve a desired dimming. Accordingly, unlike the dimmer circuit for an incandescent light, where the clipped input waveform directly supplies power to the lamp (with the degree of clipping determining the amount of power supplied and, hence, the  
35 lamp's brightness), in the LED lamp 228 the received input waveform 220 is analyzed by the analyzer 230 to infer the dimmer setting and, based thereon, the bleed circuit 232 operates to ensure that the minimum load requirement of the electronic transformer 216 is constantly satisfied.

5 [0030] FIG. 3A illustrates a representative embodiment in which the rectified and filtered output waveform 220 of the electronic transformer 216 is monitored by the analyzer 230 continuously. In the illustrated embodiment, the analyzer 230 (which may be implemented in or by using a processor, as noted above) samples the voltage of the output waveform 310. When the trailing edge 312 of the output voltage waveform 310 falls below a predetermined minimum  
10 threshold (or failing point, e.g., 10% of the peak output voltage) at a time 314, a bleed-on cycle is started, and the bleed-on timer 316 is automatically initiated; the bleed circuit 318 is applied to the electronic transformer 320 as depicted in FIG. 3B. The end of the bleed-on cycle occurs just prior to the next failing point 322 of the next period of the output waveform 324, and the timer 316 has expired. As a result, the bleed-on cycle time 326 is substantially equivalent to the period of the  
15 output voltage waveform 310, and is stored in a non-volatile memory 337 operatively connected to (and located inside or external to) the analyzer 338. Alternatively, the active period of the bleed-on timer may be empirically determined based on the load of the LED lamp and the minimum load requirements of the majority of the electronic transformers intended for use therewith. Again, this active period is stored in the memory 337.

20 [0031] Referring to FIG. 3C, when the output voltage starts to rise (i.e., at the time 328 of the leading edge 330), the bleed-off timer 332 is initiated. The end of the bleed-off cycle occurs when, for example, the leading edge 330 of the output voltage waveform 324 exceeds a predetermined maximum threshold (e.g., 50% of the peak output voltage); at this point the bleed off timer 332 is turned off. In one embodiment, the bleed-off cycle time recorded by the timer 332 is stored in the  
25 memory 337.

[0032] Control logic, implemented in the bleed circuit 318 or the analyzer 338, cause the bleed circuit 318 to turn on and turn off in accordance with the stored bleed on time 326 and/or bleed off time, 336; that is these cycle times may be used to engage and disengage the bleed circuit 318 to and from the electronic transformer 320. For example, the bleed circuit 318 may be engaged at the  
30 beginning 314 of the bleed-on cycle (FIG. 3A) and turned off at the end 334 of the bleed-off cycle (FIG. 3C). During most of the positive portion of the output waveform, therefore, the bleed circuit 318 is inactive. Applications of the bleed-on and bleed-off times based on the monitored output voltage waveform and predetermined maximum and minimum thresholds therefore prevents wasting energy while keeping the minimum load requirement satisfied. In one embodiment, the  
35 bleed-on time and/or bleed-off time (particularly the bleed-off time) is consistent over time for devices of a certain type, and therefore can be stored in memory and retrieved to control the application of the bleed circuit to the transformer during the operation of the LED lamp. For example, the bleed-on time and/or bleed-off time may be reset only when the output waveform of



5 the transformer is changed, as further described below. In other embodiments, the bleed-on and/or bleed-off times may be re-evaluated periodically and changed as necessary.

[0033] Typically, the onset of a bleed-on cycle and a bleed-off cycle are triggered by conditions sensed by the analyzer 338 — i.e., when the output voltage starts to rise (in the case of the bleed-off cycle) and when the output voltage falls below the predetermined minimum threshold after rising  
10 (in the case of the bleed-on cycle). The stored interval values may be used to establish when each cycle ends (which, in the case of the bleed-on cycle, also marks the beginning of the next cycle). Because electronic transformers can behave erratically even during continuous operation, it may be useful to sense, every few cycles or even every cycle, the conditions corresponding both to the beginning and end of each cycle, and to update the stored cycle times accordingly. Even if these  
15 values are updated every cycle, a benefit is obtained in that the stored value from the previous cycle is used for the current cycle, obviating the need to predicate both the beginning and ending of a cycle on sensed conditions, since sensing can introduce delay. Updated values will be used predictively on the next cycle and will never be more out of date than the reciprocal of the update frequency. The update frequency will be adequate so long as perceptible flicker is avoided. Often  
20 it is found necessary to adjust the bleed-off time less frequently than the bleed-on time, and indeed, the bleed-off time may often be set a single time (e.g., when the LED is first powered on) without updating.

[0034] The stored bleed-on and/or bleed-off times 326, 336 thereby facilitate predictive operation of the bleed circuit 318 during operation of the LED lamp. For example, the bleed circuit  
25 can be automatically applied to the electronic transformer upon the next bleed-on start time; the bleed circuit then stays connected until the next bleed-off stop time. In addition, during a later use of the LED lamp, if the circuitry of the electronic transformer 320 and the dimmer circuit 340 is unchanged, the bleed-on and/or bleed-off times stored in non-volatile memory can be retrieved and directly applied to the LED lamp when the LED lamp is turned on, and the transformer output  
30 waveform monitored as necessitated by variability in its output. Even with a relatively stable output, the full output voltage waveform may be monitored on a periodic basis, such as after every ten-hour use of the LED lamp; this ensures proper performance of the bleed circuit even with aging components or changing environmental conditions. Because the bleed circuit is switched in for only a portion of the output cycle, energy waste and the generation of excessive heat is avoided. In  
35 addition, the bleed-on and/or bleed-off timers allows the bleed circuit to be applied immediately after the LED lamp is turned on without time delays caused by analyzing the output voltage waveform; this avoids the initial flickering that is generally present in conventional LED lamps.

5 [0035] In some embodiments, when the transformer 320 and/or the dimmer 340 is changed, the bleed-on and/or bleed-off times may be reset by choosing a previously stored value from various dimming modes or by pressing and holding a "mode" button for a predetermined time, for example, 5 seconds. Upon receiving this signal, the analyzer 338 monitors the rectified and filtered output waveform of the dimmer circuit 340 and again determines the bleed-on and bleed-off times using  
10 the approach as described above.

[0036] It should be noted that the bleed-on and bleed-off timers need not be separate components, but instead can be the same timer configured to signal the occurrence (e.g., by issuing a pulse) of a relevant state — e.g., the start and/or end of a timing cycle. Thus, the bleed-on start and stop times may be stored in memory, and the timer configured to issue a pulse upon the  
15 occurrence of the bleed-on start time (i.e., the beginning of the bleed-on timing cycle in order to cause control logic to engage the bleed circuit) and the bleed-off time (i.e., the end of the bleed-off timing cycle in order to cause control logic to disengage the bleed circuit). The timer(s) can also be configured as a finite-state machine.

[0037] In one embodiment, the bleed-on and bleed-off timers utilize a system clock  
20 implemented in the analyzer 338. The bleed-on and bleed-off times are computed based on the period  $T_{clk}$  of the system clock and a count value generated using, for example, a digital counter. For example, in a typical implementation, the count values for the bleed-on and bleed-off times may be roughly  $5 \times 10^5$  and  $10^5$  cycles, respectively; assuming that the frequency of the analyzer is 100 MHz (i.e.,  $T_{clk} = 10$  ns), the bleed-on and bleed-off times are roughly 5 ms and 1 ms,  
25 respectively, and, as shown in FIGS. 3A and 3C, the bleed circuit will be active for slightly more than half the duty cycle. Utilization of the system clock thus conveniently and accurately records the bleed-on and bleed-off times without extra circuitry. In another embodiment, the bleed-on and bleed-off timers are external timers connected to the analyzer 338; they can be easily removed once the bleed-on and bleed-off times have been determined and stored in the memory.

30 [0038] In one embodiment, the bleed circuit is continuously connected to the electronic transformer 320 when the LED lamp is operated in the minimum dimming situation (i.e., the current drawn from the LEDs is at the minimum value and thus unable to keep the electronic transformer "awake"). For example, if the voltage waveform is detected to be chopped at a phase angle smaller than approximately 60 degrees or the LED operating time is less than approximately  
35 3 ms, this indicates that the LED lamp is in the minimum dimming situation and the bleed circuit 318 should be constantly applied to the electronic transformer 320 to satisfy the minimum load requirement. Additionally, this ensures that the phase angle of the detected voltage waveform is "locked in" at a limited value, thereby limiting the power dissipation in the bleed circuit 318 until

5 such time as enough power is applied to the LEDs to facilitate switching the bleed circuit 318 in and out of the LED lamp circuit.

[0039] Additionally, the LED lamp may be connected to electronic transformers 320 having various output frequencies. In one embodiment, the LED lamp is powered by an electronic transformer 320 having an output frequency of, for example, 50 Hz. The period of the transformer  
10 output waveform may be measured using the same edge-detection approach as described above for determining the bleed-on and/or bleed-off times of the bleed circuit 318. The bleed-on and/or bleed-off times applied to the bleed circuit 318 can be then adjusted to ensure that the transformer functions properly at its operating frequency (i.e., 50 Hz). At a later time, if the LED lamp is powered by another transformer with a different output frequency (e.g., 60 Hz), the bleed-on and/or  
15 bleed-off times applied to the bleed circuit 318 can, again, be adjusted so that it functions properly at the new output frequency.

[0040] In some embodiments, a different electronic transformer may replace the original transformer during the lifetime of an LED lamp lifetime. The new transformer may have the same output voltage waveform as the replaced transformer, or a different one. In some embodiments,  
20 when a new transformer is employed in the LED lamp circuit, the bleed circuit is applied based on the bleed-on and/or bleed-off times stored in memory and the period of the output waveform is measured through edge detection of the leading and trailing edges of the electronic transformer. If the period is approximately (e.g., within 10%) 8.33 ms (derived from a rectified 60 Hz AC mains supply voltage), the bleed circuit will operate properly with the new transformer. If, however, the  
25 period differs significantly from the expected value (i.e., more or less than 10% of 8.33 ms), the bleed-on and/or bleed-off times applied to the bleed circuit are adjusted, using the initialization procedure discussed above, to avoid flickering.

[0041] Referring to FIG. 4A, some electronic transformers may generate an output voltage waveform having a longer trailing edge 410. When a dimmer circuit is applied to the LED lamp  
30 utilizing such a transformer, a large portion of the trailing edge 410 is chopped, as depicted in FIG. 4B. Because the chopped trailing edge 412 may have a cut-off waveform at an early time 414 of the trailing edge, the bleed-on timer may be reset to start at an earlier time to ensure the proper operation of the bleed circuit. Note that because the leading edge is not affected by an application of this dimmer circuit, the bleed-off timer settings remains the same. If, however, the dimmer  
35 circuit chops the output voltage waveform at the leading edge rather than the trailing edge, the setting of the bleed-on timer may be changed while that of the bleed-off timer remains the same. Adjusting the bleed-on timer based on the output voltage waveform is important every time a new electronic transformer is used in the LED lamp. For example, FIG. 4C depicts an output voltage

5 waveform during an improper application of the bleed circuit to the electronic transformer. Waveforms 418, 420 are incomplete, containing small pulses 422, 424; this places the electronic transformer into sleep mode and, consequently, causes flickering of the LED lamp. FIG. 4D illustrates an approach to solving this problem, namely, applying the bleed circuit to the transformer at an earlier time (in this case, approximately  $64 \mu\text{s}$  earlier) in the trailing edge 426 of  
10 the output voltage waveform as indicated by the arrows 428. After each adjustment, the period of the waveform is measured again. If approximately  $8.33 \text{ ms}$  is obtained, the bleed circuit is appropriately applied, and the new bleed-on and/or bleed-off times is then set and stored in non-volatile memory. However, if the period still falls outside the  $\pm 10\%$  window, the bleed circuit is applied an additional  $64 \mu\text{s}$  earlier. This procedure may be iteratively implemented until the  
15 measured period is approximate  $8.3 \text{ ms}$ , as shown in FIG. 4B. In one embodiment, a total increase of thirty-two steps, each increasing the bleed-on time by  $64 \mu\text{s}$  (or  $2 \text{ ms}$  in total), is set as an adjustment limit so as to avoid excessive power dissipation in the bleed circuit. The properly adjusted bleed-on and bleed-off times, again, can be stored in non-volatile memory 337 for later retrieval. Use of the stored data may thus prevent the LED lamp from flickering at the beginning of  
20 the next operation. Although the discussion herein focuses on transformers having a frequency of  $60 \text{ Hz}$ , the present invention is not limited to transformers having an output voltage of a particular frequency. In addition, the incremental application time for adjusting the bleed-on timer is not limited to  $64 \mu\text{s}$  and the number of steps are not limited to thirty-two steps, any suitable incremental time and/or steps may be applied to the bleed circuit and are within the scope of the current  
25 invention.

[0042] In various embodiments, the analyzer is provided as either software, hardware, or some combination thereof. For example, the system may be implemented on one or more processors such as application specific integrated circuits (ASIC), field programmable gate arrays (FPGA), or programmable logic devices (PLD). The analyzer may also include a main memory unit for storing  
30 programs and/or data relating to the activation or deactivation described above. The memory may include random access memory (RAM), read only memory (ROM), and/or FLASH memory residing on commonly available hardware such as one or more of the processors listed above, electrically erasable programmable read-only memories (EEPROM), or programmable read-only memories (PROM).

35 [0043] The terms and expressions employed herein are used as terms and expressions of description and not of limitation, and there is no intention, in the use of such terms and expressions, of excluding any equivalents of the features shown and described or portions thereof. In addition, having described certain embodiments of the invention, it will be apparent to those of ordinary skill

5 in the art that other embodiments incorporating the concepts disclosed herein may be used without departing from the spirit and scope of the invention. Accordingly, the described embodiments are to be considered in all respects as only illustrative and not restrictive.

[0044] What is claimed is:

CLAIMS

- 1 1. A method of operating an LED system including an electronic transformer, the method  
2 comprising:  
3 monitoring an output voltage waveform of the transformer;  
4 starting a bleed-on timing cycle upon sensing a first voltage-waveform condition and ending  
5 the bleed-on timing cycle after the determined bleed-on time has elapsed;  
6 starting a bleed-off timing cycle upon sensing a second voltage-waveform condition  
7 different from the first voltage-waveform condition, and ending the bleed-off timing cycle after the  
8 determined bleed-off time has elapsed; and  
9 engaging a bleed circuit to the transformer based on a state of the bleed-on timing cycle and  
10 disengaging the bleed circuit from the transformer based on a state of the bleed-off timing cycle,  
11 the bleed circuit being engaged for a duration adequate to ensure a minimum load on the  
12 transformer sufficient to avoid cut-off throughout the output voltage waveform.
- 1 2. The method of claim 1, wherein the state of the bleed-on timing cycle a start thereof and the  
2 state of the bleed-off timing cycle is an end thereof.
- 1 3. The method of claim 1, wherein the bleed-on timing cycle starts when a trailing edge of the  
2 output voltage waveform falls below a predetermined threshold and ends when a subsequent  
3 trailing edge of the output voltage waveform falls below a predetermined minimum threshold.
- 1 4. The method of claim 1, wherein the bleed-off timing cycle starts when a leading edge of the  
2 output voltage waveform begins to rise and ends when the leading edge of the output voltage  
3 waveform exceeds a predetermined maximum threshold.
- 1 5. The method of claim 3, further comprising, for a current bleed-on timing cycle:  
2 starting the current bleed-on timing cycle upon sensing that the trailing edge of the output  
3 voltage waveform falls below the predetermined threshold;  
4 determining a time interval since the trailing edge of the output voltage waveform  
5 previously fell below the predetermined threshold; and  
6 ending the current bleed-on timing cycle after the time interval has elapsed.
- 1 6. The method of claim 4, further comprising:

- 2 (i) for an initial bleed-off timing cycle:  
3 starting the bleed-off timing cycle upon sensing that the leading edge of the output  
4 voltage waveform has begun to rise;  
5 ending the bleed-off timing cycle upon sensing that the leading edge of the output  
6 voltage waveform exceeds the predetermined maximum threshold; and  
7 determining a time interval between the starting and ending of the bleed-off timing  
8 cycle; and  
9 (ii) for a subsequent bleed-off timing cycle:  
10 starting the bleed-off timing cycle upon sensing that the leading edge of the output  
11 voltage waveform has begun to rise; and  
12 ending the bleed-off timing cycle after the time interval has elapsed.

1 7. The method of claim 5, further comprising, for a plurality of subsequent bleed-on timing  
2 cycles, omitting the determining step and ending a subsequent bleed-on timing cycle after the time  
3 interval has elapsed.

1 8. The method of claim 6, further comprising repeating step (ii) for a plurality of subsequent  
2 bleed-off timing cycles.

1 9. The method of claim 1, wherein the bleed-on and bleed-off timing cycles are determined  
2 based on a period of the output voltage waveform.

1 10. The method of claim 9, wherein the bleed-on timing cycle and the bleed-off timing cycle  
2 are adjusted until the period of the monitored output voltage waveform is equivalent to a  
3 predetermined period of the output voltage waveform generated by the transformer.

1 11. A system for minimizing flicker in a circuit comprising an LED lamp and an electronic  
2 transformer, the system comprising:  
3 a bleed circuit;  
4 an analyzer for monitoring an output voltage waveform of the transformer and determining  
5 a bleed-on timing cycle and a bleed-off timing cycle based at least in part on the monitored output  
6 voltage waveform;  
7 a memory for storing a duration of the bleed-on timing cycle and a duration of the bleed-off  
8 timing cycle; and

9 control circuitry for engaging the bleed circuit to the transformer during only a portion of a  
10 period of the output voltage waveform and based at least in part on the stored durations, the bleed  
11 circuit ensuring a minimum load on the transformer sufficient to avoid cut-off.

1 12. The system of claim 11, wherein the bleed circuit comprises at least one of a resistor or a  
2 transistor.

1 13. The system of claim 11, wherein the analyzer is configured to start a bleed-on timing cycle  
2 when a trailing edge of the output voltage waveform falls below a predetermined threshold and to  
3 end the bleed-on timing cycle when a subsequent trailing edge of the output voltage waveform falls  
4 below a predetermined minimum threshold.

1 14. The system of claim 13, wherein the analyzer is configured to start a bleed-off timing cycle  
2 when a leading edge of the output voltage waveform begins to rise and to end the bleed-off timing  
3 cycle when the leading edge of the output voltage waveform exceeds a predetermined maximum  
4 threshold.

1 15. The system of claim 14, wherein the control circuit is configured to engage the bleed circuit  
2 at a start of a bleed-on timing cycle and to disengage the bleed circuit at an end of a bleed-off  
3 timing cycle.



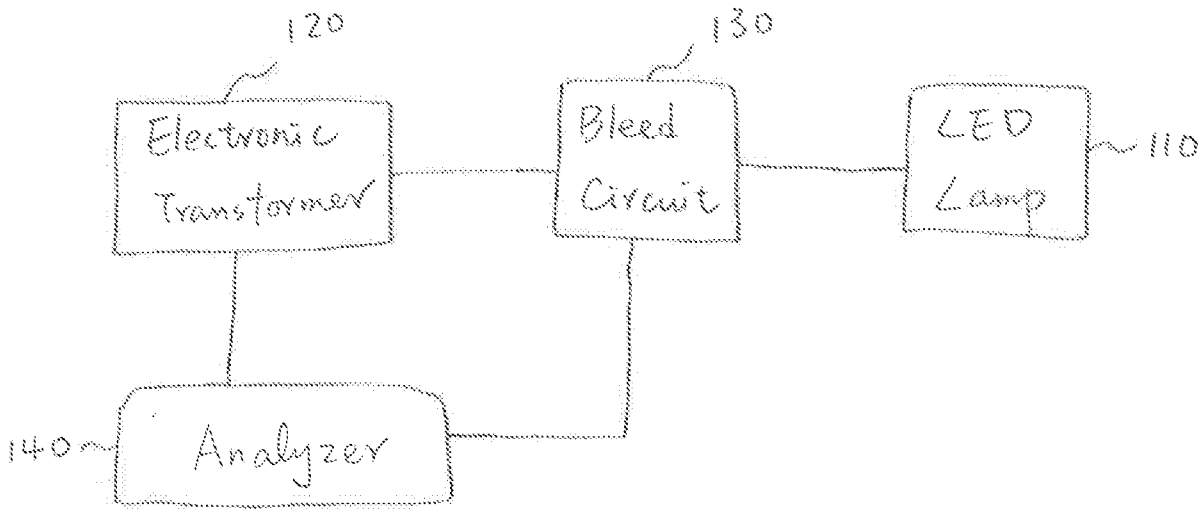


FIG. 1A  
PRIOR ART

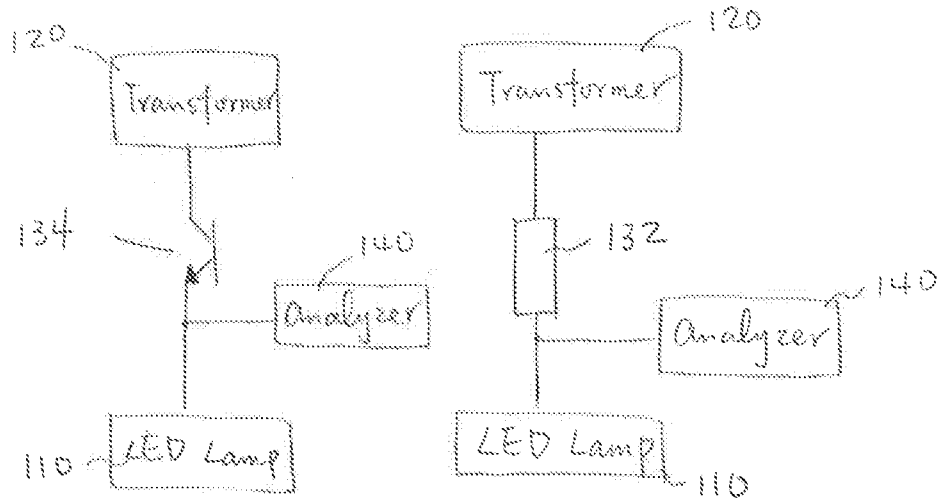


FIG. 1B  
PRIOR ART

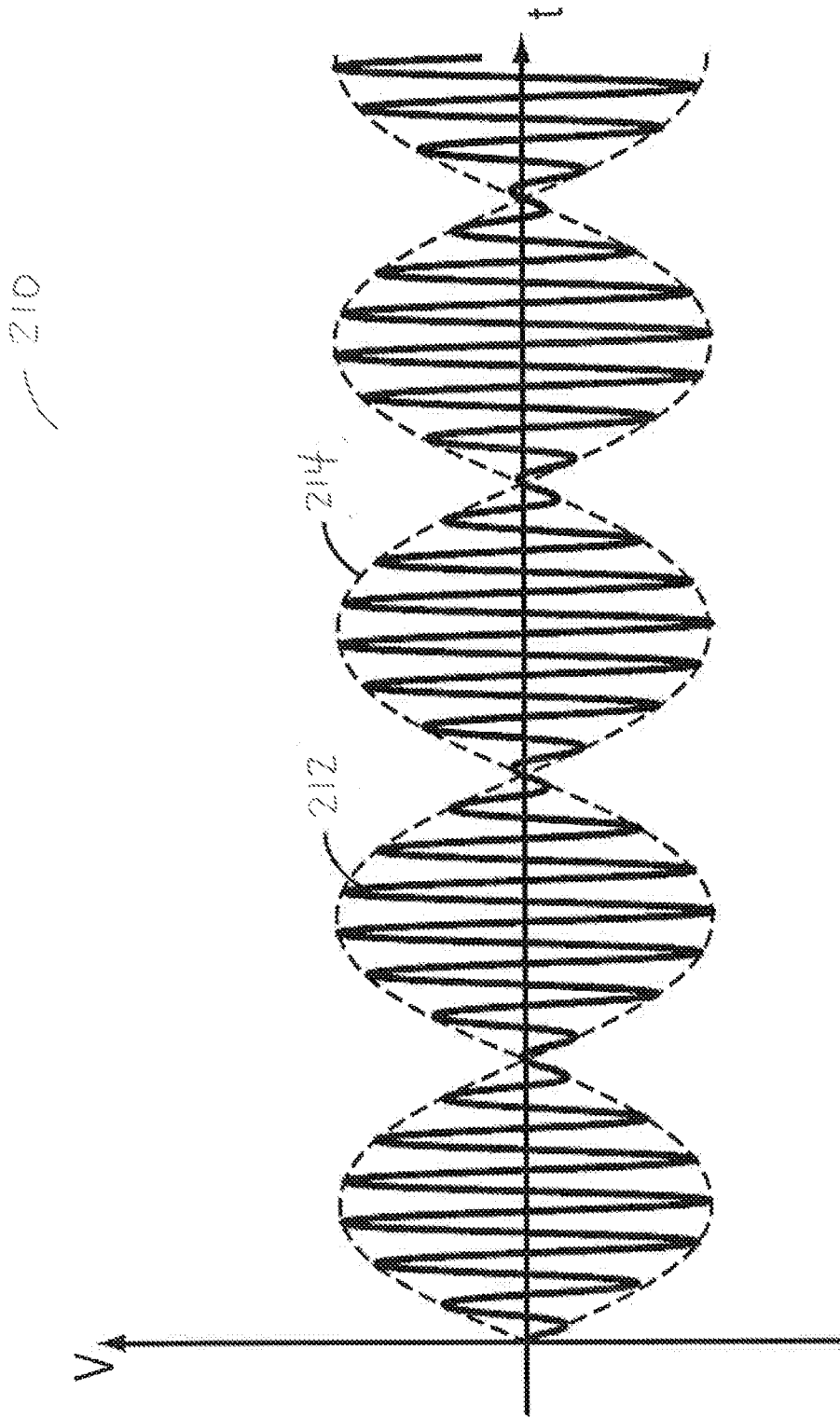


FIG. 2A

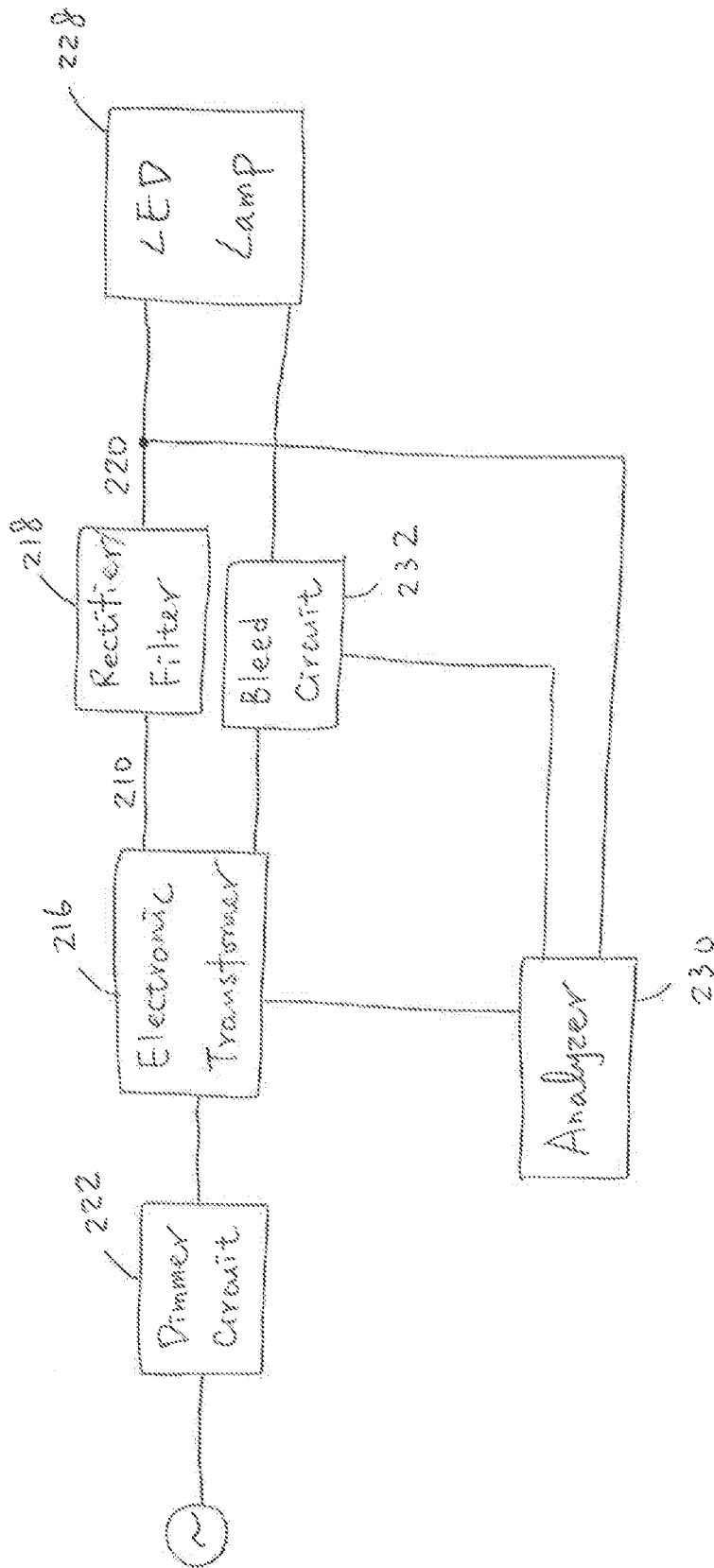


FIG. 2B

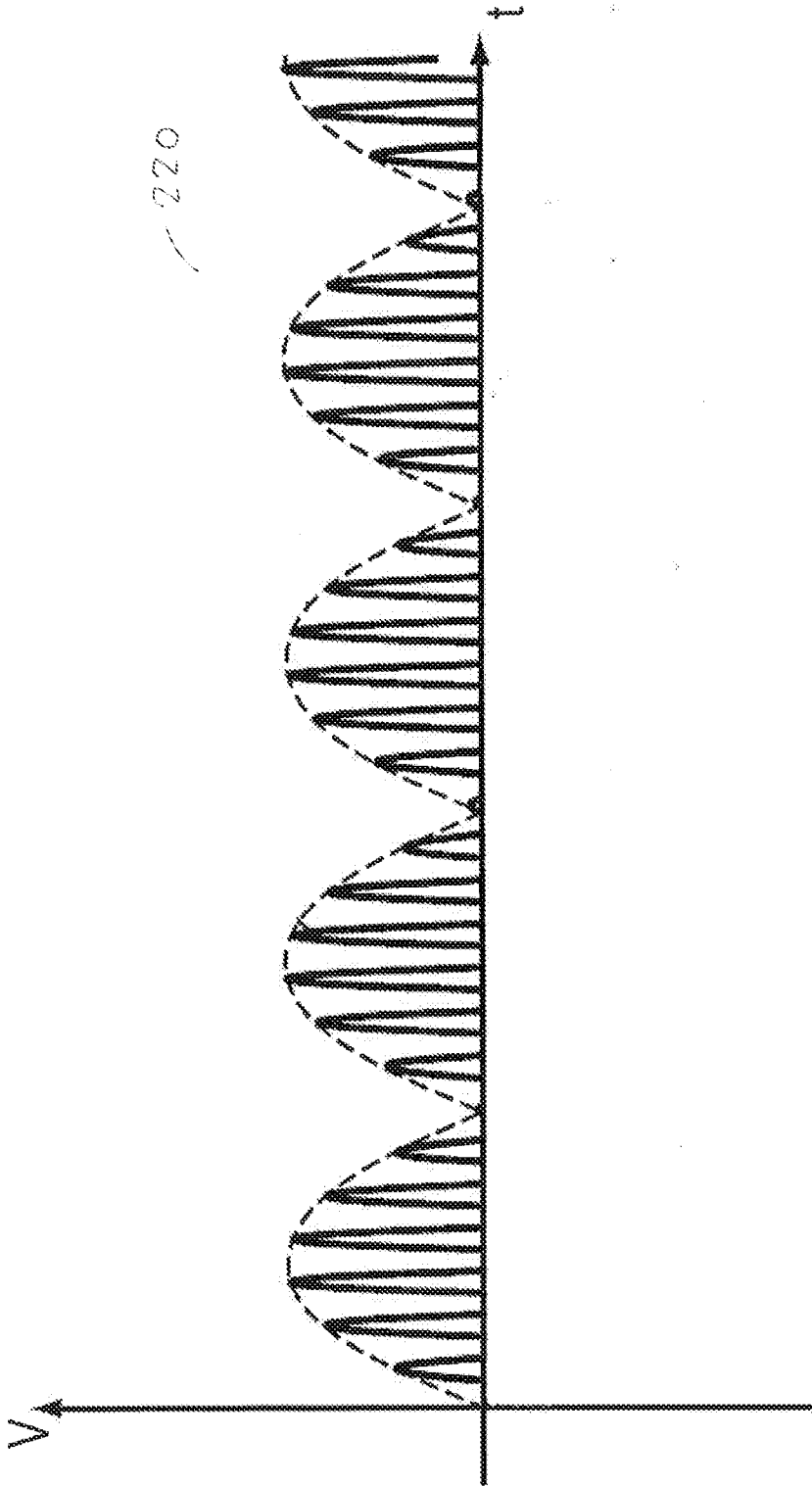


FIG. 2C

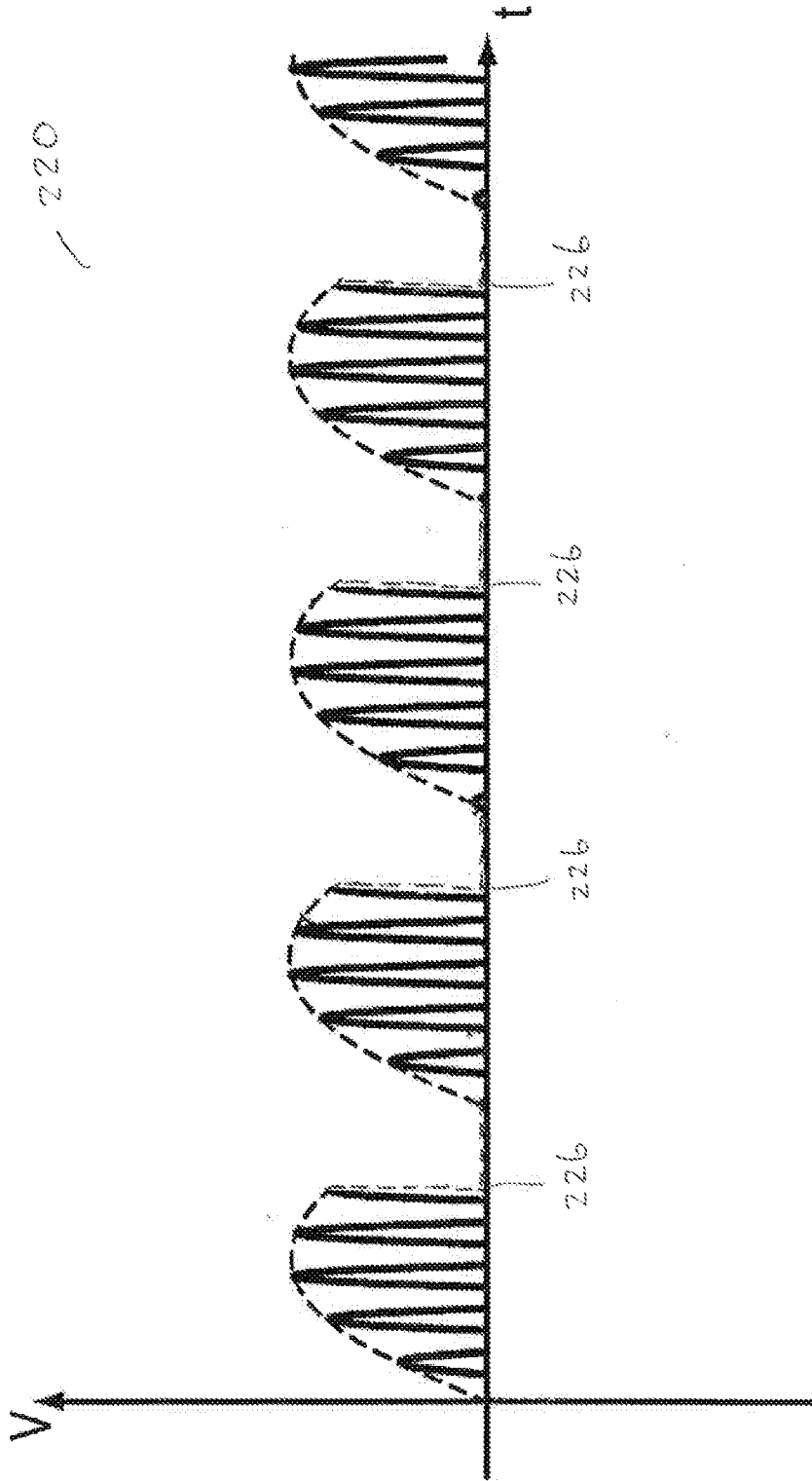


FIG. 2D

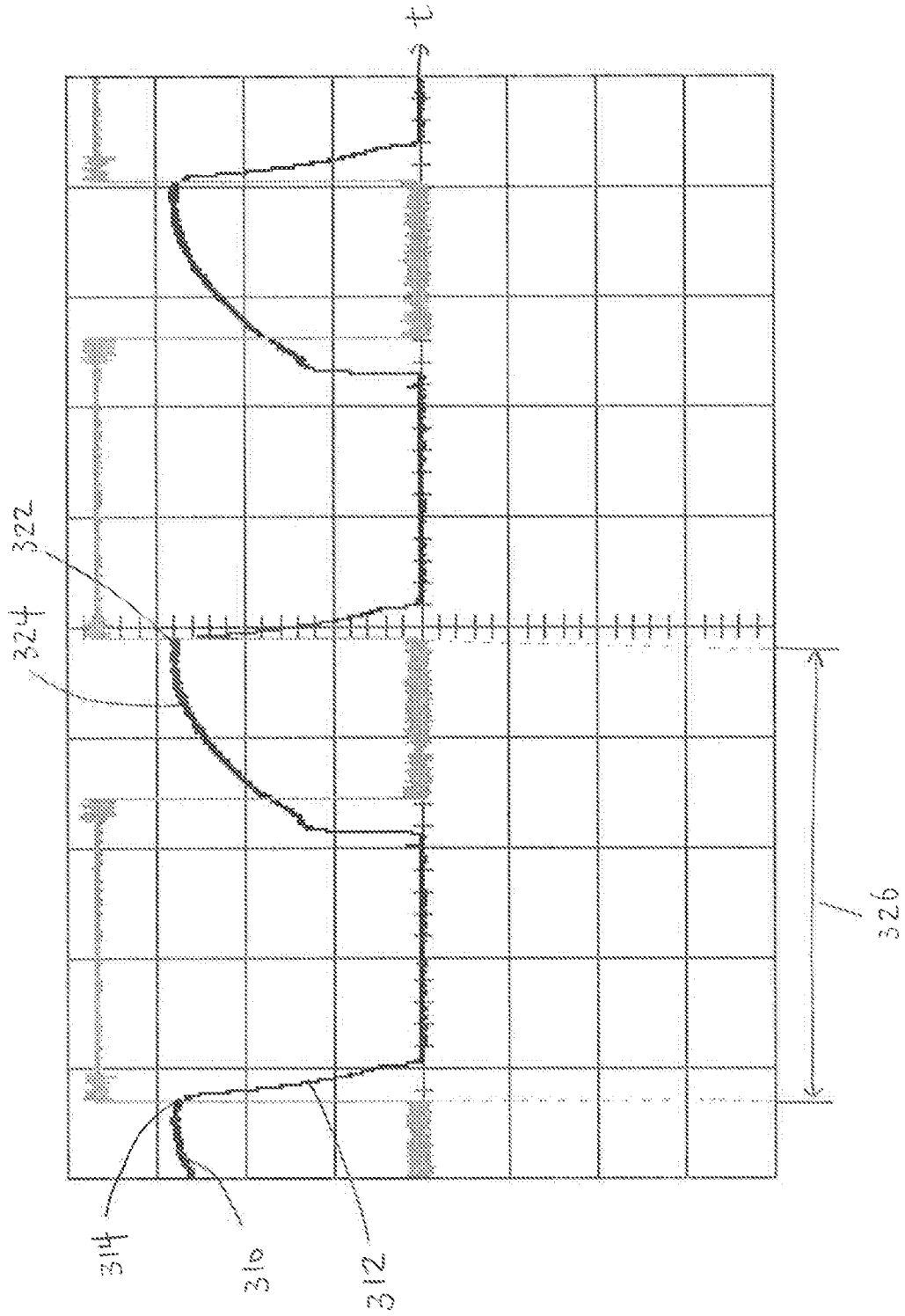


FIG. 3A

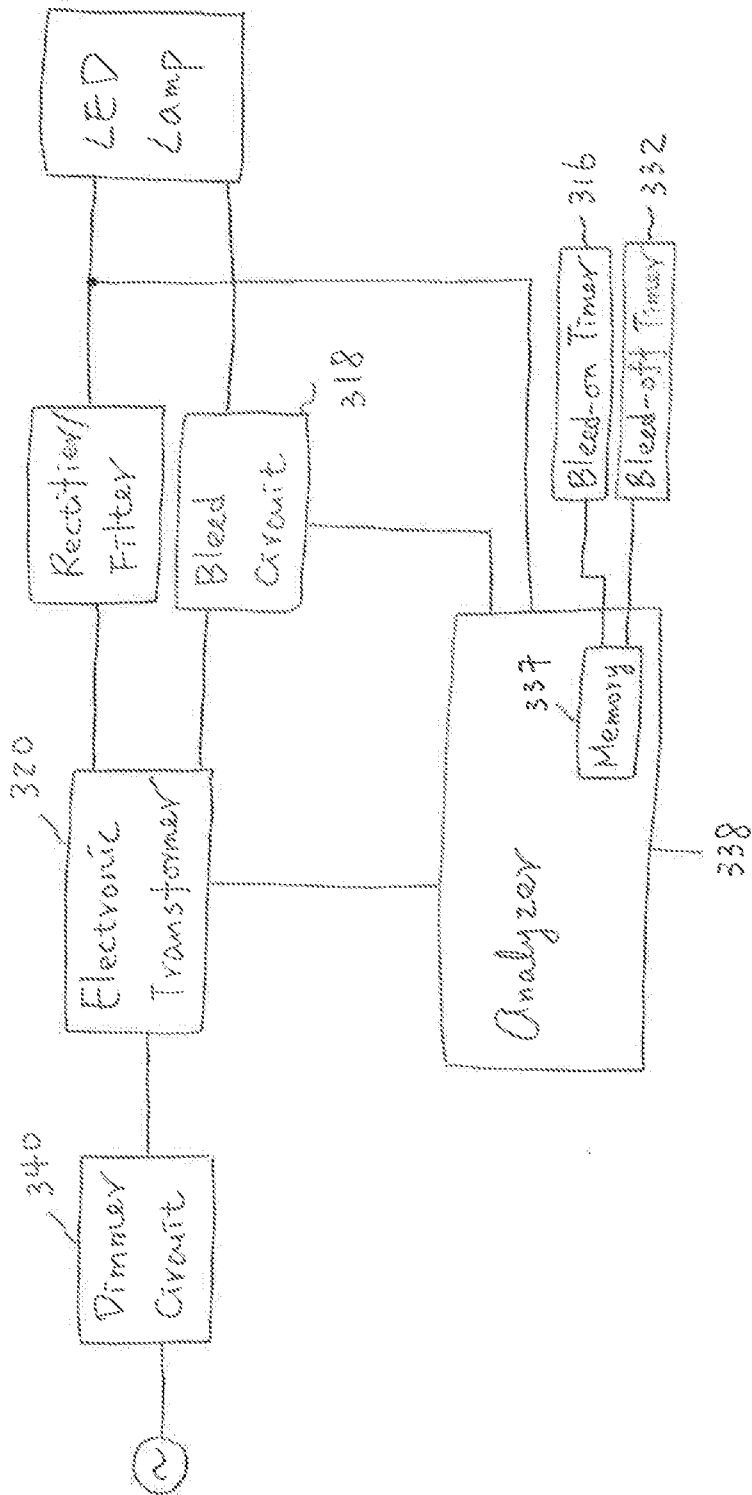


FIG. 3B

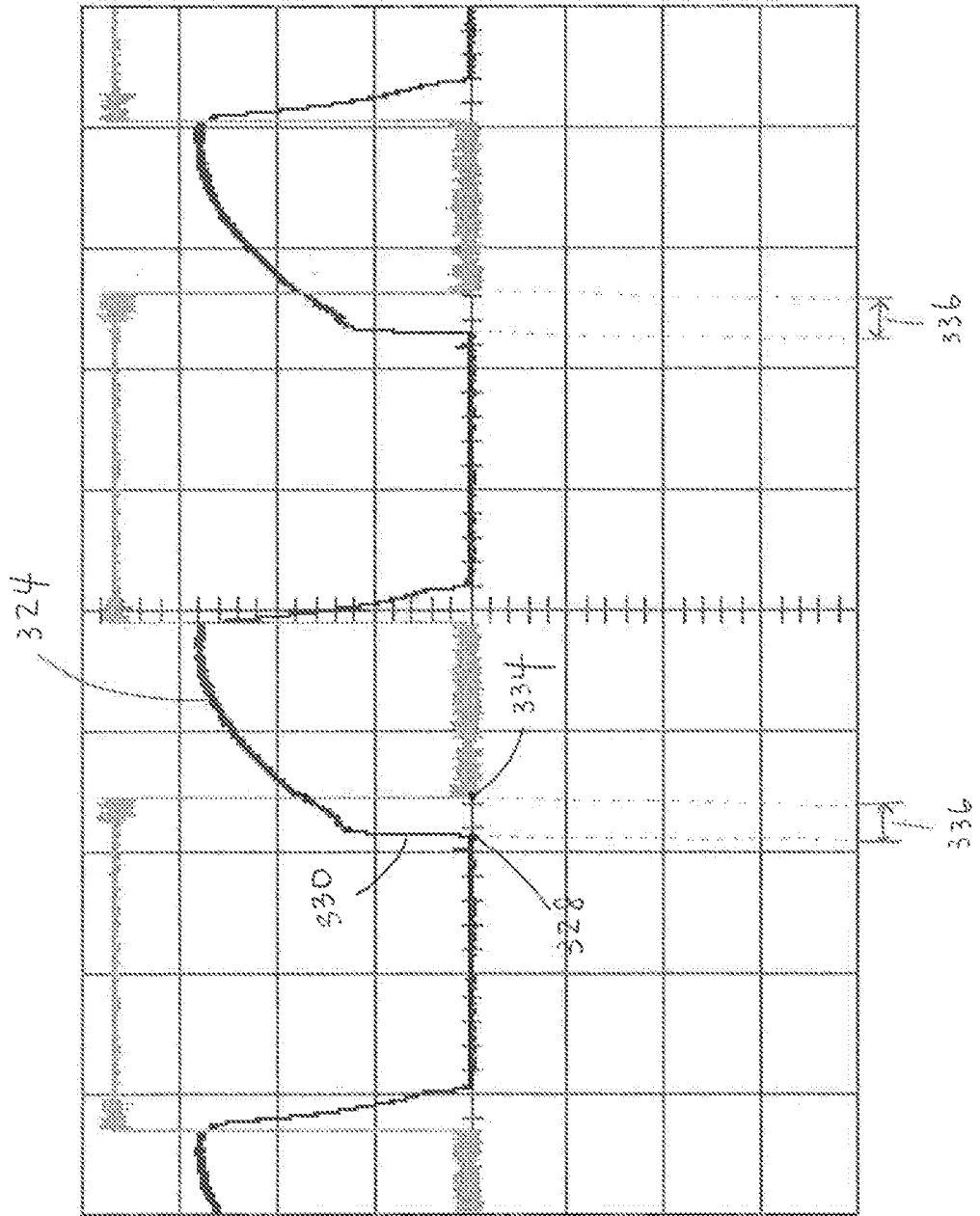


FIG. 3C



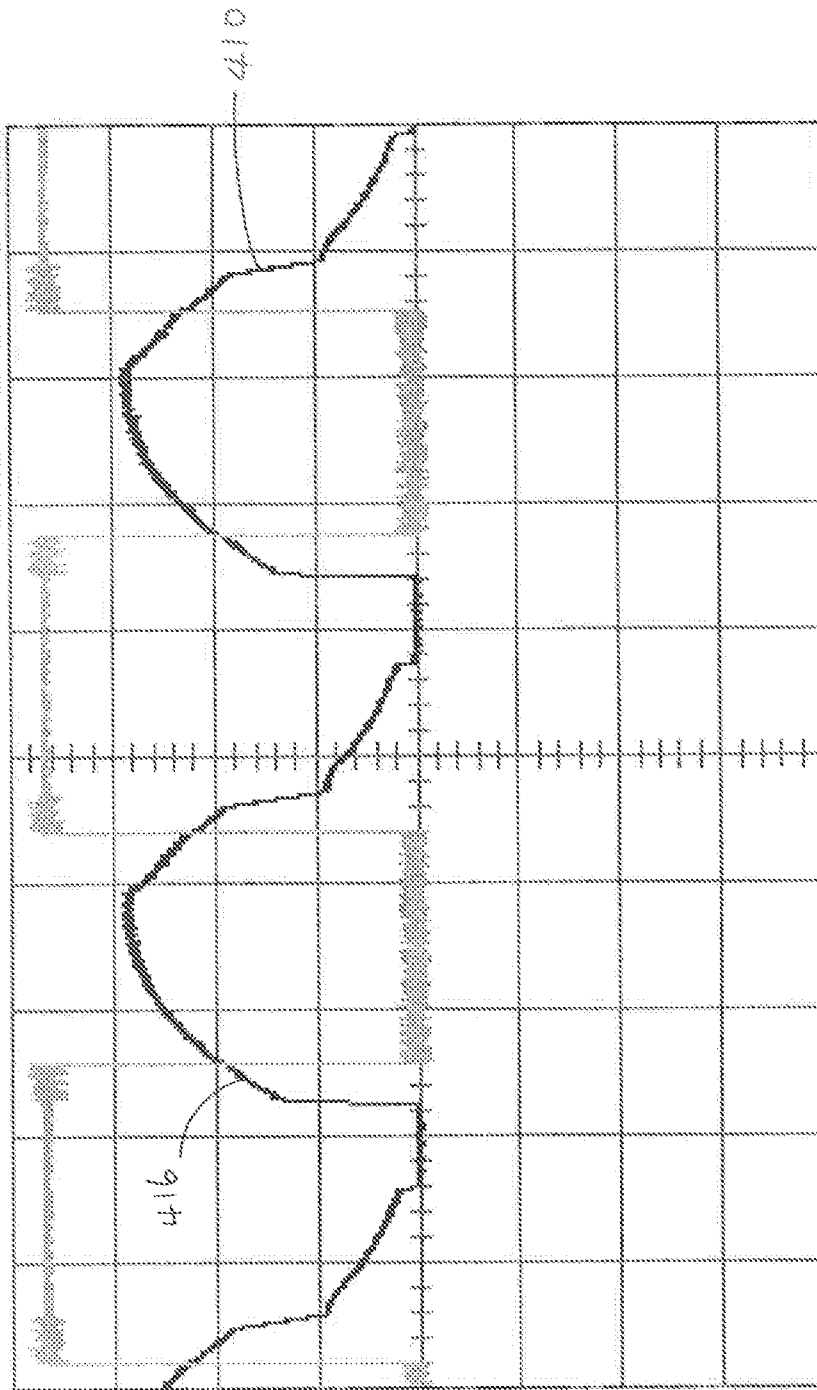


FIG. 4A

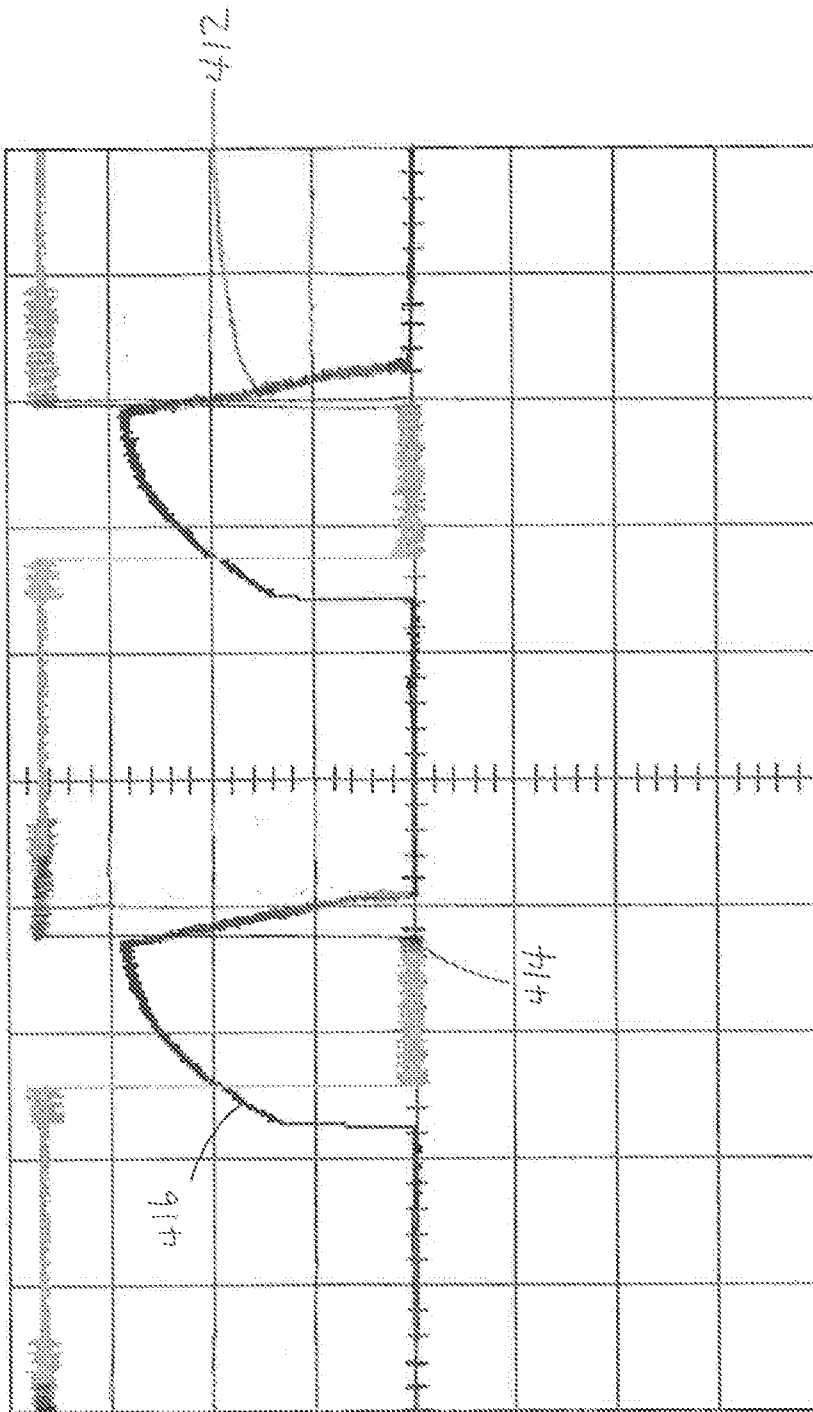


FIG. 4B

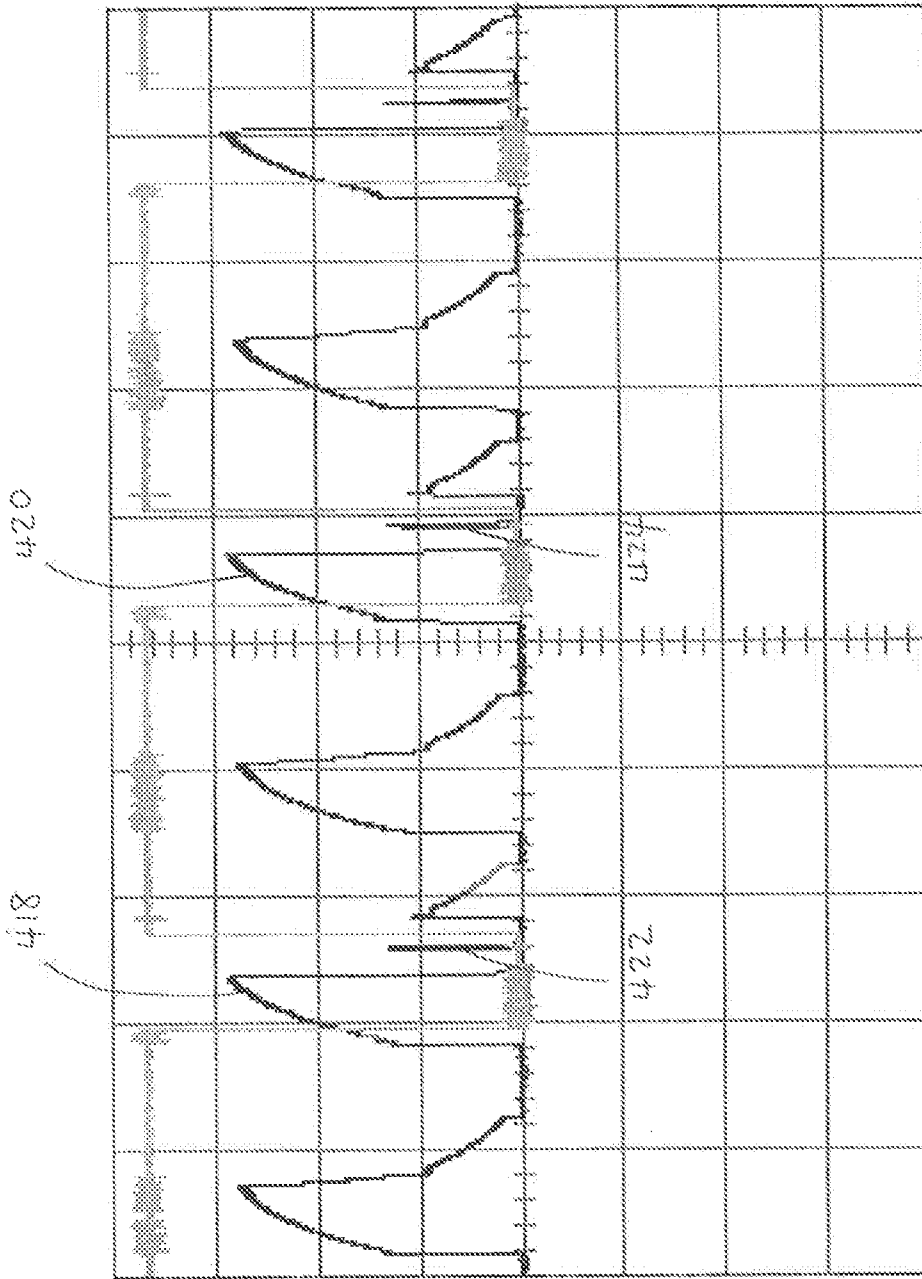


FIG. 4C

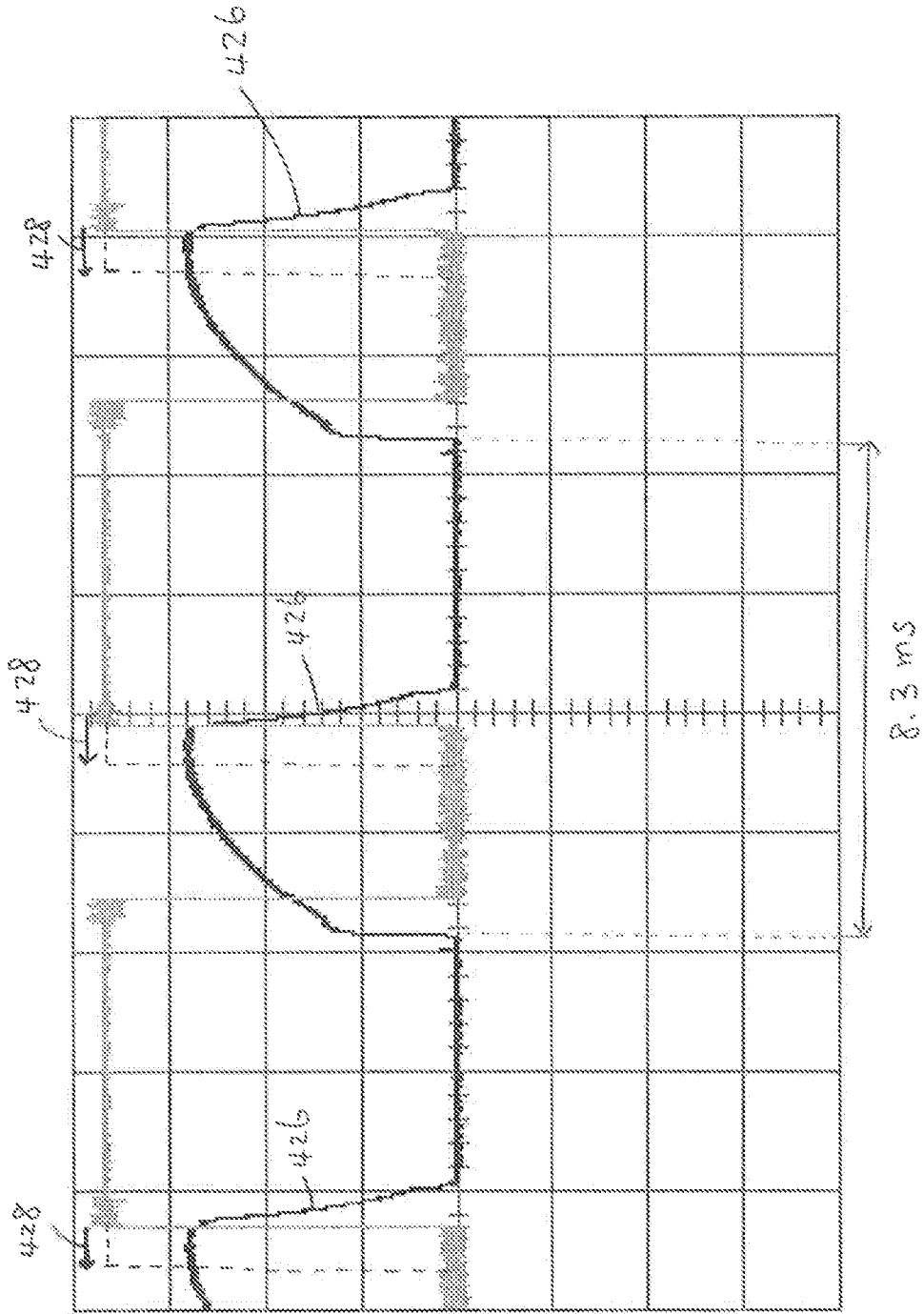


FIG. 4D

# INTERNATIONAL SEARCH REPORT

International application No PCT/US2012/070126
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<b>A. CLASSIFICATION OF SUBJECT MATTER</b> INV. H05B33/08 ADD.		
According to International Patent Classification (IPC) or to both national classification and IPC		
<b>B. FIELDS SEARCHED</b>		
Minimum documentation searched (classification system followed by classification symbols) H05B		
Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched		
Electronic data base consulted during the international search (name of data base and, where practicable, search terms used) EPO-Internal		
<b>C. DOCUMENTS CONSIDERED TO BE RELEVANT</b>		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 2011/115400 A1 (HARRISON DANIEL J [US] ET AL) 19 May 2011 (2011-05-19) paragraphs [0005] - [0007], [0032] - [0059]; figures 3,5-8 -----	1, 2, 9-12
<input type="checkbox"/> Further documents are listed in the continuation of Box C. <input checked="" type="checkbox"/> See patent family annex.		
* Special categories of cited documents :		
"A" document defining the general state of the art which is not considered to be of particular relevance "E" earlier application or patent but published on or after the international filing date "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified) "O" document referring to an oral disclosure, use, exhibition or other means "P" document published prior to the international filing date but later than the priority date claimed	"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention "X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone "Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art "&" document member of the same patent family	
Date of the actual completion of the international search  26 April 2013	Date of mailing of the international search report  06/05/2013	
Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Authorized officer  Waters, Duncan	

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No

PCT/US2012/070126

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