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(73) ( ) 가 78 11

(72) 517-21201

(74)  
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(54) 가

가 (SRAM) ; 1 2  
1 2 , 1 , 1 가 가 ; ;  
2 , 1 , 1 2 가 가 ; 2 ;  
, 1 2 . 1 2 가 가  
, . 가 가

1

- 1 가 , .
- 2 1
- 3 1 1
- 4 1 2
- 5 1



, N4) (WL) (30a) (P1, P2) 1 (BL) (/BL)  
 가 (VCC) (10) (N11) (VCC) (30)  
 1 (N21) (VCC\_INT1) (VCC\_INT1) (30a) (V  
 CC) 1 (VCC\_INT1) (41) (VCC) 1/2  
 2 (20) 2 (N31) 2 (VCC\_INT2) 1  
 (VCC\_INT1) (VCC\_INT2) 1 (41) (51) 1 (41)  
 , (/CS)가 (stand-by) 2 (51)  
 3 1 1 (N21) 2 (N31) (61) 3 1 (41)  
 1 2 (301, 305), 1 2 (303, 307), 1 2 (309, 311)  
 1 (301) 1 (N302) (309) 1 (3)  
 03) 1 (N302) (VCC) 1 (309) 1 (N304) (3)  
 (VSS) 2 (311) 2 (N304) 2 (N304)  
 (303) 2 (305) 1 (N302) 가 가 (V  
 CC) 2 (N304) 가 가 (N21) (N302) 가 가 (307)  
 1 (N21) (VCC\_INT1) 1 (N21) 2 (305), 2  
 (307) (conductance) 1 2 (VCC\_INT1)  
 (VCC) (21) 1 2 (309, 311)  
 1) ( ) 1 2 (309, 311) (VCC\_I  
 NT1) (VCC) 1/2 (VCC\_INT1) 1 2 (309, 311)  
 4 1 2 (51) 4 2 (51)  
 (410), (430) (450) (410) 1 2 (411, 413)  
 , 1 2 (415, 417) (419) (410)  
 2 (VCC\_INT2) (VCON) 2 (VCON)  
 (VCC\_INT2) 1 (VCC\_INT1) (VCON) 2 (VCC  
 \_INT1) (VCC) 가 (VCC\_INT2) 1 (VCC  
 (/CS)가 " " (VCON) (/CS)가 " "  
 (421, 423)가 " " (VCON) (VCC)  
 (419) " " (410) 1 2  
 (430) (VCON) 2 (VCC\_INT2)  
 (433) 2 (VCC\_INT2)  
 (433) 2 (VCC\_INT2)  
 2 (VCC\_INT2) (430)  
 " 2 (VCON) (VCC) (431)가 "  
 (430)  
 \_INT2) (450) 2 (VCC\_INT2) 2 (VCC  
 (451)가 " "  
 (450)  
 , 2 (20) 2 (VCC\_INT2) 가  
 (51) (430)가 2 (VCC\_INT2)  
 (20) 2 (VCC\_INT2)  
 , 2 (51)

5 1 (61) (501) (61) (/CS) (501)가 "

" , 2 (N31) 1 (N21) , , 2

(VCC\_INT2) 1 (VCC\_INT1) 가 .

(VCC\_INT1) , (30) 2 (VCC\_INT

2) (VCC\_INT1) (20) (51) , 1 (VCC\_I

NT1) 2 (VCC\_INT2) , , (20) 가

가 가 가

가

(57)

1. (SRAM)

1 1 가

1 1

2 가 ;

2 가 ;

1 2 가 ;

1 1 2 1 ;

2 2 2 1 ;

1 2 ;

2.

1 , 1 2

3.

2 , 1 2 1/2

4.

1 3 , 1 2

5.

4 , 2

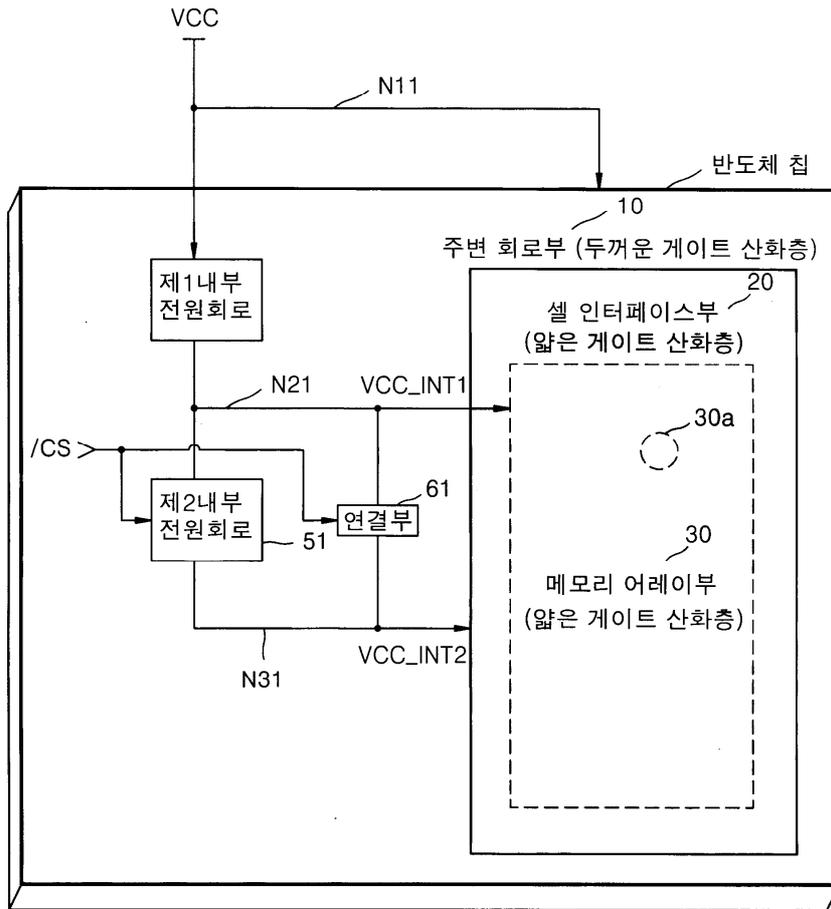
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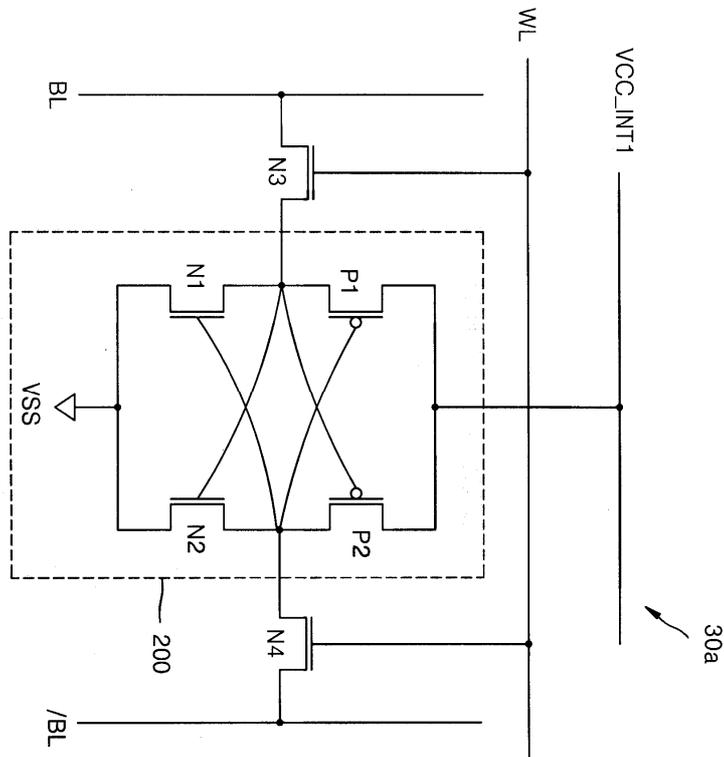
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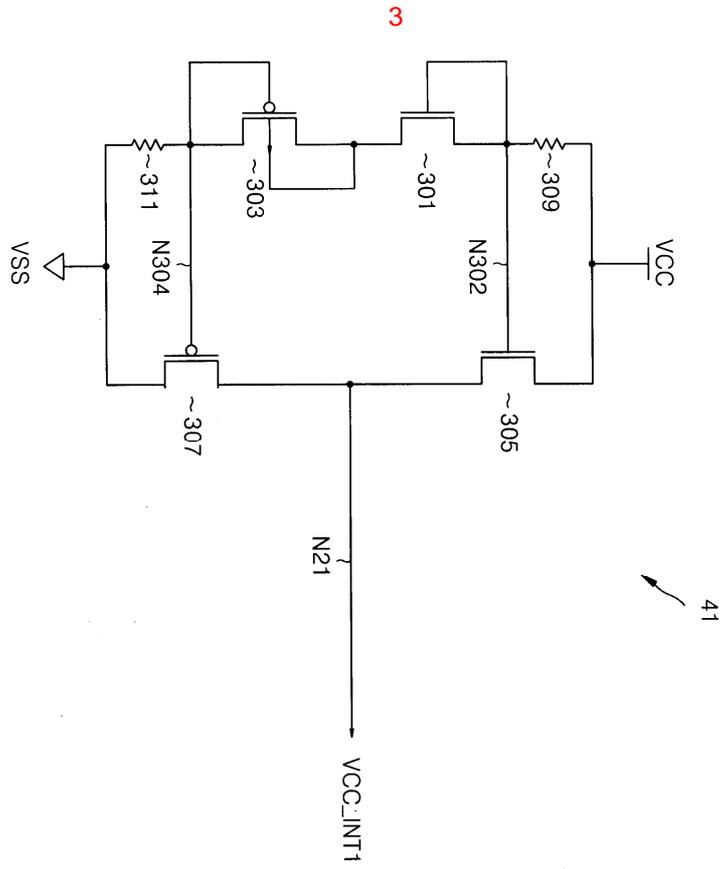
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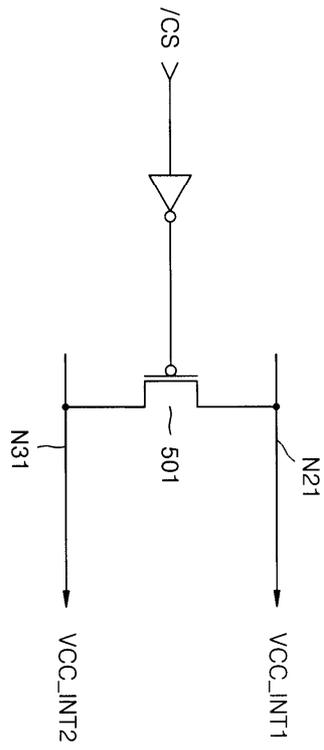
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