${\bf (19)}\ World\ Intellectual\ Property\ Organization$

International Bureau



) | 1888 | 1888 | 1888 | 1888 | 1888 | 1888 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1881 | 1

(43) International Publication Date 26 October 2006 (26.10.2006)

PCT

English

(10) International Publication Number WO~2006/111689~A1

- (51) International Patent Classification: *H05B 33/08* (2006.01)
- (21) International Application Number:

PCT/GB2005/001526

- (22) International Filing Date: 21 April 2005 (21.04.2005)
- (25) Filing Language:
- (26) Publication Language: English
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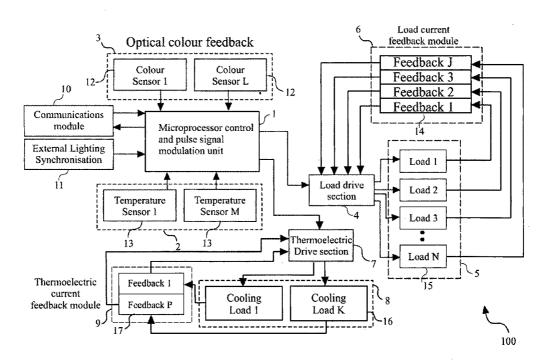
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SM, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, YU, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

Published:

with international search report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: ILLUMINATION CONTROL SYSTEM FOR LIGHT EMITTERS



(57) Abstract: A lighting fixture (1010) has a fluorescent tube (1012) and a plurality of emitters (1022-1032). A colour sensor (1016) detects light that has been totally internally reflected within a diffuser (1014) and provides a colour feedback signal to a feedback control circuit (1020) to control the light output from the fixture (1012).

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ILLUMINATION CONTROL SYSTEM FOR LIGHT EMITTERS

The present invention relates to an illumination control system for Light Emitters such as Light Emitting Diodes (LEDs), to lighting fixtures embodying such control and to methods of controlling Light Emitters.

The present applicant's earlier application WO 03/022009 (the contents of which are incorporated herein by reference) addresses, inter alia, an issue of colour coordination when multiple light sources at different wavelengths are combined to provide a composite output. Driving multiple light sources, such as LEDs, using Pulse Amplitude Modulation (PAM) provides appreciable advantages over alternative techniques such as Pulse Width Modulation (PWM) and Pulse Frequency Modulation (PFM)

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The present invention provides a number of enhancements to the disclosures therein.

A first aspect of the present invention addresses the problem of how to obtain a feedback signal for an optical sensor from a plurality of light emitters.

In accordance with the first aspect a lighting fixture is provided comprising a plurality of light emitters whose light, in operation, passes through a cover and an optical sensor arranged to detect light that has been totally internally reflected in the cover.

The cover may be a lens, diffuser, scatterer or simply a protective layer of glass, plastic or any other suitable transparent or translucent material. The light that is totally internally reflected within the cover is a representative sample in terms of colour and intensity of the light passing

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through the cover. By arranging an optical sensor to capture some of this light, an effective optical feedback loop may be formed.

The term light used herein is intended to include light having a range of wavelengths e.g. in the visible part of the spectrum as well light having a specific wavelength e.g. photons and is to be construed accordingly.

Preferably, the light emitters comprise LEDs. The optical sensor may detect any suitable characteristic of the light reflected in the cover, for example the optical sensor may comprise a colour sensor and/or an amplitude (intensity) sensor.

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Preferably, a feedback circuit is provided responsive to the output of the optical sensor to control the plurality of emitters. For example, to allow change and the control of correlated colour temperature (CCT) and colour rendering index (CRI).

Preferably, the arrangement of the sensor and feedback circuit is such that the lighting fixture automatically complies and maintains control of intensity and/or colour output of the plurality of emitters in accordance to predetermined characteristics.

Preferably, the plurality of light emitters include at least two different types, for example LEDs having a different characteristic wavelength (colour) or a different form such as organic and inorganic LEDs.

Preferably, the plurality of light emitters comprise at least one or more phosphor-light emitting diode(s) that emit a broadband visible spectral wavelength and at least one or more light emitting diode(s) that emit substantially monochromatic light within different wavelengths. This is

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beneficial for correlated colour temperature (CCT) and colour rendering index (CRI).

Preferably, a control circuit is provided comprising means for driving the two different types of LEDs to desired light intensities during coincident drive periods.

Preferably, means is provided to alter at least one of duration and repetition frequency of the drive period, for example the drive periods may start in response to an external signal.

Preferably, a drive circuit is provided for the plurality of LEDs, the drive circuit comprising power control means for controlling the amount of power supplied to at least one LED, means for measuring the intensity of the light emitted by the at least one LED, the power control means being responsive to the intensity of the light emitted wherein the power control means is arranged to drive the at least one LED at a power greater than specified.

20 Preferably, a temperature sensor is mounted in proximity to an LED to track the junction temperature thereof and a feedback circuit is preferably provided for ensuring that the power supplied to the LED is, at least in part, responsive to the junction temperature thereof. The temperature sensor may be mounted in the same package as the LED.

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Preferably, a lighting control circuit is provided for intermittently driving the plurality of light emitters and receiving feedback information from at least one feedback sensor, the control circuit comprising means for ameliorating transient effects on the output of the feedback sensor, for example an averaging circuit.

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Preferably, the means for eliminating transient effects include a delay between the actuation of at least one of the plurality of light emitters and the receiving of feedback information from the at least one feedback sensor.

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Preferably, a lighting control circuit is provided for driving at least one LED and for receiving colour information from the optical sensor, the sensor being located to receive light from at least one other light source which does not comprise an LED, the control unit comprising means for driving the at least one LED in response to the colour information received from the sensor regarding the light from the at least another light source.

Preferably, the means for driving the at least one LED are further responsive to colour information received from the sensor which colour information is derived from light generated by both the at least one LED and the at least one other light source.

Preferably, the other light source is a fluorescent light source but other types of light sources may be employed for example, an incandescent light source, a high intensity discharge light source, a tungsten light source, a sodium light source, a metal halide light source and any other suitable light source.

Preferably, at least one or more thermoelectric cooling device(s) is provided to modulate the junction temperature(s) of the light emitting diodes and a microprocessor, field programmable gate array (FPGA) or digital signal processor (DSP) or an electronic logic circuit (ELC) adapted to control said thermoelectric cooling device(s).

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Preferably, a current feedback monitor is provided for monitoring and correcting the current driving the thermoelectric cooling device(s) in response to the monitored condition(s).

Preferably, said at least one thermoelectric cooling device is a solid-state heat pump of the Peltier-effect type or a combined heat sink and electric fan unit.

Preferably, a light shaping diffuser is positioned to spatially arrange the light output of the illumination system and to provide a defined beam output that is spatially invariant in colour, colour temperature and colour rendering index properties.

Preferably, a multi-element light emitting diode beam collimating system is provided to convert wide beam angle light emitting diodes to narrow beam angle light sources.

Preferably, the multi-element beam-collimating system contains at least one non-imaging optic, preferably a micro-diffractive non-imaging optic, and at least one imaging optic for light emitting beam collimation.

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Preferably, the collimated LED beam is combined with an adjustable multi-element lens system to allow the projection of images through an aperture wherein the outgoing beam angle can be adjusted. Preferably, more than two optical lens are employed. Preferably the collimated LED beam comprises a plurality of synchronised pulsed LED light sources.

Preferably, a data connection interface such as a TCP/IP type controller is provided enabling the fixture to be controlled through a standard computer network and the Internet.

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A second aspect of the present invention permits sophisticated control based on ambient light conditions to be effected with little or no increase in component count compared with comparable approaches.

According to a second aspect of the invention, there is provided a lighting control circuit for pulsed driving of a plurality of light emitters and receiving optical output information from at least one optical sensor, the control circuit comprising means for receiving information concerning the light output of the light emitters while at least one of the light emitters is actuated and for receiving information concerning ambient light output when none of the light emitters are activated.

By the second aspect of the present invention, an optical sensor is arranged so that it detects both light issuing from the light emitters and also ambient light. The fact that, when using synchronized pulsed driving, there are OFF periods in which no light is produced by the light emitters means that the same sensor can be read during this period to provide an ambient reading. Control circuitry may then exploit this extra information to effect the control of the light emitters. This may be of particular relevance in the automotive field where collisions are particularly common at dawn and dusk. By factoring in the ambient light conditions, the control circuitry may be arranged to provide a safer environment for the driver.

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25 Preferably the circuit further comprises a feedback circuit for controlling the drive to the plurality of light emitters in response to the actuated light output and ambient light output.

According to a third aspect of the invention, there is provided a lighting control circuit for driving at least two optical emitters having different wavelength characteristics and for receiving a signal from a sensor which

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sensor can detect wavelength information, the control circuit comprising means for altering the amount of drive applied to the at least two optical emitters in response to colour information derived from the sensor.

- Preferably, the one or more sensors consist of a plurality of Silicon PIN diodes with appropriate wavelength filters or a plurality of organic light detectors with appropriate wavelength filters or a spectrometer or a solid-state colour camera detector.
- 10 Preferably, the one or more sensors consist of a device that efficiently converts photon energy into electrical energy at predefined wavelength intervals. For example, the photon energy may be converted into electrical energy that represents one of two states, either to provide the electrical energy of the sensor as a representation of the photon energy seen by the human eye, or a representation of the true optical power.

A fourth aspect of the present invention addresses a problem of limited operating range. It is well known that LEDs are non-linear devices and if they are driven at too low a voltage they will, at best, be somewhat inefficient in terms of light output per electrical power input and at worst will fail to generate any light at all.

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The fourth aspect of the present invention addresses this by providing synchronised driving waveforms in which at least one additional operating regime is added to the operating range. In this regime the period and/or the mark-space ratio of the pulse driving signal is altered from that used in a pure PAM regime.

A fifth aspect of the invention addresses applications in which a minimum

30 light output is required. Once such application is automotive lighting in
which the minimum light output will typically be specified by the relevant

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regulatory body. A difficulty arises where the light output deteriorates over time such as is the case with LEDs. (Compare this with the catastrophic failure that occurs when filament lamps reach the end of their lives). Because the deterioration of light output will be gradual it will probably not be discernible to the user, or in the case of the car, a driver. While the diminished light output will be detectable at a roadworthiness test station using special equipment, such tests are typically not conducted very frequently and so a considerable period of time could elapse without a driver realising that his or her lamps are dimmer than they ought to be.

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According to the fifth aspect of the present invention there is provided a drive circuit for at least one LED which also has means for receiving an intensity measurement of the light emitted by at least one LED. By driving the LED or LEDs in a fixture to higher and higher currents (beyond the recommended values specified by the manufacturer) the light output will be maintained at an acceptable intensity beyond the period of useful life of the LED or fixture. In addition, the increased power supply to the LED will eventually cause thermally-induced failure and it will then be clear that the LED or fixture needs replacing. This aspect of the invention, therefore, not only extends the useful life of an LED or LED fixture but also ensure that the LED or fixture are replaced in a timely fashion.

A sixth aspect of the invention provides enhanced thermal feedback. In the applicant's previous patent application, WO 03/022009, temperature sensors are disclosed for the purpose of measuring ambient temperature. However, the inventors have now come to appreciate that it is also important to measure the junction temperature of the LED. In high-power and high-efficiency LED lighting systems the junction temperature is an important control parameter. However, measurement of the junction temperature directly has not hitherto been possible.

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According to the sixth aspect, a temperature sensor is provided in sufficient proximity to the LED billet to track the junction temperature thereof. The present inventors have discovered that by mounting a temperature sensor sufficiently close to the LED billet (possibly even within the same housing) the detected temperature is found to track the junction temperature with a negative offset of around 5°C over the temperature range of interest.

A seventh aspect of the present invention is concerned with improving the quality of the feedback signals in the closed loop control circuits for light emitters. Particularly in noisy environments and as drive pulses become shorter, there is a danger that feedback signals, be they current feedback, temperature feedback or optical feedback, will be corrupted by transient effects.

This is addressed, in accordance with the seventh aspect, by sampling at a given delay and/or integrating (low pass filtering) the feedback signal prior to feeding the signal to the control circuitry.

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According to an eighth aspect of the present invention a wavelength or colour detector is provided to sample the combined output of at least two different LEDs having different wavelengths, the drive to the at least two LEDs being responsive to the output of the detector. One possible arrangement is that at least two LEDs comprise a white phosphor-based LED and a monochromatic LED.

According to a ninth aspect of the present invention an LED compensation system is provided for another type of light emitter such as a fluorescent tube. By sensing the colour of the output of the tube (and preferably the colour of the light from the tube and LEDs combined), control of, inter

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alia, colour temperature may be effected. This may be used to compensate for aging and/or to customise the characteristics of a fluorescent (or other) fitting.

- According to a tenth aspect of the present invention a lighting system is provided comprising at least one LED light source and a collimating system to produce a collimated light output from said at least one LED light source.
- 10 The collimating system may comprise a light shaping diffuser. The light shaping diffuser may be combined with a multi-element micro-refractive optic.
- In another arrangement, the collimating system may comprise a nonimaging element and an imaging element. The non-imaging element could consist of a micro-diffractive optical system or a holographic optical element.
- The collimated light output may be employed in a light projection system to provide a projected image of an aperture or object placed at the aperture.

According to an eleventh aspect of the present invention a lighting control system is provided in which a controller is utilized to control one or more remote lighting fixtures via a data connection interface.

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According to a twelfth aspect of the present invention apparatus comprising a plurality of light sources, preferably synchronized collimated LED light sources, combined with an adjustable multi-element lens system capable of varying the outgoing beam angle.

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Preferably, the apparatus can be employed in a projection system for the projection of images through an aperture.

The various aspect of the present invention will now be described, by way of example, with reference to the accompanying drawings in which:

Figure 1 illustrates by way of a timing diagram the colour imbalance that can result from (prior art) PWM drive techniques;

10 Figure 2 illustrates by way of a timing diagram the colour imbalance that can result from (prior art) PFM drive technology;

Figure 3 illustrates by way of a timing diagram, the colour balance that results from PAM driving technology;

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Figure 4 shows a block schematic diagram of a first embodiment of the present invention comprising microprocessor control to effect synchronised LED drive signals and including an optional thermoelectric feedback circuit;

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Figure 5 shows a block schematic diagram of a second embodiment of the present invention utilising a control module embodied in an Application Specific Integrated Circuit (ASIC) to generate synchronised LED drive signals;

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Figure 6 is a circuit schematic of an embodiment of a pulse signal generator to generate a pulse modulator clock signal with a defined frequency and duty cycle and a corresponding sample and hold signal for the load current feedback module;

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Figure 7 is a circuit schematic of one embodiment of a pulse modulated colour synchronisation load drive section and a load current feedback module;

- 5 Figure 8 is a timing diagram illustrating the Idac, load, clock and serial data input control signals required to program a quadruple packaged digital to analogue converter;
- Figure 9 is a circuit schematic of one alternative embodiment of a pulsemodulated colour synchronisation load drive section and a load current feedback module;
 - Figure 10 is a circuit schematic of a further alternative embodiment of a pulse-modulated colour synchronisation load drive section and a load current feedback module for four colour channels;
 - Figure 11 is a circuit schematic of an embodiment of a communications module for use with embodiments of the present invention;
- Figure 12a is a graph illustrating the forward DC current versus the ambient temperature for a typical high-brightness blue or green InGan LED at various values of thermal resistance;
- Figure 12b is a graph illustrating the normalised relative luminous flux output versus the average forward DC current for a typical high brightness blue or green InGan LED;
 - Figure 13a is a graph illustrating the relative light output versus the junction temperature for a typical range of high brightness LEDs

Figure 13b is a graph illustrating the dominant wavelength versus the temperature for a typical high brightness InGan LED;

Figure 14 is an electrical circuit schematic of one embodiment of the thermoelectric load drive section, thermoelectric cooling load and cooling load feedback module;

Figure 15a is a cross section of the human eye;

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10 Figure 15b is a block diagram of the functions of the human eye illustrating the ability of the human eye to detect colours from a range of light receptors;

Figure 16a graphically represents the relative photopic and scotopic responsivity of the human eye as a function of wavelength;

Figure 16b graphically represents the typical spectral sensitivity of a three-colour colour sensor;

20 Figure 17 is an electrical circuit schematic of one embodiment of the colour sensor feedback module;

Figure 18a is a graphical representation of the XYZ colour matching functions defined by the Commision Internationale de L'Eclairage in 1931;

Figure 18b is a graphical representation of the relative spectral power distributions for a range of different high brightness LEDs;

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Figure 19 represents a typical chromaticity diagram illustrating the standardised white illuminants A, B, C, D and their respective colour temperatures;

- Figure 20a is a graphical representation of the relative spectral power distributions of a metal halide, fluorescent and standard daylight white light sources;
- Figure 20b is a graphical representation of the relative spectral power distributions of a typical phosphor-based white LED light source;
 - Figure 21 shows an embodiment of the invention utilising a fluorescent light-emitting unit together with LED light emitters;
- 15 Figure 22 shows a schematic view of an embodiment of the invention utilising optical feedback derived from total internal reflection within a cover of a light unit;
- Figure 23 is a graph showing intensity and drive current for a further embodiment of the invention;
 - Figure 24 shows a number of timing diagrams that illustrate the performance of the first embodiment of the present invention;
- 25 Figure 25 shows a schematic view of a light shaping diffuser with a LED light source arranged to provide a defined beam output;
 - Figure 26 shows a multi-element light emitting diode beam collimating system;

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Figure 27 shows a multi-element beam collimating system containing at least one non-imaging micro-diffractive optic and at least one imaging optic for light beam collimation;

5 Figure 28 shows an embodiment of the invention utilizing a light beam collimator, aperture and projection lens for variable beam output; and

Figure 29 shows a schematic view of an embodiment of the invention, enabling the light fixture to be controlled through a standard computer network and the internet.

Figure 1 shows driving waveforms of four colour channels covering red, green, blue and amber parts of the visible spectrum that are driven by a PWM technique. It is clear that for particular colour settings, the LED cluster will exhibit several different colours and intensities that makes it impossible, especially for use with solid state TV cameras, to adequately control colour, intensity, correlated colour temperature (CCT) and colour rendering index (CRI). Figure 1 assumes that all colour channels are synchronised at TO but in practice this may not occur and further colour and intensity discrepancies would result.

Figure 2 shows an alternative method for driving LEDs, that of Pulse Frequency Modulation (PFM). PFM utilises pulses of equal amplitude and duration that are generated at a rate determined by the signals' frequency but again this technique does not enable true colour mixing or precise CCT control when two or more wavelengths or colours are used within an LED cluster. The figure illustrates how using PFM leads to very poor colour mixing, CCT and CRI properties as well as different signal frequencies for each colour channel.

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An additional method of driving LEDs is that of Pulse Amplitude Modulation (PAM) which controls the current through each LED colour channel by varying the amplitude of the current. This method has many advantages for driving LEDs such that all of the colour channels can be pulsed at exactly the same time enabling true control of colour mixing, CCT and CRI. Figure 3 illustrates such PAM driving waveforms. While square waves are shown it should be noted that other waveforms such as trapezoidal or even analogue waveforms such as sinusoidal may be used.

10 Figure 24 illustrates the principles underlying the first embodiment of the invention by way of a timing diagram. Figure 24(a) shows the PAM driving waveforms for red (R), green (G) and blue (B) emitters at a low intensity. Unfortunately, since LEDs are non-linear devices, driving them at very low current levels results in inefficient driving or in extreme cases, no driving at all.

To address this, the present embodiment may vary the pulse width of the driving waveforms. Figure 24(b) illustrates the same amount of power being applied to each of the three colour channels but the power is located in pulses of around half the previous duration. The intensity of the drive can thus be increased to a level at which each LED or LEDs are driven in a more efficient, or in a preferred arrangement linear, operating region. Because the duration of the pulses has been reduced, the overall light output is still at the (low) desired level.

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Figures 24 (c) and 24(d) illustrate the opposite scenario in which a very high intensity is required and this may even be sufficiently high to place the LEDs themselves at risk. Instead of using the 50% mark-space ratio shown in Figure 24(c), the pulse ON time is lengthened within a cycle to permit a lower absolute drive amplitude to be applied to the LEDs. This is shown in Figure 24(d).

Alternatively, the width of the individual pulse may be maintained while altering the pulse repetition period (lengthening for low light values and shortening for high light values). As in the first alternative, this permits an appropriate level of drive to be applied to LED or LEDs for a given power output. Note that this differs from PFM in that the colour outputs remain synchronised.

Both the PWM and PFM techniques are widely utilised within LED based lighting and display applications as they often assume that the light will be received by the human eye. As the human eye (Figure 15a) has an integrating function, it is assumed that colour discrepancies will not be noticed provided the pulse repetition frequency is above a few hundred Hertz. However, this is not applicable to solid state camera systems in which illumination of the subject using PWM will result in flicker and poor colour balance. It should also be noted that the current generation of high brightness inorganic semiconductor LEDs available from LumiLEDs Lighting of San Jose, CA, USA advise using somewhat lower modulation frequencies than this.

Figure 4 shows a block diagram of an illumination control system 100 according to the first embodiment. The system comprises a microprocessor 1 used to control the system and to generate amplitude modulation signals in response to inputs from a temperature sensor module 2 and a colour sensor module 3. A load drive section 4 receives inputs from both the microprocessor 1 and the load current feedback module 6. It also has an output connected to the load 5 which in turn provides an output to the load current feedback module. An optional thermoelectric drive section 7 has inputs from the microprocessor and a cooling load feedback module 9. The section 7 has an output connected to a thermoelectric load module 8 which in turn provides an output to the

cooling load feedback module 9. An optional communications module 10 provides bi-directional data communication between the microprocessor and one or more external devices or controllers. An external lighting synchronisation module 11 is provided to form an interface to an external synchronisation signal such as from a solid state camera when such synchronisation is required.

A temperature sensor module 2 includes a plurality of temperature sensors (13), the colour sensor feedback module 3 includes at least one and preferably a plurality of colour sensors 12 and the load drive section includes at least one and preferably a plurality of load drivers (not shown) for driving a plurality of loads. The load current feedback module comprises a plurality of load current feedback sensors while the thermoelectric drive section 7 includes a plurality of thermoelectric drivers (not shown) used to drive a plurality of cooling loads 16 and the thermoelectric current feedback module contains a plurality of cooling load current feedback sensors.

The embodiment operates as follows. A required light intensity and colour are received from an external controller via communications module 10. The microprocessor then calculates the appropriate drive intensities for each of the different colour channels within the system. In doing so, any feedback from the optical colour feedback module 3 will be taken into account. The microprocessor will then determine the drive current, pulse width and pulse repetition frequency that best matches the required intensities in each channel. For instance, that channel with the lowest required intensity must be driven at a sufficiently high drive level to ensure illumination, while that channel at the highest required intensity must not be driven to such a level (considering the thermal feedback from the temperature sensing module 2) that the Load LEDs are jeopardised. Once appropriate pulse widths and intensities have been calculated, the

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load drive section 4 is instructed to apply the appropriate pulses to the loads — always within the requirement that the duration and timing of the pulses on each channel are the same. This results in synchronised control.

Where one or more colour channels require a much lower drive intensity than other channels is maybe appropriate not to drive some of the LEDs in that or those colour channels.

The Load Drive Section 4 and the load current feedback module 6 cooperate to control the current through the load LEDs to ensure both adequate drive and overload protection.

Referring to the block diagram of Figure 5 a second embodiment of the illumination control system 100 is shown. In this embodiment the communications module 10 communicates not with a microprocessor but with a logic control circuit embodied in an ASIC 91. In this embodiment the temperature feedback is applied to the load drive section 4, as is the optical colour feedback from module 3. The optional thermoelectric drive section has been omitted.

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The ASIC 91 is designed to perform the functions (with the exception of the temperature and colour feedback) previously performed by the microprocessor in the first embodiment. The ASIC may comprise a small, cheap Field Programmable Gate Array (FPGA) such as the Xilinx XC9572XL from Xilinx Inc. San Jose CA, USA to a mixed signal ASIC carrying additional circuitry for the illumination control system and even further circuitry for any system integrated therewith.

Figure 6 shows an embodiment of a pulse signal generator 21 which 30 provides a pulse modulator clock signal output 19 to the load drive section 4 and a corresponding sample-and-hold output signal 20 for the

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load current feedback module 6. The pulse modulator clock input may be derived from either the microprocessor 1 or an ASIC 91 with a defined frequency and duty cycle. The frequency and duty cycle may be programmed by changing data values held within registers of the microprocessor 1 or by timing synchronisation pulses within the ASIC 91 to enable the illumination system 100 to vary its frequency or duty cycle according to the in tended application. An application brief from Agilent Technologies entitled PULSED OPERATING RANGES FOR ALINGAP VS **PROJECTED** LONG TERM LIGHT OUTPUT **LEDS** PERFORMANCE recommends typical duty cycle ratios for common LED-based applications. By utilising one or more output pins located on the microprocessor 1 a highly stable duty cycle and frequency can be achieved which in turn enables precise control of colour synchronisation and therefore CCT and CRI. Additional advantages are a reduction in external component requirements and the ability to change the frequency and duty cycle from software operating on the microprocessor 1.

In general the illumination control system will be operated in a non-continuous pulse mode of operation that will provide current to the LED loads 15 through the load drive section 4. In practice the load current feedback circuits will benefit from amelioration of transient effects in the sensed current. In this embodiment this is achieved using a sample and hold circuit to ensure that the pulse modulated colour synchronised current feedback control signal is maintained during a time period when the Load LEDs are not energised. Without such a circuit, the control system is likely to see transient switching voltages which could result in unsettled drive currents through the loads 15 producing variations in the intensity of the LED light output. A delay is advantageously added to reduce the effect of the initial switching transient.

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An inverted and time delayed clock signal relative to the pulse modulator clock signal 19 is thus derived. The sample and hold output signal 20 and clock output signal 19 can be created using NPN transistor Ql and resistor R2 to invert the pulse modulator clock signal input from a microprocessor 1 or ASIC 91. The inverted signal is buffered and inverted by the dual input NAND gate (Ula) with its output connected directly to both inputs of NANDgate (UIc) and also to one of the inputs of NAND gate (UIb) and to the other input of the NAND gate (Ulb) via resistor (R3) capacitor (C1) network. The resistor capacitor (RC) network provides a time delay according to a well known formula and typical values of R3 is 68kOhms and Cl is lnF which provides a time constant of around 68 microseconds. The signal is then inverted and delayed again by Ulc and the RC network R4 and C2 with a final inversion by NANDgate (Uld) to create the pulse modulator clock signal output 19 that represents the ON portion of the duty cycle in Figure 3. To those of skill in the art, selection of appropriate component values can be performed to accommodate values of maximum and minimum desired values of modulation signal frequency. Equally, alternative circuits to perform the requisite function could readily be designed the skilled person.

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This technique is also applicable to the other feedback circuits within the system such as the colour feedback circuit.

Figure 7 shows the electrical schematic of an embodiment of the load drive section 4 with a load current feedback module 6 whereby a pulse modulated colour synchronised control signal 30 is formed to drive a voltage controlled current source or load driver (31), such as an n-channel MOSFET through a differential amplifier (U3a) and analogue switching arrangement (U4a, U4b). The pulse modulated colour synchronised control signal is created by combining the amplitude modulated control signal 27 using the output of a Digital to

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Analogue Converter (DAC) U2 and the current feedback control signal is generated using a sense resistor, which is consequently pulse modulated by employing an analogue switching network using the pulse modulator clock signal outputs representing the time periods Ton (19) and Toff (20).

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An exemplary DAC is the 14 pin TLC5620 with serial programming interface available from Texas Instruments Incorporated, Dallas TX, USA. In the current embodiment the DAC is used to generate an amplitude modulated control signal 27 in the form of a voltage output, which is created as a proportion of the DAC's input reference voltage 26 using an 8-bit data register representing one of 256 different voltage levels between ground and the DAC's reference voltage input. Therefore, by selecting the appropriate value to be stored in the DAC's data register an amplitude (or voltage) of the pulse modulated colour synchronised control signal 30 can be altered accordingly. The DAC's input reference voltage 26 may be generated using a standard voltage divider arrangement containing two resistors R5 and R6 in series between the logic supply voltage and Ground. A typical value for R5 and R6 is 2k2Ohms, which provides a DAC input reference voltage 26 of approximately 50% of the logic supply voltage, or 2.5 volts. From the foregoing description it will be appreciated that the DAC's input reference voltage 26 may be provided using alternative techniques.

25 The amplitude modulated control signal 27 output is modified by programming the DAC (U2) using a serial interface which is connected to a microprocessor 1 or ASIC 91. To program the DAC with a new output voltage the microprocessor or ASIC sets the DAC Load control line 22 high and then clocks in a command byte followed by a data byte on the DAC Data line 24. Once all the bits have been clocked in, the DAC load control line is pulsed low to transfer the data from the serial input

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register to the selected DAC data register. When the DAC LDAC control line 25 is set high during serial programming and then pulsed low the DAC data register value is transferred to the DAC output directly. The DAC LDAC control line 25 enables each of the four DACs outputs to be updated simultaneously enabling precise synchronisation of all DAC output channels. The command and data bytes are clocked into the DAC (U2) on the falling edge of the DAC clock line 23 provided by either a microprocessor or ASIC. Figure 8 is a timing diagram illustrating the sequence for programming the DAC, where the command bits are set to instruct to write the data contained within the data byte to the DAC determined by the channel select bits A0 and A1. The range bit RG controls the DAC output range. When RING is set low, the output range is between the applied reference voltage and GND, and when RING is set high, the range is between twice the reference voltage and GND.

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Referring to Figure 7, the amplitude modulated control signal voltage is fed to a non-inverting amplifier (U3a) configured to provide a continuous differential output voltage generated by the voltage difference between the amplitude-modulated control signal voltage 27 and the current feedback control signal voltage 28. The current feedback control signal voltage is generated by a load feedback current sensor 14 in the form of a sense resistor (Rsense) used to measure the current flowing from a power source (+Vdd) through the load 15 and the load driver MOSFET 31 to GND. The sense resistor may be connected between the source terminal of the load driver MOSFET and ground to generate a small voltage(Vs) which is proportional to the load current that flows thorough the load 15. A low value is preferred for Rsense to minimise dissipation and the resistor commonly comprises a discrete metallic resistor with zero temperature coefficient such as manganin or constantin. The sense resistor may comprise a portion of track on the Printed Circuit Board (PCB).

A pulse modulated colour synchronised control signal 30 is created by connecting the output voltage from the differential operational amplifier (U3a) stage to the input (Bl) of a two-channel, single-pole double-throw analogue switch (U4a) through a resistor (R13). The second input (DO) of the analogue switch (U4a) is connected directly to circuit ground while the output (A) of the analogue switch (U4a) is connected to the gate electrode 30 of the load driver MOSFET 31 through a coupling resistor R9. The control input (S) of the analogue switch (U4a) is connected to the pulse modulator clock signal output 19 which connects the appropriate input DO or D1 of the analogue switch (U4a) to the output (A) according to the logic level 0 or 1 of the pulse modulator clock output signal 19 respectively. During the ON period Ton, of the pulse modulator clock output signal 19 the analogue switch (U4a) output (A) is connected to the coupling resistor (R9) and charges the capacitor (C3) which produces a voltage potential proportional to the output voltage of the differential operational amplifier (U3a). However, during the off period, Toff, the analogue switch (U4a) output (A) is connected to circuit ground and ensures that the load driver MOSFET 31 does not allow current to pass through the load 15. The voltage potential across the capacitor (C3) remains stable and does not discharge during the time period, Toff, as the inputs D1 and BOof the analogue switches U4a and U4b become high impedance ensuring little or no current discharge occurs.

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During the off time period, Toff, of the sample-and-hold output signal 20 the input DO of the analogue switch (U4b) is connected to output (A) and hence to the coupling resistor (RIO) enabling the voltage potential (Vs) across the sense resistor (Rsense) or current feedback control signal voltage 28 to appear at the negative input of the operational amplifier (U3a). A protection diode (D1) is used to protect the operational amplifier (U3a) from negative voltage spikes that may occur during a failure of the

microprocessor 1 or ASIC 91. Accordingly, during the on time, Ton, of the sample-and-hold output signal 20 the input (D1) of the analogue switch (U4b) is connected to output (A) leaving a high impedance state at the input (D0). The capacitor (C3) ensures that a voltage is maintained during the timing period, Toff, of the pulsed modulated colour synchronised control signal and therefore does not require a large transition current through the load driver MOSFET when entering the timing period, Ton, thus ensuring the current through the load 15 is stable.

A Zobel filter is present across the load drive signal 33 and the circuit ground in the form of resistor (RI I) and capacitor (C4) in series. The presence of a Zobel filter ensures that the load 15 appears resistive at high frequencies and helps to ensure oscillation is attenuated at high frequencies. Typical values for the resistor (RI 1) in 10 Ohms and capacitor (C4) is 10 nanoFarad.

In one alternative embodiment of the load drive section 4 and the load current feedback module 6 shown in Figure 9, the continuous amplitude modulated control signal 27 is generated by combining two timing signals (one timer represents frequency and the other duty cycle) to form a timing control signal 90 from a microprocessor 1 or ASIC 91. The timing control signal is then integrated through a low pass filter in the form of a resistor/capacitor network. The foregoing embodiment has considerable advantages in practice as the DAC uses low cost passive components and the DAC resolution is software-configurable. Alternatively, the timing control signal 90 may be generated by a dedicated PWM timer within the microprocessor I or ASIC 91 and produces continuous output voltage with an amplitude resolution proportional to the resolution of the input timing control signal 90. Similar DAC conversion techniques are well known to those skilled in the art.

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In yet another separate embodiment of the load drive section 4 shown in Figure 10 up to 4 separate load driver 4 and four separate load current feedback sensor 14 channels can be are used to invert the serial line data input 57 and slow down the transistor operation. Typical values for capacitor CS and resistor R14 are 100pF and 22Kohms respectively.

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Figure 21 shows a lighting arrangement 1010 in accordance with an embodiment of the present invention. A fluorescent tube 1012 is shown arranged in traditional fashion between a reflector 1036 (shown in section) and a diffuser 1014. Light output from the tube passes through the diffuser either directly or via the reflector which is typically shaped and arranged to minimise the amount of "wasted" light.

However, such fluorescent tubes deteriorate with age and, to counter this, a feedback circuit 1020 and six LEDs 1022 — 1032 are provided to compensate. The diffuser 1014 is provided with a colour sensor 1016 which detects light that has been totally internally reflected within the diffuser. This light will be representative of the light passing through the diffuser and so the output of the sensor 1016 provides a colour feedback signal to the feedback control circuit 1020 via signal line 1018.

The feedback control circuit is arranged to respond to the output of the colour sensor 1016 to control the light issuing from the fixture. To this end the circuit 1020 is connected via line 1034 to six amber LEDs 1022, 1024, 1026, 1028, 1030, 1032 which are mounted in the reflector 1036. Because of the nature of the deterioration of fluorescent tubes, adding some amber light provides compensation for the aging process. As the tube 1012 deteriorates still further, a greater amount of light will be provided by the LEDs to compensate. The closed loop feedback provided by the colour sensor 1016, feedback control circuit 1020 and the LEDs

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allows at least one colour parameter (for example colour temperature) to be consistently maintained.

The fluorescent tube 1012 may be arranged to be separately removable since the lifetime of such tubes is typically 1000 hours while the lifetime of LEDs is typically 10000 hours. Alternatively, since LEDs are comparatively cheap, they could be incorporated with the tube and discarded when the tube is renewed.

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While the colour sensor 1016 is shown as sensing the output from the whole fixture, i.e. the fluorescent tube and the LEDs, it is possible for the sensor to be arranged to detect only the output from the fluorescent tube.

While this embodiment has been described as providing compensation for the aging of the fluorescent tube, it should be noted that it could equally be used to set a desired characteristic, such as colour temperature, for a tube of any age. Since fluorescent tubes are currently provided in various colour temperatures for various purposes, this would allow manufacturers and dealers in fluorescent tubes to reduce their inventory with attendant reduction in cost.

Moreover, this embodiment of the invention may equally be applied to other non-LED sources of light that deteriorate with age or which would benefit from the ability to provide custom colour features such as incandescent lamps.

Figure 22 shows another embodiment 1100 of the present invention that exploits the total internal reflection of light within a diffuser, lens or cover. The control circuitry has been omitted for clarity. The embodiment

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may preferably be a headlamp for a car but this aspect of the invention is applicable to a wide range of lighting fixtures.

A circuit board 1102 carries a number of LEDs of which five 1104, 1106,1108,1110 and 1112 are shown. Light from these LEDs passes through a cover 1114 which may be a lens, diffuser etc. constructed from glass or plastics. By the nature of such an arrangement, some of the light incident upon the cover 1114 will be totally internally reflected within the cover as depicted by the zigzag lines. A portion of this totally internally reflected light will impinge upon an optical sensor 1116 mounted somewhere on the periphery of the cover.

The sensor 1116 could be a colour sensor as used in the embodiment shown in the previous figure or it might simply be an intensity sensor an required by the following embodiment. By utilising such as sensor, the collective output of a large number of LEDs may be sensed using a single sensor. This has the benefit over mounting one or more sensors on the circuit board of sensing the entire light output and will not be distorted by the performance (or otherwise ~ of individual LEDs.

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Figure 23 shows a diagrammatic graph of LED light intensity 1202 and drive current 1204 over time.

Imagine that light intensity 1202 is generated in response to current 1204 towards the left hand side of the graph. In the case of a car headlamp, for example, there is a minimum acceptable intensity value determined by the regulatory authorities. In other applications there will be a minimum specification of light output and this value is illustrated by the horizontal broken line 1206.

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After a long usage time the output of the LED will deteriorate as illustrated by the downward turn of curve 1202 at time A. At time B the intensity of the LED output falls below specification. One possibility here would be to detect this and light a warning light on the dashboard of the car (or somewhere else on another, non-automotive, light fitting) to inform the driver that his or her lighting unit needs to be replaced. However, this would quite likely be ignored as the deterioration in intensity would probably not be discernable to the driver.

- In the present embodiment, this is addressed by increasing the current drive provided to the LED at time B to cause the light intensity to follow the dot-dash line 1208 (i.e. continue at or above the minimum output level). As a consequence of this the LED will fail catastrophically at time C as shown by the line 1210. The advantages are that the lifetime of the LED is extended beyond that at which it fails to perform to specification and the catastrophic failure ensures that the driver or other user has to replace the LED, lighting unit or fixture and cannot continue to operate a failing unit with the attendant safety implications.
- 20 Conventional LED devices usually emit a radiant light pattern at large beam angles that restricts their use for applications where a defined beam output is required. Figure 25 shows a LED beam collimating system 200 that provides a degree of illumination beam output control to convert the light output from a LED source 220 to a more usable light source with collimated light output characteristics.

As shown a light shaping diffuser 210 can be placed directly in front of the LED light source 220 containing LED's of different wavelength characteristics 230, 232, 234, 236 to enable the light output of the illumination system to be spatially invariant in terms of colour, colour temperature and colour rendering index properties.

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The light shaping diffuser 210 could also be utilised to change the beam pattern of the LED beam collimating system 200 from a typical Gaussian beam output to a square, elliptical, line or some other required beam profile.

Figure 26 shows how a multi-element LED beam collimating system can be utilised to convert one or more wide beam angle LED's to a narrow angle light source providing a highly collimated LED beam output by utilising a combination of multi-element micro refractive elements 300 aligned to the LED dies contained within the LED source 320 and a light shaping diffusing element 340. Combining a multi-element micro refractive optic and a light shaping diffuser enables a compact, lightweight highly collimated LED source to be obtained.

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Referring now to Figure 27, a further embodiment of the invention is shown incorporating a configuration whereby a non-imaging diffractive 400 and an imaging element 420 are combined to provide a highly collimated beam output from the LED source 430.

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The non-imaging element 400 could consist of a micro diffractive optical system or a holographic optical element to concentrate the light emitted from the LED source 430 without maintaining any of the spatial characteristics of the LED's 440, 442, 444, 446. The second imaging element 420 could consist of a lens and provides enhanced collimation and beam control.

This arrangement enable the use of the LED light source 430 in a large number of lighting application such as image projection systems, whilst maintaining high light efficiency.

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Figure 28 shows a light projection arrangement 500 in accordance with an embodiment of the present invention. A LED source 510 is shown with an arrangement containing an array of one or more LED emitters 520, 522, 524, 526. The light output from the LED source 510 passes through a multi-element light emitting diode beam collimating system 530 into an aperture arrangement 540 that can contain an object that can be projected using a series of imaging lenses 550, 552, 554 that can be continuously adjusted to provide a projected image of the aperture or object placed at the aperture with a continuously variable beam profile output. The imaging lenses 550, 552, 554, may contain a combination of aspheric or spherical surfaces to provide the required imaging properties.

This arrangement utilises the advantages of non-imaging and imaging optics to provide a highly efficient compact, low cost, low weight lighting projection system.

Figure 29 shows another embodiment of the invention whereby a plurality of lighting fixtures 620 are controlled and coordinated utilising a system 600 through the use of a Local Area Network (LAN), a Wide Area Network (WAN) 610 or the Internet utilising 630 TCP/IP protocols.

This communication technique provides a considerable advantage as standard IT infrastructures may be utilised to control a large network of lighting fixtures in a variety of topologies with low cost.

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For example, one controller 600 could be connected via the Internet 630 to a remote site located anywhere in the World, shown as 640 & 650, and control the lighting scene automatically and communicate lighting fixture control and diagnostic information in real time mode.

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The various aspects of the invention described herein are applicable, unless the context indicates otherwise, to PAM driving techniques and, albeit with possible reduction in performance, to PWM, PFM and other non-PAM driving techniques.

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The present disclosure extends to any novel feature or combination of features disclosed herein whether express or implied and to any generalisation thereof.

CLAIMS

- 1. A lighting fixture comprising a plurality of emitters and a cover through which light from the emitters travels when they are activated, and an optical sensor arranged to detect light that has been totally internally reflected in the cover.
- 2. A lighting fixture as claimed in claim 1 wherein the plurality of emitters comprise LEDs.

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- 3. A lighting fixture as claimed in claim 1 or claim 2 wherein the optical sensor comprises a colour sensor.
- 4. A lighting fixture as claimed in any one of the preceding claims
 wherein the optical sensor comprises an amplitude sensor.
 - 5. A lighting fixture as claimed in any one of the preceding claims further comprising a feedback circuit responsive to the output of the optical sensor to control the plurality of emitters.

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6. A lighting fixture as claimed in any one of the preceding claims wherein the arrangement is such that the lighting fixture automatically complies and maintains control of intensity and/or colour output of the plurality of emitters in accordance to predetermined characteristics.

- 7. A lighting fixture as claimed in claim 2 wherein the plurality of LEDs are of at least two different types.
- 8. A lighting fixture as claimed in claim 7 wherein said at least two different types of LEDs have a different characteristic wavelength.

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- 9. A lighting fixture as claimed in claim 7 wherein said at least two different types of LEDs include organic and inorganic LEDs.
- 10. A lighting fixture as claimed in any one of claims 7 to 9 wherein a control circuit is provided comprising means for driving the two different types of LEDs to desired light intensities during coincident drive periods and means is provided to alter at least one of duration and repetition frequency of the drive period.
- 10 11. A lighting fixture as claimed in claim 10, wherein the drive periods start in response to an external signal.
 - 12. A lighting fixture as claimed in claim 2 further comprising a drive circuit for the plurality of LEDs, the drive circuit comprising power control means for controlling the amount of power supplied to at least one LED, means for measuring the intensity of the light emitted by the at least one LED, the power control means being responsive to the intensity of the light emitted wherein the power control means is arranged to drive the at least one LED at a power greater than specified.

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- 13. A lighting fixture as claimed in claim 2 further comprising a temperature sensor mounted in proximity to an LED to track the junction temperature thereof.
- 25 14. A lighting fixture as claimed in claim 13 further comprising a feedback circuit for ensuring that the power supplied to the LED is, at least in part, responsive to the junction temperature thereof.
- 15. A lighting fixture as claimed in claim 13 or claim 14, wherein the sensor is mounted in the same package as the LED.

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16. A lighting fixture as claimed in claim 1 or claim 2 further comprising a lighting control circuit for intermittently driving the plurality of light emitters and receiving feedback information from at least one feedback sensor, the control circuit comprising means for ameliorating transient effects on the output of the feedback sensor.

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- 17. A lighting fixture as claimed in claim 16, wherein the means for eliminating transient effects comprises an averaging circuit.
- 10 18. A lighting fixture as claimed in claim 16 or claim 17 wherein the means for eliminating transient effects include a delay between the actuation of at least one of the plurality of light emitters and the receiving of feedback information from the at least one feedback sensor.
- 19. A lighting fixture as claimed in claim 2 further comprising a lighting control circuit for driving at least one LED and for receiving colour information from the optical sensor, the sensor being located to receive light from at least one other light source which does not comprise an LED, the control unit comprising means for driving the at least one LED in response to the colour information received from the sensor regarding the light from the at least another light source.
 - 20. A lighting fixture as claimed in claim 19 wherein the means for driving the at least one LED are further responsive to colour information received from the sensor which colour information is derived from light generated by both the at least one LED and the at least one other light source.
- 21. A lighting fixture as claimed in claim 19 or claim 20 wherein the another light source is selected from the group comprising a fluorescent light source, an incandescent light source, a high density discharge light

source, a tungsten light source, a sodium light source and a metal halide light source.

- 22. A lighting fixture according to 2 wherein further comprising at least one or more thermoelectric cooling device(s) to modulate the junction temperature(s) of the light emitting diodes and a microprocessor, FPGA or DSP adapted to control said thermoelectric cooling device(s).
- 23. A lighting fixture according to claim 22, wherein a current feedback monitor is provided for monitoring and correcting the current driving the thermoelectric cooling device(s) in response to the monitored condition(s).
- 24. A lighting fixture according to claim 22 or claim 23, wherein at least one thermoelectric cooling device is a solid-state heat pump of the Peltier-effect type.
- 25. A lighting fixture according to claim 22 or claim 23, wherein at least one thermoelectric cooling device comprises a combined heat sink20 and fan unit.
 - 26. A lighting fixture according to claim 1, further including a light shaping diffuser positioned to spatially arrange the light output of the illumination system and to provide a defined beam output that is spatially invariant in colour, colour temperature and colour rendering index properties.

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A lighting fixture according to claim 2, further including a multielement light emitting diode beam collimating system to convert wide beam angle light emitting diodes to narrow beam angle light sources.

- 28. A lighting fixture according to claim 27, wherein the multi-element beam-collimating system contains at least one non-imaging optic and at least one imaging optic for light emitting beam collimation.
- 5 29. A lighting fixture according to claim 27, wherein the collimated LED beam is combined with an adjustable multi-element lens system whereby the outgoing beam angle can be varied.
- 30. A lighting fixture according to claim 29, wherein the collimated 10 LED beam comprises a plurality of synchronised pulsed LED light sources.
- 31. A lighting control circuit according to claim 1, wherein a data connection interface such as a TCP/IP type controller is provided enabling the fixture to be controlled through a standard computer network and the Internet.
 - 32. A lighting control circuit for pulsed driving of a plurality of light emitters and receiving optical output information from at least one optical sensor, the control circuit comprising means for receiving information concerning the light output of the light emitters while at least one of the light emitters is actuated and for receiving information concerning ambient light output when none of the light emitters are activated.

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- 25 33. A lighting control circuit as claimed in claim 32 further comprising a feedback circuit for controlling the drive to the plurality of light emitters in response to the actuated light output and ambient light output.
- 34 A lighting control circuit according to claim 32, wherein the light 30 emitters comprise LEDs and further including a multi-element light

emitting diode beam collimating system to convert wide beam angle light emitting diodes to narrow beam angle light sources.

- 35. A lighting control circuit according to claim 34, wherein the multielement beam-collimating system contains at least one non-imaging optic and at least one imaging optic for light emitting beam collimation.
- 36. A lighting control circuit for driving at least two optical emitters having different wavelength characteristics and for receiving a signal from one or more sensors which can detect wavelength information, the control circuit comprising means for altering the amount of drive applied to the at least two optical emitters in response to colour information derived from the or more sensors.
- 15 37. A lighting control circuit according to claim 36, wherein the one or more sensors consist of a plurality of Silicon PIN diodes with appropriate wavelength filters.
- 38. A lighting control circuit according to claim 36, wherein the one or 20 more sensors consist of a plurality of organic light detectors with appropriate wavelength filters.
 - 39. A lighting control circuit according to claim 36, wherein the one or more colour sensors consist of a spectrometer.
 - 40. A lighting control circuit according to claim 36, wherein the one or more sensors consist of a solid-state colour camera detector.

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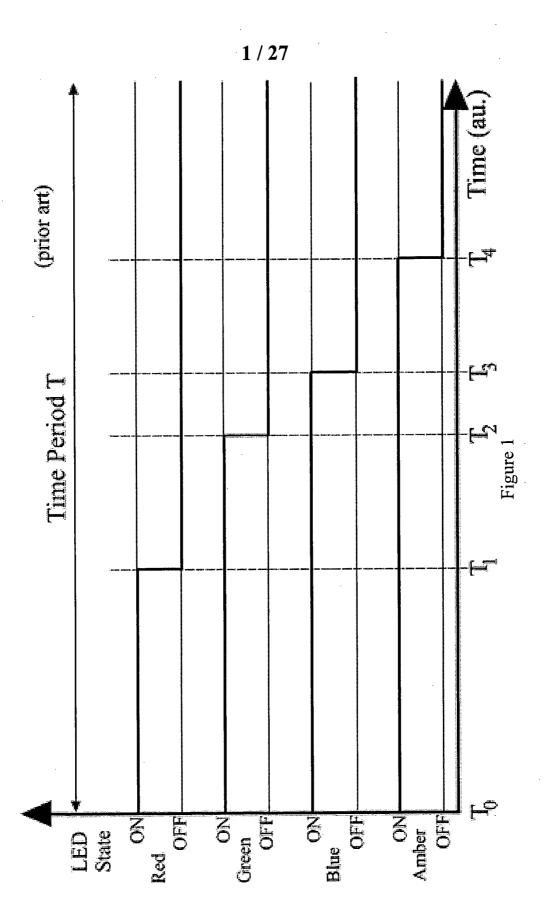
41. A lighting control circuit according to claim 40, wherein the one or more sensors consist of a device that efficiently converts photon energy into electrical energy at predefined wavelength intervals.

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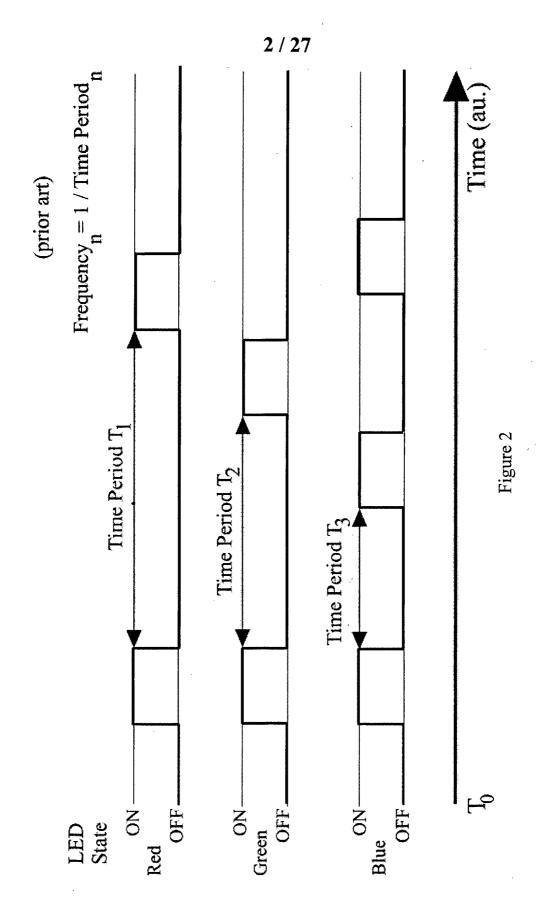
- A lighting control circuit according to claim 36, wherein the optical emitters comprise LEDs and further including a multi-element light emitting diode beam collimating system to convert wide beam angle light emitting diodes to narrow beam angle light sources.
- 43. A lighting control circuit according to claim 42, wherein the multielement beam-collimating system contains at least one non-imaging optic and at least one imaging optic for light emitting beam collimation.

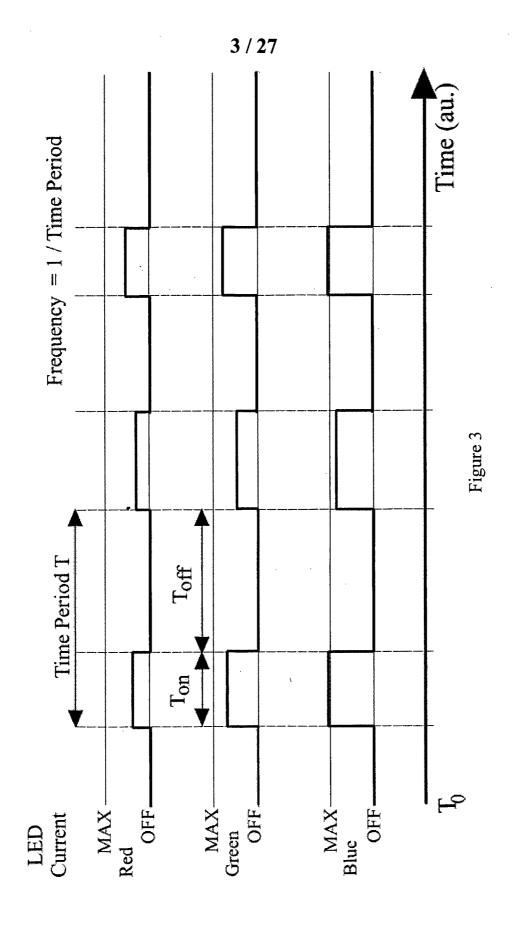
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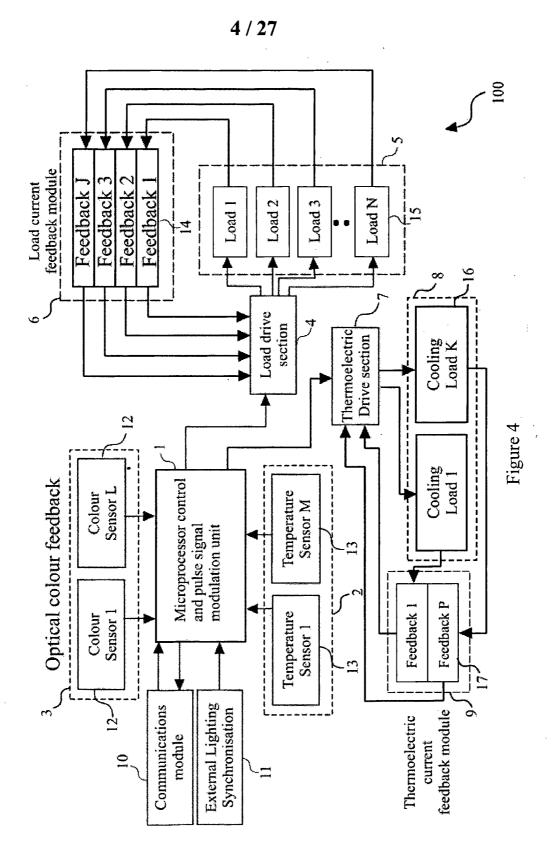
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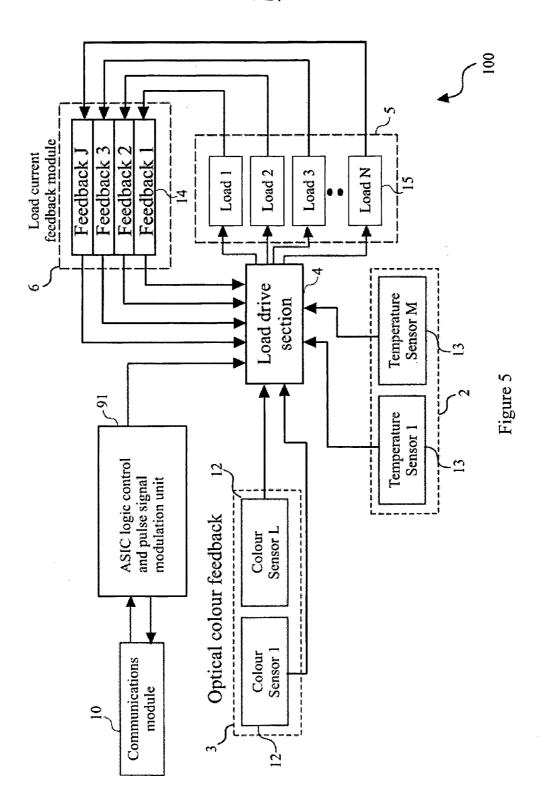
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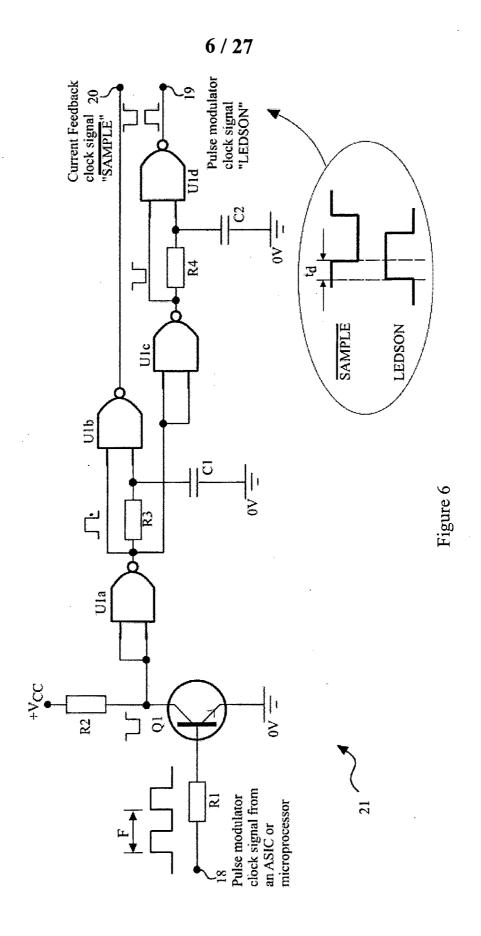




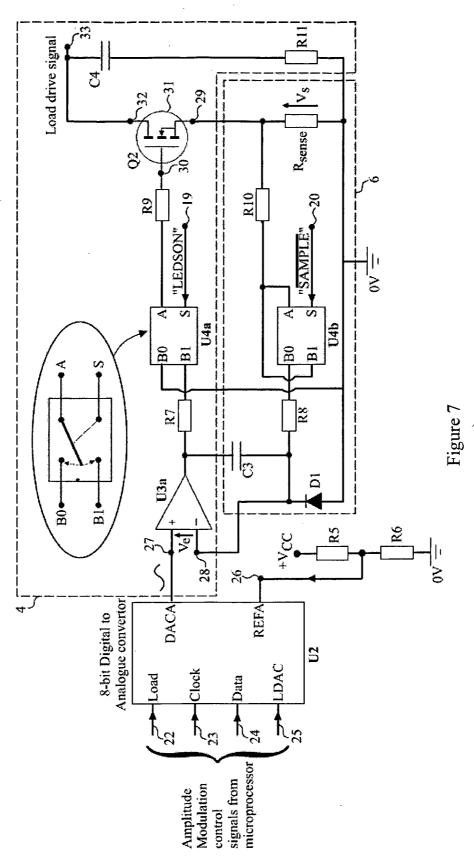


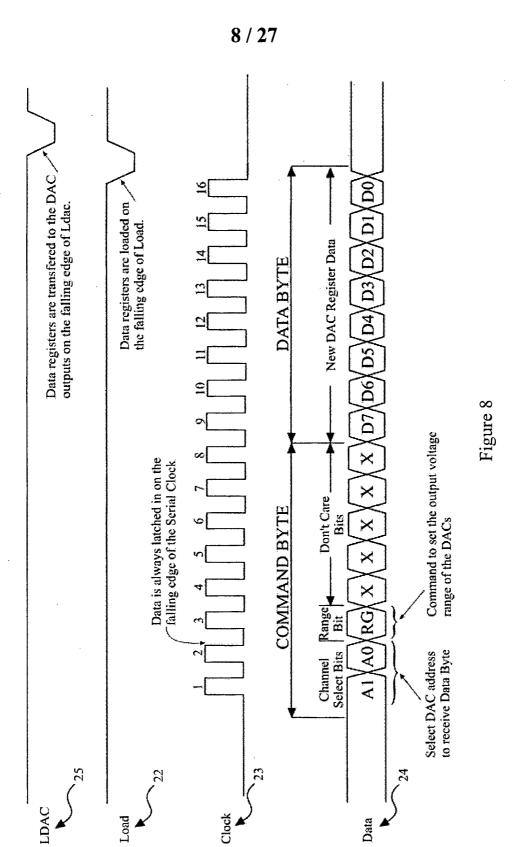












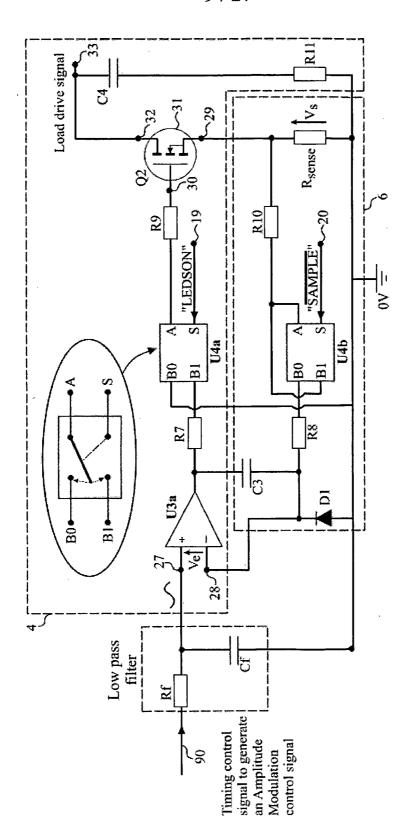


Figure 9

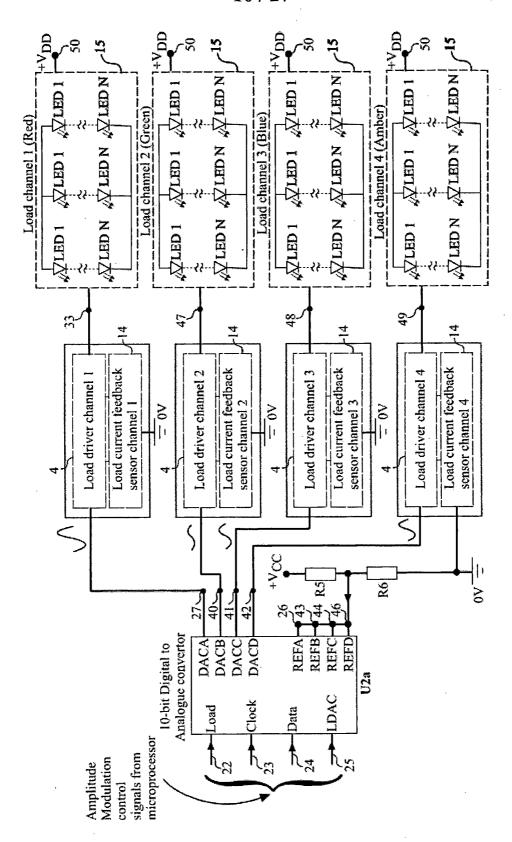
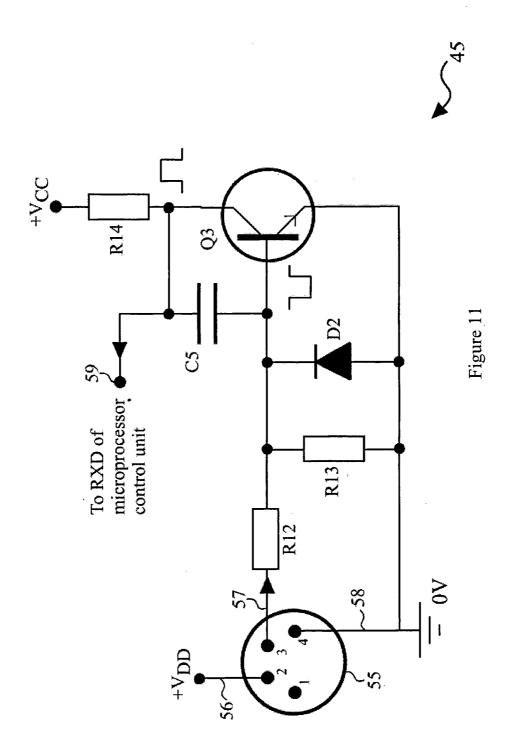
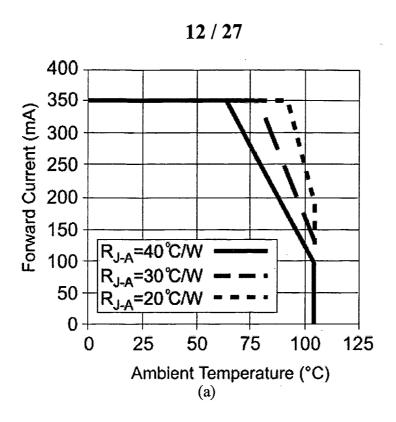


Figure 10





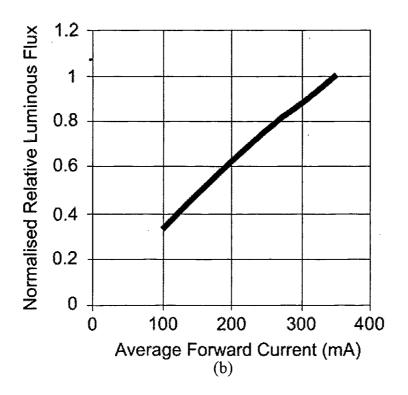
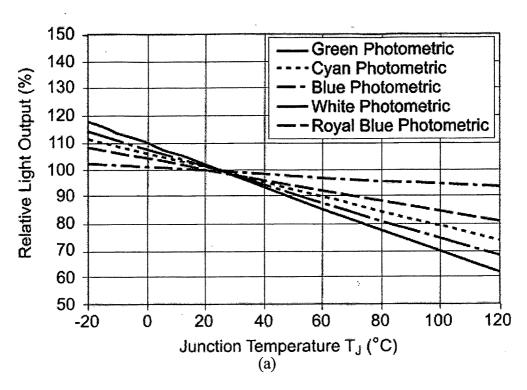
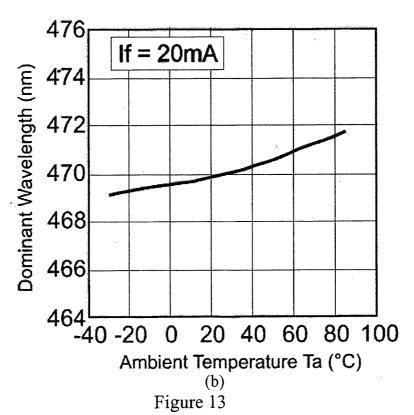
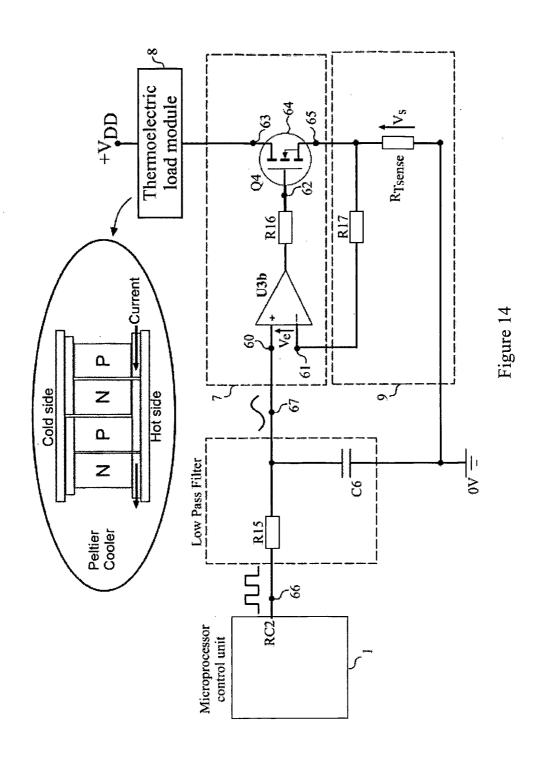


Figure 12











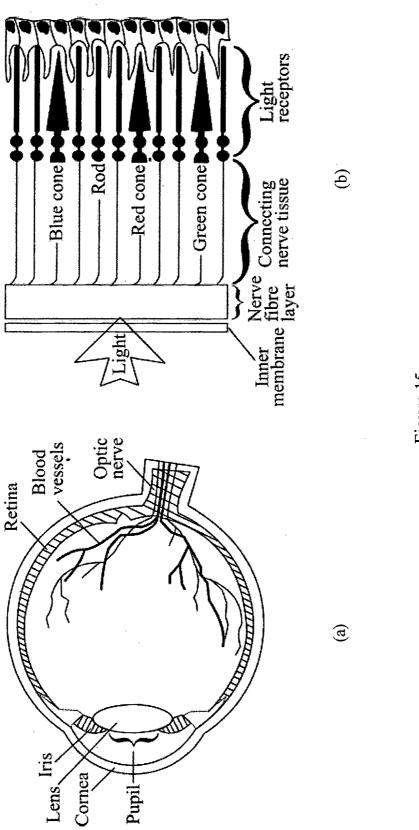
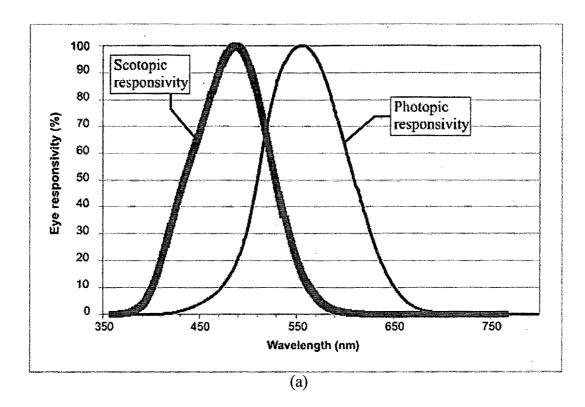


Figure 15

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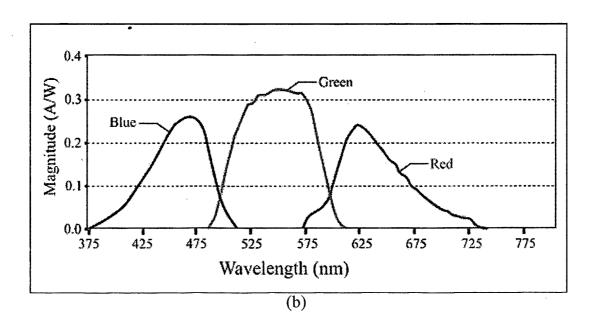
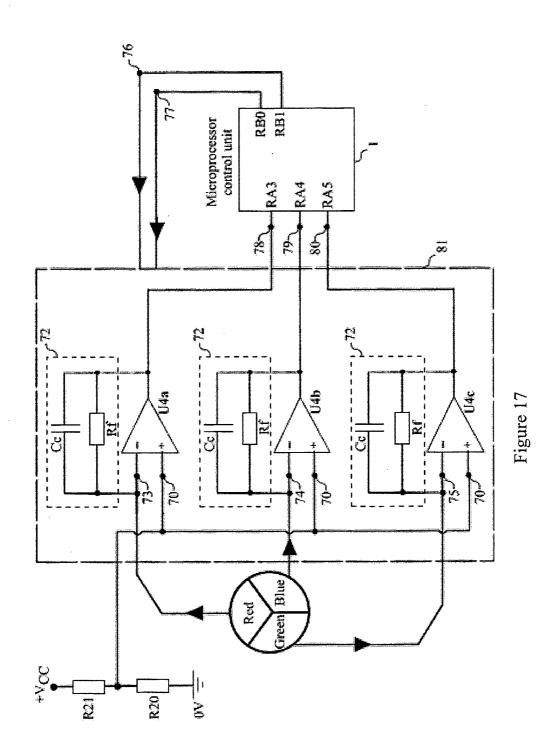
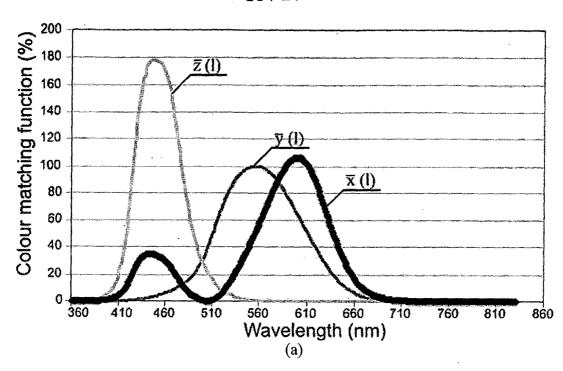


Figure 16







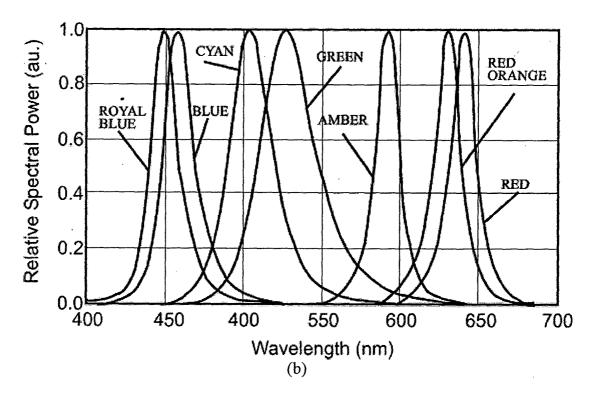


Figure 18

R Red P Purple
B Blue O Orange
G Green Pk Pink
Y Yellow
Lower Case: "ish" takes suffix

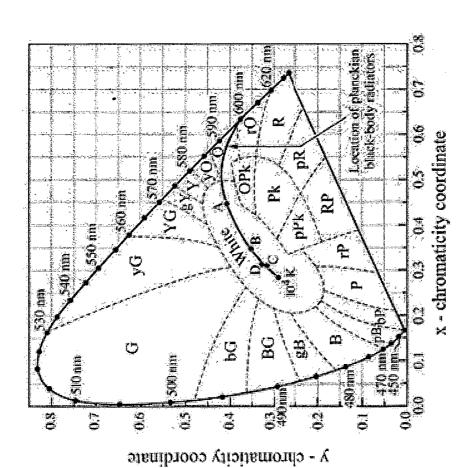
Illuminant A (x,y) = (0.4476, 0.4074) (Incandescent source, T=2856K)

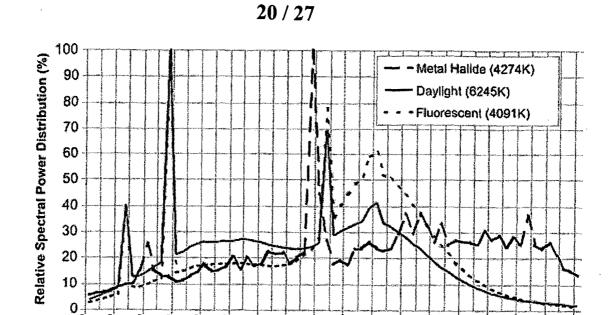
Illuminant B (x,y) = (0.3484, 0.3516)(Direct sunlight, T=4870K

Illuminant C (x,y) = (0.3101, 0.3162)(Overcast source, T=2856K)

Illuminant D (x,y) = (0.3128, 0.3292) (Daylight, T=6504K)

Figure 19





Wavelength (nm)
(a)

ARO ARO

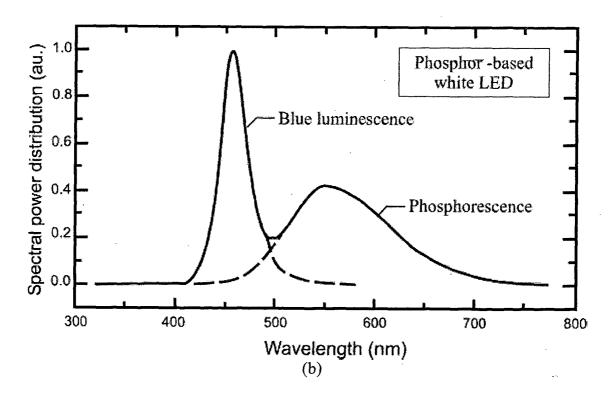


Figure 20

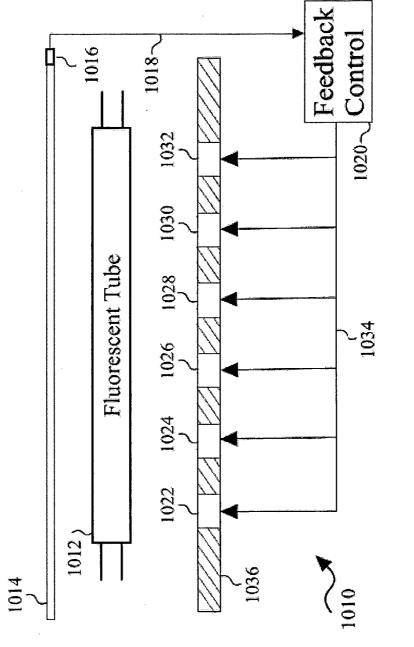


Figure 2

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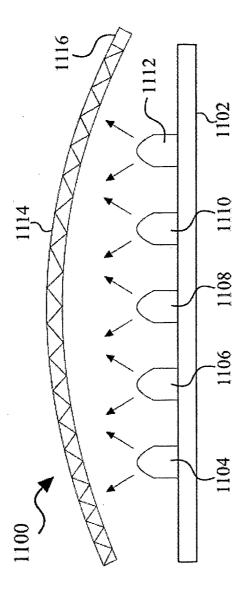
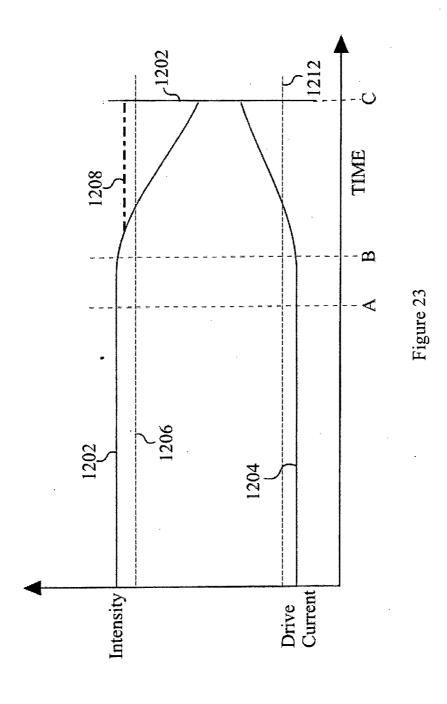


Figure 22



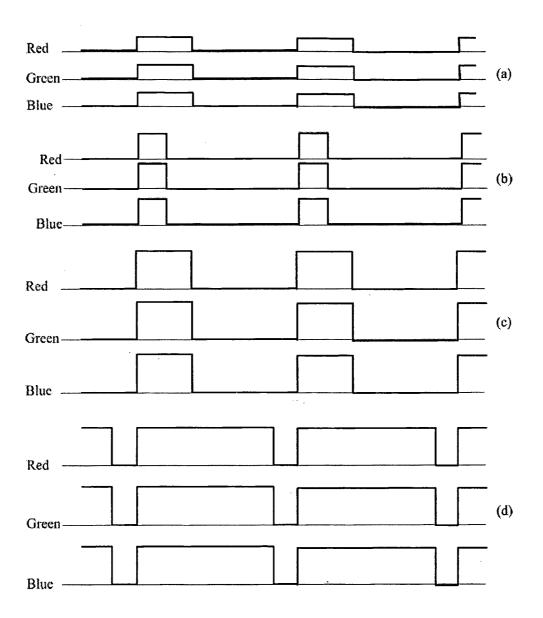


Figure 24

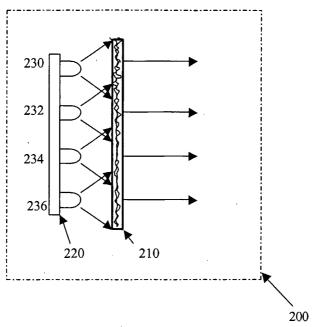


Figure 25

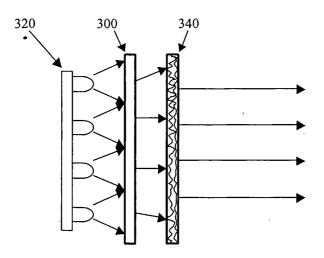


Figure 26

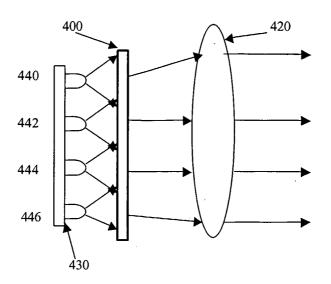


Figure 27

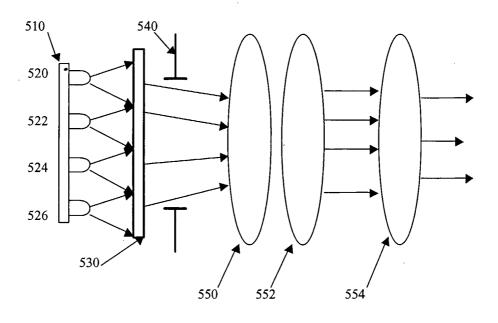


Figure 28

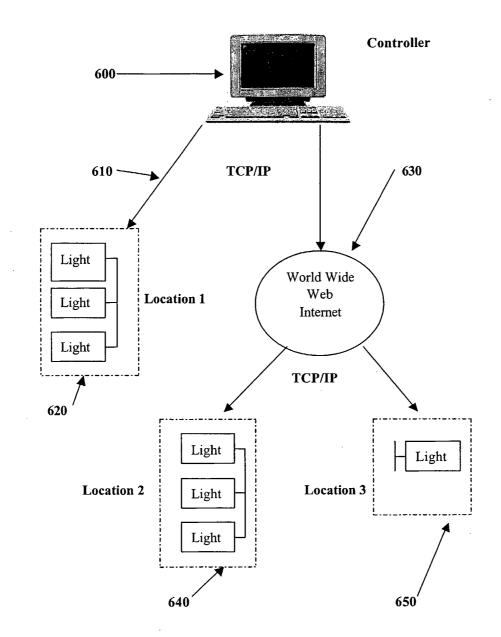


Figure 29

INTERNATIONAL SEARCH REPORT

International Application No
PCT/GB2005/001526

| A. CLASSI | FICATION OF SUBJECT MATTER H05B33/08 | | |
|--------------|--|---|-----------------------|
| | · | | |
| According to | o International Patent Classification (IPC) or to both national classifica | ation and IPC | |
| | SEARCHED | | |
| Minimum do | cumentation searched (classification system followed by classification $H05B$ | on symbols) | |
| | | | |
| Documentat | tion searched other than minimum documentation to the extent that s | uch documents are included in the fields sea | arched |
| | | | |
| Electronic d | ata base consulted during the international search (name of data base | se and, where practical, search terms used) | |
| EPO-In | ternal, PAJ, WPI Data | | |
| | | | |
| | | | |
| C. DOCUM | ENTS CONSIDERED TO BE RELEVANT | | |
| Category ° | Citation of document, with indication, where appropriate, of the rele | evant passages | Relevant to claim No. |
| Х | US 6 445 139 B1 (MARSHALL THOMAS | M ET AL) | 1-15, |
| , | 3 September 2002 (2002-09-03) | | 36-41 |
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| х | US 2001/024112 A1 (JACOBS RONNY A | ANDREAS | 1-15, |
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| | 27 September 2001 (2001-09-27) paragraphs [0001], [0006], [00 | 11]., | |
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| | the whole document | | |
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| | | | |
| | listed in the continuation of how C | X Patent family members are listed in | a appear |
| Ш | her documents are listed in the continuation of box C. | Patent family members are listed in | Tarriex. |
| ' | ategories of cited documents : | "T" later document published after the inter or priority date and not in conflict with t | the application but |
| consid | ent defining the general state of the art which is not dered to be of particular relevance | cited to understand the principle or the invention | ory underlying the |
| filing | | "X" document of particular relevance; the cl cannot be considered novel or cannot | be considered to |
| which | ent which may throw doubts on priority claim(s) or is cited to establish the publication date of another in or other special reason (as specified) | involve an inventive step when the doc "Y" document of particular relevance; the cl | aimed invention |
| "O" docum | ent referring to an oral disclosure, use, exhibition or | cannot be considered to involve an inv document is combined with one or mo ments, such combination being obviou | re other such docu- |
| "P" docume | means ent published prior to the international filing date but | in the art. "&" document member of the same patent f | • |
| | han the priority date claimed actual completion of the international search | Date of mailing of the international sear | |
| | | 25. 91 | 206 : |
| 1 | November 2005 | 44.3; | LGU.1 |
| Name and | mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 | Authorized officer | |
| | NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl, | llagar C | |
| | Fax: (+31-70) 340-3016 | Hagan, C | |

International application No. PCT/GB2005/001526

INTERNATIONAL SEARCH REPORT

| Box II Observations where certain claims were found unsearchable (Continuation of item 2 of first sheet) |
|---|
| This International Search Report has not been established in respect of certain claims under Article 17(2)(a) for the following reasons: |
| 1. Claims Nos.: because they relate to subject matter not required to be searched by this Authority, namely: |
| 2. Claims Nos.: because they relate to parts of the International Application that do not comply with the prescribed requirements to such an extent that no meaningful International Search can be carried out, specifically: |
| 3. Claims Nos.: because they are dependent claims and are not drafted in accordance with the second and third sentences of Rule 6.4(a). |
| Box III Observations where unity of invention is lacking (Continuation of item 3 of first sheet) |
| This International Searching Authority found multiple inventions in this international application, as follows: |
| see additional sheet |
| As all required additional search fees were timely paid by the applicant, this International Search Report covers all searchable claims. |
| 2. As all searchable claims could be searched without effort justifying an additional fee, this Authority did not invite payment of any additional fee. |
| 3. As only some of the required additional search fees were timely paid by the applicant, this International Search Report covers only those claims for which fees were paid, specifically claims Nos.: |
| 4. No required additional search fees were timely paid by the applicant. Consequently, this International Search Report is restricted to the invention first mentioned in the claims; it is covered by claims Nos.: |
| restricted to the invention first mentioned in the claims; it is covered by claims Nos.: 1-15, 36-41 |
| Remark on Protest The additional search fees were accompanied by the applicant's protest. No protest accompanied the payment of additional search fees. |

FURTHER INFORMATION CONTINUED FROM PCT/ISA/ 210

This International Searching Authority found multiple (groups of) inventions in this international application, as follows:

1. claims: 1-15, 36-41

Lighting fixture and control thereof using LEDs as light source with optical and temperature sensors feedback control of an LED driving circuit

2. claims: 16-18

Lighting fixture with feedback control for the reduction of transient effects on a feedback sensor output

3. claims: 19-21

Lighting fixture using LEDs and non-LEDs as light sources with optical sensor feedback control

4. claims: 22-25

Lighting fixture with thermoelectric cooling control

5. claims: 26-30, 42,43

Lighting fixture with output light-shaping

6. claims: 31, 32-35

Lighting control circuit for controlling the drive to a plurality of light emitters in response to ambient light.

INTERNATIONAL SEARCH REPORT

| International Application No | | | | | | | |
|------------------------------|----------|--|--|--|--|--|--|
| PCT/GB2005 | 5/001526 | | | | | | |

| Patent document cited in search report | Publication date | | Patent family member(s) | Publication date |
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| US 2001024112 A | 27-09-2001 | US CN WO | 6495964 B 1363198 A 0158218 A | 1 17-12-2002 |
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