LLOV: A Fast Static Data-Race Checker for OpenMP Programs



Utpal Bora

PhD Student Computer Science and Engineering IIT Hyderabad, India

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Definition (Data Race)

An execution of a concurrent program is said to have a *data race* when two different threads access the same memory location,

- these accesses are not protected by a mutual exclusion mechanism
- the order of the two accesses is non-deterministic
- one of these accesses is a write

Common race conditions in OpenMP programs



• Missing data sharing clauses

1	<pre>#pragma omp parallel for</pre>									
	<pre>private (temp,i,j)</pre>									
2	<pre>for (i = 0; i < len; i++)</pre>									
3	<pre>for (j = 0; j < len; j++)</pre>									
	{									
4	temp = u[i][j];									
5	<pre>sum = sum + temp * temp;</pre>									
6	}									

 $\mathsf{DRB021:}\ \mathsf{OpenMP}\ \mathsf{Worksharing}\ \mathsf{construct}\ \mathsf{with}\ \mathsf{data}\ \mathsf{race}$

Common race conditions in OpenMP programs



- Missing data sharing clauses
- Loop carried dependences

```
1 for (i=0;i<n;i++) {
2 #pragma omp parallel for
3 for (j=1;j<m;j++) {
4 b[i][j]=b[i][j-1];
5 }
6 }</pre>
```

DRB038: Example with Loop Carried Dependence



- Missing data sharing clauses
- Loop carried dependences
- SIMD races

1	#pragma omp simd
2	<pre>for (int i=0; i<len-1; i++){<="" pre=""></len-1;></pre>
3	a[i+1] = a[i] + b[i];
4	}

DRB024: Example with SIMD data race

Common race conditions in OpenMP programs



- Missing data sharing clauses
- Loop carried dependences
- SIMD races
- Synchronization issues

```
1 #pragma omp parallel shared(b,
error) {
2 #pragma omp for nowait
3 for(i = 0; i < len; i++)
4 a[i] = b + a[i]*5;
5 #pragma omp single
6 error = a[9] + 1;
7 }
```

DRB013: Example with data race due to improper synchronization



- Missing data sharing clauses
- Loop carried dependences
- SIMD races
- Synchronization issues
- Control flow dependent on number of threads

1	#pragma omp parallel		
2	<pre>if (omp_get_thread_num()</pre>	%	2
	== 0) {		
3	<pre>Flag = true;</pre>		
4	}		

Control flow dependent on number of threads



Tools	Infrastructure	Analysis Type
Helgrind [Vp07b]	Valgrind	Dynamic
VALGRIND DRD [Vp07a]	Valgrind	Dynamic
TSAN [SI09]	LLVM/GCC	Dynamic
Archer [AGR+16]	LLVM	Hybrid
SWORD [AGR+18]	LLVM	Dynamic
ROMP [GMC18]	Dyninst	Dynamic
PolyOMP [CSS15]	ROSE	Static
DRACO [YSL+18]	ROSE	Static
OMPVERIFY $[BYR^+11]$	AlphaZ	Static

Table: OpenMP Race Detection Tools: A Short Survey



Tools	Infrastructure	Analysis Type
Helgrind [Vp07b]	Valgrind	Dynamic
VALGRIND DRD [Vp07a]	Valgrind	Dynamic
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PolyOMP [CSS15]	ROSE	Static
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OMPVERIFY [BYR ⁺ 11]	AlphaZ	Static

Table: OpenMP Race Detection Tools: A Short Survey

There is still need for a static OpenMP data race checker in LLVM.



• Can detect races in SIMD constructs



- Can detect races in SIMD constructs
- Are independent of the runtime thread schedule



- Can detect races in SIMD constructs
- Are independent of the runtime thread schedule
- Are independent of the input size



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- Are independent of the runtime thread schedule
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- Are independent of the input size
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 $\rm LLOV$ is an attempt to bridge this gap and move towards a fast, language agnostic, robust, static OpenMP data race checker in LLVM.

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2 Architecture and Methodology









 $\rm LLOV$ is a language agnostic, static <code>OpenMP</code> data race checker in the <code>LLVM</code> compiler framework.



• based on Intermediate representation of LLVM (LLVM-IR)



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- uses Polyhedral framework, Polly, of LLVM



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- has all the advantages of a static data-race checker



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- can be extended for approximate dependences (like LAI of LLVM)



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- can detect that a program is race free
- has all the advantages of a static data-race checker
- can be extended for approximate dependences (like LAI of LLVM)
- has provision for handling entire OpenMP pragmas

LLOV Architecture





Figure: Flow Diagram of LLVM OpenMP Verifier (LLOV)

Methodology (with Example)





Example with Loop Carried Dependence



Figure: Dependence Polyhedra

Methodology (with Example)





Listing 1: Example with Loop Carried Dependence



Figure: Projection of the Dependence Polyhedra on i-dimension

Zero magnitude of the projections on a dimension signifies that the dimension is parallel.

Methodology (with Example)





Listing 2: Example with Loop Carried Dependence



Figure: Projection of the Dependence Polyhedra on j-dimension

Non-zero magnitude of the projections on a dimension signifies that the dimension is not parallel.

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D Motivation for LLOV

2 Architecture and Methodology









Benchmarks:

- DataRaceBench C/C++ v1.2 [LLA⁺18, LLSK18]
- OmpSCR v2.0 [Dor04, DRd05]
- DataRaceBench FORTRAN [KSB19]

System Specifications:

System: Two Intel Xeon E5-2697 v4 @ 2.30GHz processors OS: 64 bit Ubuntu 18.04.2 LTS server Kernel: Linux kernel version 4.15.0-48-generic Threads: 72 (2 x 36) hardware threads Memory: 128GB OpenMP library: LLVM OpenMP runtime v5.0.1 (libomp5)



Table: Race detection tools with the version numbers used for comparison

Tools	Source	Version / Commit
HELGRIND [Vp07b]	Valgrind	3.13.0
VALGRIND DRD [Vp07a]	Valgrind	3.13.0
TSAN-LLVM [SI09]	LLVM	6.0.1
Archer [AGR+16]	git master branch	fc17353
SWORD [AGR+18]	git master branch	7a08f3c
ROMP [GMC18]	git master branch	6a0ad6d

Results: DataRaceBench v1.2 comparison



Table: Maximum number of Races reported by different tools in DataRaceBench 1.2

Tools	Race: Yes		Race: No		Coverage/116
10015	TP	FN	ΤN	FP	Coverage/110
Helgrind	56	3	2	55	116
VALGRIND DRD	56	3	26	31	116
TSAN-LLVM	57	2	2	55	116
Archer	56	3	2	55	116
SWORD	47	4	24	4	79
LLOV	45	3	28	9	85

Results: DataRaceBench v1.2 comparison



Tools	Race: Yes		Race: No		Coverage/116
TOOIS	TΡ	FN	ΤN	FP	Coverage/110
Helgrind	56	3	2	55	116
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Table: Maximum number of Races reported by different tools in DataRaceBench 1.2

Table: Maximum number of Races reported by different tools in common 66 kernels of DataRaceBench 1.2 $\,$

Teels	Race	: Yes	Race	: No	Course 70 /116
TOOIS	TΡ	FN	ΤN	FP	Coverage/110
Helgrind	46	1	2	17	66
VALGRIND DRD	46	1	13	6	66
TSAN-LLVM	46	1	2	17	66
Archer	46	1	2	17	66
SWORD	46	1	18	1	66
LLOV	44	3	16	3	66

Results: DataRaceBench v1.2 statistics



Tools	Precision	Recall	Accuracy	F1 Score	Diagnostic odds ratio
Helgrind	0.50	0.95	0.50	0.66	0.68
VALGRIND DRD	0.64	0.95	0.71	0.77	15.66
TSAN-LLVM	0.51	0.97	0.51	0.67	1.04
Archer	0.50	0.95	0.50	0.66	0.68
SWORD	0.92	0.92	0.90	0.92	70.50
LLOV	0.83	0.94	0.86	0.88	46.67

Table: Precision, Recall and Accuracy of the tools on DataRaceBench 1.2

Results: DataRaceBench v1.2 statistics



Tools	Precision	Recall	Accuracy	F1 Score	Diagnostic odds ratio
Helgrind	0.50	0.95	0.50	0.66	0.68
Valgrind DRD	0.64	0.95	0.71	0.77	15.66
TSAN-LLVM	0.51	0.97	0.51	0.67	1.04
Archer	0.50	0.95	0.50	0.66	0.68
SWORD	0.92	0.92	0.90	0.92	70.50
LLOV	0.83	0.94	0.86	0.88	46.67

Table: Precision, Recall and Accuracy of the tools on DataRaceBench 1.2

Table: Precision, Recall and Accuracy of the tools on common 66 kernels of DataRaceBench 1.2

Tools	Precision	Recall	Accuracy	F1 Score	Diagnostic odds ratio
Helgrind	0.73	0.98	0.73	0.84	5.41
Valgrind DRD	0.88	0.98	0.89	0.93	99.67
TSAN-LLVM	0.73	0.98	0.73	0.84	5.41
Archer	0.73	0.98	0.73	0.84	5.41
SWORD	0.98	0.98	0.97	0.98	828.00
LLOV	0.94	0.94	0.91	0.94	78.22

Results: DataRaceBench v1.2 runtime





Figure: DataRaceBench v1.2 total time taken by different tools for all 116 kernels on logarithmic scale

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Results: DataRaceBench v1.2 runtime





Figure: DataRaceBench v1.2 total time taken by different tools for common 66 kernels on logarithmic scale

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LLVM-Performance@CGO20

Results: OmpSCR v2.0 race conditions



Table: Number of Races detected in OmpSCR v2.0 benchmark (CT is Compilation Timeout)

Kernel	LLOV	Helgrind	DRD	TSAN	Archer	SWORD
Manually verified kernels with data races						
c_loopA.badSolution	1	1	1	1	1	1
c_loopA.solution2	1	1	1	1	1	0
c_loopA.solution3	1	1	1	1	1	0
c_loopB.badSolution1	1	1	1	1	1	1
c_loopB.badSolution2	1	1	1	1	1	1
c_loopB.pipelineSolution	1	1	1	1	1	0
c_md	1	2	2	2	1	СТ
c_lu	1	1	1	1	1	0
Manually verified race free kernels						
c_loopA.solution1	0	2	1	2	1	0
c_mandel	0	1	0	1	1	0
c_pi	0	1	0	1	1	0
c_jacobi01	1	2	1	0	0	CT
c_jacobi02	1	1	1	0	0	СТ
c_jacobi03	0	1	1	0	0	СТ
Unverified kernels						
c_fft	1	1	1	1	1	СТ
c_fft6	1	1	0	1	1	СТ
c_qsort	0	1	1	1	1	СТ
c_GraphSearch	0	0	0	0	0	0
cpp_qsomp1	0	0	0	0	0	0
cpp_qsomp2	0	0	0	0	0	0
cpp_qsomp3	0	0	0	0	0	0
cpp_qsomp4	0	0	0	0	0	0
cpp_qsomp5	0	0	0	0	0	0
срр_qsompб	0	0	0	0	0	0
cpp_qsomp7	0	0	0	0	0	0

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Results: OmpSCR v2.0 runtime





Figure: OmpSCR v2.0 total execution time by different tools on logarithmic scale

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An implementation of DataRaceBench C/C++ v1.2 [LLSK18] in FORTRAN 95.

- \bullet Converted 92 (out of 116) C/C++ kernels to FORTRAN
- $\bullet\,$ Demonstrate that $\rm LLOV$ is language agnostic
- Already open-sourced this benchmark [KSB19]



Table: Maximum number of Races reported by different tools in DataRaceBench FORTRAN

Tools	Race: Yes		Race: No		Coverage /02
10015	TP	FN	ΤN	FP	Coverage/ 92
Helgrind	46	6	4	36	92
VALGRIND DRD	45	7	21	19	92
LLOV	34	6	19	5	64

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OpenMP v4.5 Pragma Handling Status: Various Tools



Table: Comparison of OpenMP pragma handling by OpenMP aware tools. (Y for Yes, N for No)

OpenMP Pragma	LLOV	PolyOMP	DRACO	SWORD
#pragma omp parallel	Y	Y	Y	Y
#pragma omp for	Y	Y	Y	Y
#pragma omp parallel for	Y	Y	Y	Y
#pragma omp atomic	Y	N	N	Y
#pragma omp threadprivate	Y	N	N	N
#pragma omp master	Y	N	N	Y
#pragma omp single	Y	N	N	Y
#pragma omp simd	Y	N	Y	N
#pragma omp parallel for simd	Y	N	Y	N
#pragma omp distribute	Y	N	N	N
#pragma omp ordered	Y	N	N	N
#pragma omp critical	Y	N	N	Y
#pragma omp parallel sections	N	N	N	Y
#pragma omp sections	N	N	N	Y
#pragma omp declare reduction	N	N	N	N
#pragma omp task	N	N	N	N
#pragma omp taskgroup	N	N	N	N
#pragma omp taskloop	N	N	N	N
#pragma omp taskwait	N	N	N	N
#pragma omp teams	N	N	N	N
#pragma omp barrier	N	N	N	Y
#pragma omp target map	N	N	N	N

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Working on

• Use approximate dependece analysis (LAI) [Gro19] of LLVM



Working on

- Use approximate dependece analysis (LAI) [Gro19] of LLVM
- Increase coverage- handle more OpenMP pragmas



Working on

- Use approximate dependece analysis (LAI) [Gro19] of LLVM
- Increase coverage- handle more OpenMP pragmas
- Use May-Happen-in-Parallel analysis for race detection



Open source links:

- DataRaceBench FORTRAN: https://github.com/IITH-Compilers/drb_fortran
- LLOV: Please drop me an email at cs14mtech11017@iith.ac.in

We welcome your contributions in any form.



Johannes Doerfert Tobias Grosser GSoC mentors for "Polly as a pass in LLVM" LLVM Community

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Thank You!

$\operatorname{LLOV}:$ Race Detection Algorithm



		4	Algorithm 2: Algorithm to check			
Algorithm 1: Race Detection			parallelism			
Algorithm		- L	Input: RDG. dim			
Input: L		C	Dutput: True/False			
Output: result		1 F	Function isParallel(RDG, dim):			
<pre>1 Function isRaceFree(L):</pre>		2	if RDG is Empty then			
2	SCoP = ConstructSCoP(L);	3	return True ;			
3	RDG =	4	else			
	ComputeDependences(SCoP)	5	Flag = True;			
	;	6	while Dependence D in RDG			
4	depth = GetLoopDepth(L);		do			
5	if isParallel(RDG, depth) then	7	D' = Project Out first <i>dim</i>			
6	result = "Program is race		dimensions from D ;			
	free.";	8	if D' is Empty then			
7	else	9	continue ;			
8	result = "Data Race	10	else			
	detected.";	11	Flag = False;			
9	return result	12	break ;			
0	End Function		return Flag ;			
		14 E	End Function			
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Terminology I



- True Positive (TP): If the evaluation tool correctly detects a data race present in the kernel it is a True Positive test result. A higher number of true positives represents a better tool.
- **True Negative (TN):** If the benchmark does not contain a race and the tool declares it as race-free, then it is a true negative case. A higher number of true negatives represents a better tool.
- False Positives (FP): If the benchmark does not contain any race, but the tool reports a race condition, it is a false positive. False Positives should be as low as possible.
- False Negatives (FN): False Negative test result is obtained when the tool fails to detect a known race in the benchmark. These are the cases that are missed by the tool. A lower number of false negatives are desirable.

Terminology II



- **Precision :** Precision is the measure of closeness of the outcomes of prediction. Thus, a higher value of precision represents that the tool will more often than not identify a race condition when it exists. $Precision = \frac{TP}{TP + FP}$
- **Recall :** Recall gives the total number of cases detected out of the maximum data races present. A higher recall value means that there are less chances that a data race is missed by the tool. It is also called true positive rate (TPR). $Recall = \frac{TP}{TP + FN}$
- Accuracy : Accuracy gives the chances of correct reports out of all the reports, as the name suggests. A higher value of accuracy is always desired and gives overall measure of the efficacy of the tool. $Accuracy = \frac{TP + TN}{TP + FP + TN + FN}$

Terminology III



• **F1 Score :** The harmonic mean of precision and recall is called the F1 score. An F1 score of 1 can be achieved in the best case when both precision and recall are perfect. The worst case F1 score is 0 when either precision or recall is 0.

 $F1 Score = 2 * \frac{Precision * Recall}{Precision + Recall}$

• **Diagnostic odds ratio (DOR) :** It is the ratio of the positive likelihood ratio (LR+) to the negative likelihood ratio (LR-). $DOR = \frac{LR+}{LR-}$ where,

Positive Likelihood Ratio $(LR+) = \frac{TPR}{FPR}$, Negative Likelihood Ratio $(LR-) = \frac{FNR}{TNR}$, True Positive Rate $(TPR) = \frac{TP}{TP+FN}$, False Positive Rate $(FPR) = \frac{FP}{FP+TN}$, False Negative Rate $(FNR) = \frac{FN}{FN+TP}$ and True Negative Rare $(TNR) = \frac{TN}{TN+FP}$



DOR is the measure of the ratio of the odds of race detection being positive given that the test case has a data race, to the odds of race detection being positive given the test case does not have a race.