====START OF HERA REGRESSION TESTING LOG====

UFSWM hash used in testing:

e779edb51e67b6c780c85f5737005b4d3a1b5ab4

Submodule hashes used in testing:

 37cbb7d6840ae7515a9a8f0dfd4d89461b3396d1 AQM (v0.2.0-37-g37cbb7d)

 3d7067a523b8557058755289e4275f5f5c985daf CDEPS-interface/CDEPS (cdeps0.4.17-40-g3d7067a)

 c9e4679f449e30bb4cc0f22164b4401a8b50f7a6 CICE-interface/CICE (CICE6.0.0-447-gc9e4679)

 4e19850cb083bc474b7cde5dc2f8506ec74cc442 CMEPS-interface/CMEPS (cmeps_v0.4.1-2306-g4e19850)

 cabd7753ae17f7bfcc6dad56daf10868aa51c3f4 CMakeModules (v1.0.0-28-gcabd775)

 683061b3fd58716625eed61d7a7143e985f733d4 FV3 (heads/develop)

 041422934cae1570f2f0e67239d5d89f11c6e1b7 GOCART (sdr_v2.1.2.6-119-g0414229)

 35789c757766e07f688b4c0c7c5229816f224b09 HYCOM-interface/HYCOM (2.3.00-121-g35789c7)

 129e1bda02d454fb280819d1d87ae16347fd044c MOM6-interface/MOM6 (dev/master/repository_split_2014.10.10-10049-g129e1bda0)

 1e259014c1eba9070cec7027d8b4b479ae54275a NOAHMP-interface/noahmp (v3.7.1-430-g1e25901)

 d9b3172f4197c65d471662c6952a668152d71230 WW3 (6.07.1-345-gd9b3172f)

 31e4e3e57bad8a7cdaa0ce2d3c4efbca63b5f74a stochastic_physics (ufs-v2.0.0-216-g31e4e3e)

NOTES:

[Times](Memory) are at the end of each compile/test in format [MM:SS](Size).

The first time is for the full script (prep+run+finalize).

The second time is specifically for the run phase.

Times/Memory will be empty for failed tests.

BASELINE DIRECTORY: /scratch2/NAGAPE/epic/UFS-WM_RT/NEMSfv3gfs/develop-20240503

COMPARISON DIRECTORY: /scratch1/NCEPDEV/stmp2/Jiande.Wang/FV3_RT/rt_2246189

RT.SH OPTIONS USED:

* (-a) - HPC PROJECT ACCOUNT: marine-cpu

* (-e) - USE ECFLOW

PASS -- COMPILE 's2swa_32bit_intel' [14:12, 13:17] (2 warnings 8 remarks)

PASS -- TEST 'cpld_control_p8_mixedmode_intel' [07:21, 05:33](3188 MB)

PASS -- COMPILE 's2swa_32bit_pdlib_intel' [17:11, 16:12] (2 warnings 8 remarks)

PASS -- TEST 'cpld_control_gfsv17_intel' [21:12, 17:18](1753 MB)

PASS -- TEST 'cpld_control_gfsv17_iau_intel' [20:22, 17:54](2014 MB)

PASS -- TEST 'cpld_restart_gfsv17_intel' [11:14, 08:09](1120 MB)

PASS -- TEST 'cpld_mpi_gfsv17_intel' [24:14, 19:20](1652 MB)

PASS -- COMPILE 's2swa_32bit_pdlib_debug_intel' [05:12, 04:44] (1526 warnings 1998 remarks)

PASS -- TEST 'cpld_debug_gfsv17_intel' [24:12, 22:29](1702 MB)

PASS -- COMPILE 's2swa_intel' [14:12, 13:15] (1 warnings 8 remarks)

PASS -- TEST 'cpld_control_p8_intel' [08:18, 05:49](3217 MB)

PASS -- TEST 'cpld_control_p8.v2.sfc_intel' [09:27, 05:57](3230 MB)

PASS -- TEST 'cpld_restart_p8_intel' [06:22, 03:29](3263 MB)

PASS -- TEST 'cpld_control_qr_p8_intel' [07:17, 05:57](3214 MB)

PASS -- TEST 'cpld_restart_qr_p8_intel' [05:37, 03:30](3280 MB)

PASS -- TEST 'cpld_2threads_p8_intel' [07:16, 05:32](3560 MB)

PASS -- TEST 'cpld_decomp_p8_intel' [08:16, 06:08](3211 MB)

PASS -- TEST 'cpld_mpi_p8_intel' [07:16, 04:52](3088 MB)

PASS -- TEST 'cpld_control_ciceC_p8_intel' [08:27, 05:48](3232 MB)

PASS -- TEST 'cpld_control_c192_p8_intel' [17:22, 10:13](3352 MB)

PASS -- TEST 'cpld_restart_c192_p8_intel' [11:00, 06:15](3626 MB)

PASS -- TEST 'cpld_bmark_p8_intel' [21:22, 09:45](4122 MB)

PASS -- TEST 'cpld_restart_bmark_p8_intel' [16:49, 06:31](4359 MB)

PASS -- TEST 'cpld_s2sa_p8_intel' [07:16, 05:25](3180 MB)

PASS -- COMPILE 's2sw_intel' [13:11, 12:19] (1 warnings 8 remarks)

PASS -- TEST 'cpld_control_noaero_p8_intel' [06:55, 04:48](1742 MB)

PASS -- TEST 'cpld_control_nowave_noaero_p8_intel' [06:04, 04:20](1802 MB)

PASS -- COMPILE 's2swa_debug_intel' [05:11, 04:42] (1451 warnings 1228 remarks)

PASS -- TEST 'cpld_debug_p8_intel' [10:19, 08:24](3262 MB)

PASS -- COMPILE 's2sw_debug_intel' [05:11, 04:28] (1451 warnings 1228 remarks)

PASS -- TEST 'cpld_debug_noaero_p8_intel' [07:08, 05:38](1759 MB)

PASS -- COMPILE 's2s_aoflux_intel' [12:12, 11:38] (1 warnings 1 remarks)

PASS -- TEST 'cpld_control_noaero_p8_agrid_intel' [06:09, 04:25](1786 MB)

PASS -- COMPILE 's2s_intel' [12:11, 11:31] (1 warnings 1 remarks)

PASS -- TEST 'cpld_control_c48_intel' [10:48, 09:21](2829 MB)

PASS -- TEST 'cpld_warmstart_c48_intel' [03:45, 02:35](2833 MB)

PASS -- TEST 'cpld_restart_c48_intel' [02:47, 01:26](2301 MB)

PASS -- COMPILE 's2swa_faster_intel' [17:11, 16:29] (1 warnings 8 remarks)

PASS -- TEST 'cpld_control_p8_faster_intel' [09:33, 05:31](3235 MB)

PASS -- COMPILE 's2sw_pdlib_intel' [16:11, 15:48] (1 warnings 8 remarks)

PASS -- TEST 'cpld_control_pdlib_p8_intel' [19:08, 17:14](1777 MB)

PASS -- TEST 'cpld_restart_pdlib_p8_intel' [10:17, 08:10](1180 MB)

PASS -- TEST 'cpld_mpi_pdlib_p8_intel' [21:09, 19:42](1683 MB)

PASS -- COMPILE 's2sw_pdlib_debug_intel' [05:11, 04:24] (1561 warnings 1998 remarks)

PASS -- TEST 'cpld_debug_pdlib_p8_intel' [25:59, 24:59](1694 MB)

PASS -- COMPILE 'atm_dyn32_intel' [12:12, 11:13] (2 warnings 1 remarks)

PASS -- TEST 'control_flake_intel' [06:23, 03:26](708 MB)

PASS -- TEST 'control_CubedSphereGrid_intel' [07:51, 02:56](1604 MB)

PASS -- TEST 'control_CubedSphereGrid_parallel_intel' [08:58, 03:01](1615 MB)

PASS -- TEST 'control_latlon_intel' [07:48, 03:04](1605 MB)

PASS -- TEST 'control_wrtGauss_netcdf_parallel_intel' [07:55, 03:00](1605 MB)

PASS -- TEST 'control_c48_intel' [14:55, 07:37](1770 MB)

PASS -- TEST 'control_c48.v2.sfc_intel' [08:27, 06:24](876 MB)

PASS -- TEST 'control_c192_intel' [16:08, 10:46](1752 MB)

PASS -- TEST 'control_c384_intel' [14:05, 10:44](2014 MB)

PASS -- TEST 'control_c384gdas_intel' [15:06, 08:18](1406 MB)

PASS -- TEST 'control_stochy_intel' [05:24, 01:38](664 MB)

PASS -- TEST 'control_stochy_restart_intel' [01:27, 00:58](510 MB)

PASS -- TEST 'control_lndp_intel' [06:26, 01:33](663 MB)

PASS -- TEST 'control_iovr4_intel' [05:26, 02:28](657 MB)

PASS -- TEST 'control_iovr5_intel' [05:26, 02:27](659 MB)

PASS -- TEST 'control_p8_intel' [06:01, 02:59](1638 MB)

PASS -- TEST 'control_p8.v2.sfc_intel' [05:58, 02:57](1639 MB)

PASS -- TEST 'control_p8_ugwpv1_intel' [04:55, 02:51](1633 MB)

PASS -- TEST 'control_restart_p8_intel' [03:53, 01:39](897 MB)

PASS -- TEST 'control_noqr_p8_intel' [05:50, 02:54](1626 MB)

PASS -- TEST 'control_restart_noqr_p8_intel' [02:56, 01:36](942 MB)

PASS -- TEST 'control_decomp_p8_intel' [04:45, 03:00](1612 MB)

PASS -- TEST 'control_2threads_p8_intel' [04:43, 02:44](1716 MB)

PASS -- TEST 'control_p8_lndp_intel' [06:46, 05:13](1635 MB)

PASS -- TEST 'control_p8_rrtmgp_intel' [04:56, 03:54](1701 MB)

PASS -- TEST 'control_p8_mynn_intel' [03:53, 02:58](1635 MB)

PASS -- TEST 'merra2_thompson_intel' [04:53, 03:20](1648 MB)

PASS -- TEST 'regional_control_intel' [06:44, 05:14](852 MB)

PASS -- TEST 'regional_restart_intel' [03:41, 02:45](1024 MB)

PASS -- TEST 'regional_decomp_intel' [06:46, 05:30](845 MB)

PASS -- TEST 'regional_2threads_intel' [04:47, 03:15](847 MB)

PASS -- TEST 'regional_noquilt_intel' [05:52, 05:09](1364 MB)

PASS -- TEST 'regional_netcdf_parallel_intel' [06:47, 05:12](850 MB)

PASS -- TEST 'regional_2dwrtdecomp_intel' [06:46, 05:13](852 MB)

PASS -- TEST 'regional_wofs_intel' [07:43, 06:43](1915 MB)

PASS -- COMPILE 'rrfs_intel' [11:12, 10:46] (4 warnings 9 remarks)

PASS -- TEST 'rap_control_intel' [08:41, 07:48](1112 MB)

PASS -- TEST 'regional_spp_sppt_shum_skeb_intel' [07:00, 04:13](1300 MB)

PASS -- TEST 'rap_decomp_intel' [10:37, 08:05](1041 MB)

PASS -- TEST 'rap_2threads_intel' [09:35, 07:14](1178 MB)

PASS -- TEST 'rap_restart_intel' [06:46, 04:03](1105 MB)

PASS -- TEST 'rap_sfcdiff_intel' [09:39, 07:43](1112 MB)

PASS -- TEST 'rap_sfcdiff_decomp_intel' [09:41, 08:08](1040 MB)

PASS -- TEST 'rap_sfcdiff_restart_intel' [08:50, 05:54](1131 MB)

PASS -- TEST 'hrrr_control_intel' [05:35, 03:59](1046 MB)

PASS -- TEST 'hrrr_control_decomp_intel' [05:34, 04:06](1034 MB)

PASS -- TEST 'hrrr_control_2threads_intel' [04:36, 03:38](1112 MB)

PASS -- TEST 'hrrr_control_restart_intel' [03:33, 02:08](1004 MB)

PASS -- TEST 'rrfs_v1beta_intel' [08:50, 07:37](1104 MB)

PASS -- TEST 'rrfs_v1nssl_intel' [10:27, 09:23](1994 MB)

PASS -- TEST 'rrfs_v1nssl_nohailnoccn_intel' [09:29, 09:04](2063 MB)

PASS -- COMPILE 'csawmg_intel' [11:12, 10:29] (2 warnings)

PASS -- TEST 'control_csawmg_intel' [06:48, 06:00](759 MB)

PASS -- TEST 'control_ras_intel' [04:26, 03:14](757 MB)

PASS -- COMPILE 'csawmg_gnu' [04:12, 03:46]

PASS -- TEST 'control_csawmg_gnu' [09:44, 08:17](548 MB)

PASS -- COMPILE 'wam_intel' [11:12, 09:55] (1 warnings)

PASS -- TEST 'control_wam_intel' [02:23, 02:02](661 MB)

PASS -- COMPILE 'atm_faster_dyn32_intel' [11:12, 10:51] (1 warnings 1 remarks)

PASS -- TEST 'control_p8_faster_intel' [03:56, 02:36](1633 MB)

PASS -- TEST 'regional_control_faster_intel' [05:42, 04:43](855 MB)

PASS -- COMPILE 'atm_debug_dyn32_intel' [05:11, 04:54] (886 warnings 9 remarks)

PASS -- TEST 'control_CubedSphereGrid_debug_intel' [03:45, 02:34](1607 MB)

PASS -- TEST 'control_wrtGauss_netcdf_parallel_debug_intel' [03:47, 02:34](1622 MB)

PASS -- TEST 'control_stochy_debug_intel' [04:21, 03:01](820 MB)

PASS -- TEST 'control_lndp_debug_intel' [03:21, 02:40](820 MB)

PASS -- TEST 'control_csawmg_debug_intel' [04:39, 04:08](872 MB)

PASS -- TEST 'control_ras_debug_intel' [03:21, 02:44](828 MB)

PASS -- TEST 'control_diag_debug_intel' [03:52, 02:39](1672 MB)

PASS -- TEST 'control_debug_p8_intel' [04:47, 02:48](1643 MB)

PASS -- TEST 'regional_debug_intel' [21:46, 17:15](845 MB)

PASS -- TEST 'rap_control_debug_intel' [05:24, 04:52](1211 MB)

PASS -- TEST 'hrrr_control_debug_intel' [08:24, 04:39](1209 MB)

PASS -- TEST 'hrrr_gf_debug_intel' [08:24, 04:51](1216 MB)

PASS -- TEST 'hrrr_c3_debug_intel' [09:24, 04:45](1214 MB)

PASS -- TEST 'rap_unified_drag_suite_debug_intel' [09:24, 04:49](1213 MB)

PASS -- TEST 'rap_diag_debug_intel' [09:41, 05:03](1293 MB)

PASS -- TEST 'rap_cires_ugwp_debug_intel' [08:29, 04:53](1208 MB)

PASS -- TEST 'rap_unified_ugwp_debug_intel' [08:23, 04:55](1213 MB)

PASS -- TEST 'rap_lndp_debug_intel' [08:28, 04:57](1202 MB)

PASS -- TEST 'rap_progcld_thompson_debug_intel' [08:25, 04:57](1214 MB)

PASS -- TEST 'rap_noah_debug_intel' [07:28, 04:51](1205 MB)

PASS -- TEST 'rap_sfcdiff_debug_intel' [07:27, 04:53](1212 MB)

PASS -- TEST 'rap_noah_sfcdiff_cires_ugwp_debug_intel' [10:26, 07:54](1205 MB)

PASS -- TEST 'rrfs_v1beta_debug_intel' [07:26, 04:42](1209 MB)

PASS -- TEST 'rap_clm_lake_debug_intel' [08:24, 05:47](1216 MB)

PASS -- TEST 'rap_flake_debug_intel' [07:25, 05:08](1211 MB)

PASS -- TEST 'gnv1_c96_no_nest_debug_intel' [10:40, 08:36](1210 MB)

PASS -- COMPILE 'atm_debug_dyn32_gnu' [03:11, 02:44]

PASS -- TEST 'control_csawmg_debug_gnu' [03:38, 02:12](531 MB)

PASS -- COMPILE 'wam_debug_intel' [04:11, 03:46] (856 warnings)

PASS -- TEST 'control_wam_debug_intel' [06:25, 05:00](524 MB)

PASS -- COMPILE 'rrfs_dyn32_phy32_intel' [11:11, 10:14] (4 warnings 8 remarks)

PASS -- TEST 'regional_spp_sppt_shum_skeb_dyn32_phy32_intel' [05:06, 03:55](1168 MB)

PASS -- TEST 'rap_control_dyn32_phy32_intel' [07:42, 06:32](1054 MB)

PASS -- TEST 'hrrr_control_dyn32_phy32_intel' [04:45, 03:23](988 MB)

PASS -- TEST 'rap_2threads_dyn32_phy32_intel' [07:37, 06:10](1091 MB)

PASS -- TEST 'hrrr_control_2threads_dyn32_phy32_intel' [07:36, 03:11](966 MB)

PASS -- TEST 'hrrr_control_decomp_dyn32_phy32_intel' [04:37, 03:38](927 MB)

PASS -- TEST 'rap_restart_dyn32_phy32_intel' [05:44, 04:50](1041 MB)

PASS -- TEST 'hrrr_control_restart_dyn32_phy32_intel' [04:32, 01:53](935 MB)

PASS -- COMPILE 'rrfs_dyn32_phy32_faster_intel' [13:11, 12:30] (4 warnings 8 remarks)

PASS -- TEST 'conus13km_control_intel' [04:01, 02:10](1212 MB)

PASS -- TEST 'conus13km_2threads_intel' [03:46, 00:53](1122 MB)

PASS -- TEST 'conus13km_restart_mismatch_intel' [03:50, 01:15](1114 MB)

PASS -- COMPILE 'rrfs_dyn64_phy32_intel' [15:11, 10:04] (4 warnings 8 remarks)

PASS -- TEST 'rap_control_dyn64_phy32_intel' [05:43, 04:11](989 MB)

PASS -- COMPILE 'rrfs_dyn32_phy32_debug_intel' [08:12, 03:34] (788 warnings 8 remarks)

PASS -- TEST 'rap_control_debug_dyn32_phy32_intel' [06:28, 04:45](1090 MB)

PASS -- TEST 'hrrr_control_debug_dyn32_phy32_intel' [06:28, 04:44](1093 MB)

PASS -- TEST 'conus13km_debug_intel' [16:59, 14:34](1239 MB)

PASS -- TEST 'conus13km_debug_qr_intel' [16:54, 14:28](933 MB)

PASS -- TEST 'conus13km_debug_2threads_intel' [09:55, 08:21](1157 MB)

PASS -- TEST 'conus13km_radar_tten_debug_intel' [17:52, 14:37](1305 MB)

PASS -- COMPILE 'rrfs_dyn64_phy32_debug_intel' [07:11, 03:29] (788 warnings 8 remarks)

PASS -- TEST 'rap_control_dyn64_phy32_debug_intel' [07:27, 04:56](1130 MB)

PASS -- COMPILE 'hafsw_intel' [15:11, 11:42] (2 warnings 8 remarks)

PASS -- TEST 'hafs_regional_atm_intel' [07:21, 05:09](739 MB)

PASS -- TEST 'hafs_regional_atm_thompson_gfdlsf_intel' [08:39, 06:42](1117 MB)

PASS -- TEST 'hafs_regional_atm_ocn_intel' [13:40, 07:01](827 MB)

PASS -- TEST 'hafs_regional_atm_wav_intel' [19:23, 13:31](866 MB)

PASS -- TEST 'hafs_regional_atm_ocn_wav_intel' [17:32, 15:15](880 MB)

PASS -- TEST 'hafs_regional_1nest_atm_intel' [11:03, 05:35](502 MB)

PASS -- TEST 'hafs_regional_telescopic_2nests_atm_intel' [12:33, 07:07](516 MB)

PASS -- TEST 'hafs_global_1nest_atm_intel' [03:43, 02:41](372 MB)

PASS -- TEST 'hafs_global_multiple_4nests_atm_intel' [13:43, 07:28](476 MB)

PASS -- TEST 'hafs_regional_specified_moving_1nest_atm_intel' [06:50, 03:56](530 MB)

PASS -- TEST 'hafs_regional_storm_following_1nest_atm_intel' [06:54, 03:38](531 MB)

PASS -- TEST 'hafs_regional_storm_following_1nest_atm_ocn_intel' [05:58, 04:13](588 MB)

PASS -- TEST 'hafs_global_storm_following_1nest_atm_intel' [03:29, 01:14](402 MB)

PASS -- TEST 'gnv1_nested_intel' [05:45, 04:21](801 MB)

PASS -- COMPILE 'hafsw_debug_intel' [07:11, 04:02] (1468 warnings 1501 remarks)

PASS -- TEST 'hafs_regional_storm_following_1nest_atm_ocn_debug_intel' [13:53, 13:00](583 MB)

PASS -- COMPILE 'hafsw_faster_intel' [13:11, 11:57] (1 warnings 7 remarks)

PASS -- TEST 'hafs_regional_storm_following_1nest_atm_ocn_wav_intel' [10:04, 08:54](658 MB)

PASS -- TEST 'hafs_regional_storm_following_1nest_atm_ocn_wav_inline_intel' [10:08, 08:56](714 MB)

PASS -- COMPILE 'hafs_mom6w_intel' [12:11, 11:54] (1 warnings 7 remarks)

PASS -- TEST 'hafs_regional_storm_following_1nest_atm_ocn_wav_mom6_intel' [08:06, 06:37](729 MB)

PASS -- COMPILE 'hafs_all_intel' [12:11, 11:22] (1 warnings 7 remarks)

PASS -- TEST 'hafs_regional_docn_intel' [08:20, 06:38](837 MB)

PASS -- TEST 'hafs_regional_docn_oisst_intel' [08:19, 06:38](822 MB)

PASS -- TEST 'hafs_regional_datm_cdeps_intel' [17:57, 16:21](1216 MB)

PASS -- COMPILE 'datm_cdeps_intel' [07:11, 06:07]

PASS -- TEST 'datm_cdeps_control_cfsr_intel' [03:20, 02:44](1147 MB)

PASS -- TEST 'datm_cdeps_restart_cfsr_intel' [02:21, 01:36](1122 MB)

PASS -- TEST 'datm_cdeps_control_gefs_intel' [03:19, 02:35](1023 MB)

PASS -- TEST 'datm_cdeps_iau_gefs_intel' [03:20, 02:37](1020 MB)

PASS -- TEST 'datm_cdeps_stochy_gefs_intel' [03:18, 02:39](1020 MB)

PASS -- TEST 'datm_cdeps_ciceC_cfsr_intel' [03:20, 02:41](1153 MB)

PASS -- TEST 'datm_cdeps_bulk_cfsr_intel' [03:19, 02:39](1152 MB)

PASS -- TEST 'datm_cdeps_bulk_gefs_intel' [03:19, 02:35](1015 MB)

PASS -- TEST 'datm_cdeps_mx025_cfsr_intel' [08:32, 06:08](1057 MB)

PASS -- TEST 'datm_cdeps_mx025_gefs_intel' [08:29, 06:17](1050 MB)

PASS -- TEST 'datm_cdeps_multiple_files_cfsr_intel' [03:16, 02:41](1168 MB)

PASS -- TEST 'datm_cdeps_3072x1536_cfsr_intel' [04:23, 04:00](2481 MB)

PASS -- TEST 'datm_cdeps_gfs_intel' [04:22, 03:57](2488 MB)

PASS -- COMPILE 'datm_cdeps_debug_intel' [04:10, 03:17] (2 warnings)

PASS -- TEST 'datm_cdeps_debug_cfsr_intel' [07:22, 06:22](1079 MB)

PASS -- COMPILE 'datm_cdeps_faster_intel' [07:11, 06:16]

PASS -- TEST 'datm_cdeps_control_cfsr_faster_intel' [03:20, 02:41](1146 MB)

PASS -- COMPILE 'datm_cdeps_land_intel' [02:10, 01:01] (1 remarks)

PASS -- TEST 'datm_cdeps_lnd_gswp3_intel' [01:31, 00:48](258 MB)

PASS -- TEST 'datm_cdeps_lnd_era5_intel' [02:26, 00:51](330 MB)

PASS -- TEST 'datm_cdeps_lnd_era5_rst_intel' [01:29, 00:33](318 MB)

PASS -- COMPILE 'atml_intel' [12:11, 11:42] (9 warnings 2 remarks)

PASS -- TEST 'control_p8_atmlnd_sbs_intel' [06:15, 04:17](1619 MB)

PASS -- TEST 'control_p8_atmlnd_intel' [06:07, 04:17](1609 MB)

PASS -- TEST 'control_restart_p8_atmlnd_intel' [03:52, 02:20](900 MB)

PASS -- COMPILE 'atml_debug_intel' [05:11, 04:28] (881 warnings 2 remarks)

PASS -- TEST 'control_p8_atmlnd_debug_intel' [07:00, 05:32](1637 MB)

PASS -- COMPILE 'atmw_intel' [12:11, 11:08] (1 warnings 8 remarks)

PASS -- TEST 'atmwav_control_noaero_p8_intel' [02:57, 01:50](1652 MB)

PASS -- COMPILE 'atmaero_intel' [11:11, 10:37] (1 warnings 1 remarks)

PASS -- TEST 'atmaero_control_p8_intel' [05:07, 04:04](3043 MB)

PASS -- TEST 'atmaero_control_p8_rad_intel' [06:02, 04:59](3089 MB)

PASS -- TEST 'atmaero_control_p8_rad_micro_intel' [05:43, 04:56](3118 MB)

PASS -- COMPILE 'atmaq_debug_intel' [04:11, 03:25] (885 warnings 6 remarks)

PASS -- TEST 'regional_atmaq_debug_intel' [31:43, 28:54](4462 MB)

PASS -- COMPILE 'atm_gnu' [06:11, 03:39]

PASS -- TEST 'control_c48_gnu' [12:47, 11:15](1545 MB)

PASS -- TEST 'control_stochy_gnu' [04:29, 03:21](510 MB)

PASS -- TEST 'control_ras_gnu' [05:23, 04:49](514 MB)

PASS -- TEST 'control_p8_gnu' [06:02, 04:48](1273 MB)

PASS -- TEST 'control_p8_ugwpv1_gnu' [06:00, 04:34](1276 MB)

PASS -- TEST 'control_flake_gnu' [11:24, 10:43](553 MB)

PASS -- COMPILE 'rrfs_gnu' [06:12, 03:43]

PASS -- TEST 'rap_control_gnu' [11:35, 10:45](857 MB)

PASS -- TEST 'rap_decomp_gnu' [11:34, 11:03](863 MB)

PASS -- TEST 'rap_2threads_gnu' [10:39, 09:38](941 MB)

PASS -- TEST 'rap_restart_gnu' [06:48, 05:36](589 MB)

PASS -- TEST 'rap_sfcdiff_gnu' [11:41, 10:45](863 MB)

PASS -- TEST 'rap_sfcdiff_decomp_gnu' [11:38, 10:52](862 MB)

PASS -- TEST 'rap_sfcdiff_restart_gnu' [09:51, 07:58](592 MB)

PASS -- TEST 'hrrr_control_gnu' [06:39, 05:33](857 MB)

PASS -- TEST 'hrrr_control_noqr_gnu' [06:35, 05:31](846 MB)

PASS -- TEST 'hrrr_control_2threads_gnu' [07:34, 05:00](930 MB)

PASS -- TEST 'hrrr_control_decomp_gnu' [07:35, 05:37](859 MB)

PASS -- TEST 'hrrr_control_restart_gnu' [04:30, 02:50](575 MB)

PASS -- TEST 'hrrr_control_restart_noqr_gnu' [04:29, 02:48](667 MB)

PASS -- TEST 'rrfs_v1beta_gnu' [11:54, 10:21](857 MB)

PASS -- COMPILE 'atm_dyn32_debug_gnu' [06:11, 03:44]

PASS -- TEST 'control_diag_debug_gnu' [02:55, 01:38](1282 MB)

PASS -- TEST 'regional_debug_gnu' [12:46, 11:09](563 MB)

PASS -- TEST 'rap_control_debug_gnu' [03:25, 02:38](872 MB)

PASS -- TEST 'hrrr_control_debug_gnu' [03:24, 02:34](866 MB)

PASS -- TEST 'hrrr_gf_debug_gnu' [03:24, 02:32](875 MB)

PASS -- TEST 'hrrr_c3_debug_gnu' [03:22, 02:40](872 MB)

PASS -- TEST 'rap_diag_debug_gnu' [04:37, 02:53](954 MB)

PASS -- TEST 'rap_noah_sfcdiff_cires_ugwp_debug_gnu' [05:23, 04:10](871 MB)

PASS -- TEST 'rap_progcld_thompson_debug_gnu' [03:22, 02:40](869 MB)

PASS -- TEST 'rrfs_v1beta_debug_gnu' [03:22, 02:35](866 MB)

PASS -- TEST 'control_ras_debug_gnu' [02:22, 01:34](500 MB)

PASS -- TEST 'control_stochy_debug_gnu' [02:22, 01:47](495 MB)

PASS -- TEST 'control_debug_p8_gnu' [02:45, 01:40](1261 MB)

PASS -- TEST 'rap_flake_debug_gnu' [03:23, 02:39](867 MB)

PASS -- TEST 'rap_clm_lake_debug_gnu' [03:23, 02:55](872 MB)

PASS -- TEST 'gnv1_c96_no_nest_debug_gnu' [05:36, 04:23](873 MB)

PASS -- COMPILE 'wam_debug_gnu' [04:11, 01:52]

PASS -- COMPILE 'rrfs_dyn32_phy32_gnu' [06:11, 03:39]

PASS -- TEST 'rap_control_dyn32_phy32_gnu' [10:39, 09:17](717 MB)

PASS -- TEST 'hrrr_control_dyn32_phy32_gnu' [07:31, 05:01](717 MB)

PASS -- TEST 'rap_2threads_dyn32_phy32_gnu' [10:34, 08:36](763 MB)

PASS -- TEST 'hrrr_control_2threads_dyn32_phy32_gnu' [06:38, 04:32](753 MB)

PASS -- TEST 'hrrr_control_decomp_dyn32_phy32_gnu' [06:35, 05:01](714 MB)

PASS -- TEST 'rap_restart_dyn32_phy32_gnu' [08:57, 08:00](559 MB)

PASS -- TEST 'hrrr_control_restart_dyn32_phy32_gnu' [03:43, 02:39](546 MB)

PASS -- TEST 'conus13km_control_gnu' [04:56, 03:09](887 MB)

PASS -- TEST 'conus13km_2threads_gnu' [06:50, 05:34](886 MB)

PASS -- TEST 'conus13km_restart_mismatch_gnu' [02:48, 01:46](567 MB)

PASS -- COMPILE 'atm_dyn64_phy32_gnu' [07:11, 05:23]

PASS -- TEST 'rap_control_dyn64_phy32_gnu' [07:48, 05:50](738 MB)

PASS -- COMPILE 'atm_dyn32_phy32_debug_gnu' [06:13, 03:54]

PASS -- TEST 'rap_control_debug_dyn32_phy32_gnu' [04:23, 02:32](723 MB)

PASS -- TEST 'hrrr_control_debug_dyn32_phy32_gnu' [04:22, 02:31](723 MB)

PASS -- TEST 'conus13km_debug_gnu' [08:46, 07:04](909 MB)

PASS -- TEST 'conus13km_debug_qr_gnu' [07:46, 06:50](591 MB)

PASS -- TEST 'conus13km_debug_2threads_gnu' [08:43, 07:24](907 MB)

PASS -- TEST 'conus13km_radar_tten_debug_gnu' [07:47, 06:56](971 MB)

PASS -- COMPILE 'atm_dyn64_phy32_debug_gnu' [04:11, 03:43]

PASS -- TEST 'rap_control_dyn64_phy32_debug_gnu' [03:27, 02:36](750 MB)

PASS -- COMPILE 's2swa_gnu' [15:13, 14:37]

PASS -- COMPILE 's2s_gnu' [15:13, 14:20]

PASS -- TEST 'cpld_control_nowave_noaero_p8_gnu' [08:15, 06:41](1357 MB)

PASS -- COMPILE 's2swa_debug_gnu' [03:12, 02:33]

PASS -- COMPILE 's2sw_pdlib_gnu' [15:12, 14:25]

PASS -- TEST 'cpld_control_pdlib_p8_gnu' [25:10, 24:08](1326 MB)

PASS -- COMPILE 's2sw_pdlib_debug_gnu' [03:12, 02:24]

PASS -- TEST 'cpld_debug_pdlib_p8_gnu' [18:58, 17:17](1327 MB)

PASS -- COMPILE 'datm_cdeps_gnu' [14:13, 14:04]

PASS -- TEST 'datm_cdeps_control_cfsr_gnu' [03:20, 03:04](703 MB)

SYNOPSIS:

Starting Date/Time: 20240509 13:07:20

Ending Date/Time: 20240509 14:44:04

Total Time: 01h:36m:58s

Compiles Completed: 54/54

Tests Completed: 242/242

NOTES:

A file 'test_changes.list' was generated but is empty.

If you are using this log as a pull request verification, please commit 'test_changes.list'.

Result: SUCCESS

====END OF HERA REGRESSION TESTING LOG====